University of California at Berkeley
College of Engineering
Department of Electrical Engineering and Computer Science

EECS150, Spring 2010

Homework Assignment 5: MIPS Microarchitecture
Due February 24th, 2pm

1. DDCA 7.2

2. DDCA 7.4

3. DDCA 7.10 using a, b, and c from Exercise 7.3

4. DDCA 7.24

5. DDCA 7.26

6. DDCA 7.28

7. DDCA 7.30

8. DDCA 7.32