

Demand Paging

1. **True or False:** Adding more RAM reduces the number of page faults that occur in a system.
2. **True or False:** Compulsory misses in a cache can be reduced with prefetching.
3. **True or False:** The Clock Algorithm requires hardware support for a “use” bit in the PTE.
4. **True or False:** The Clock Algorithm swaps the oldest page to disk on a page fault.
5. **True or False:** LRU swaps the oldest to disk on a page fault (age is defined by amount of time in RAM).
6. **True or False:** One advantage of software TLB is that the same hardware platform can support both forward and inverse page tables.

For each policy:

List a disadvantage.

FIFO Policy:

MIN Policy:

LRU Policy:

Clock Policy:

[illegible]