EE42/100   Spring 2006   Problem Set 10

Please remember that homeworks are due at 12:00 noon Tuesday Apr. 4.
Please put your homework in the appropriate box (EE42 or EE100) in 240 Cory Hall.
Print your name(s) in upper right corner of your paper and indicate whether you're
enrolled in EE42 or EE100.

1. *(Reading Assignment)* Sec. 12.7 CMOS Logic Circuits, Hambley 3rd edition.

2. *Hambley 3rd Ed. P 12.4*

3. *Hambley 3rd Ed P. 12.9.*

4. *Hambley 3rd Ed, P. 12.16.*

5. *Hambley 3rd Ed P. 12.18*

6. *Hambley 3rd Ed P. 12.20*