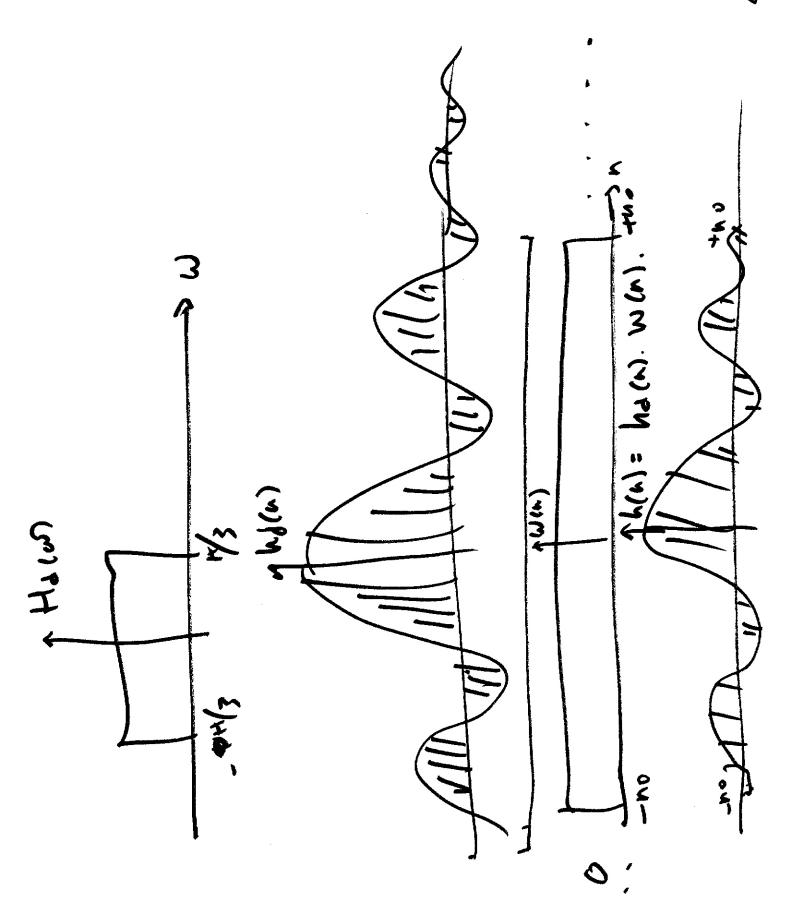
to tol 艺 LIR Simplem Noing Filter Design California (2) (2) Transcript(2) (2) (2) (2) (2) (2) Deternity of the Coe 445 of the Delay THE BEST OF STATE OF 0ct 26.05

, program 05P chip. Programble ASIC + X; (ix FP&A. -> mathel pray. Cooleran on Oc. Directfor 1,2, Cascod, ASIC building C. 16.3 Determine 6 3 ACC 2 Reali retur Speit wh Design

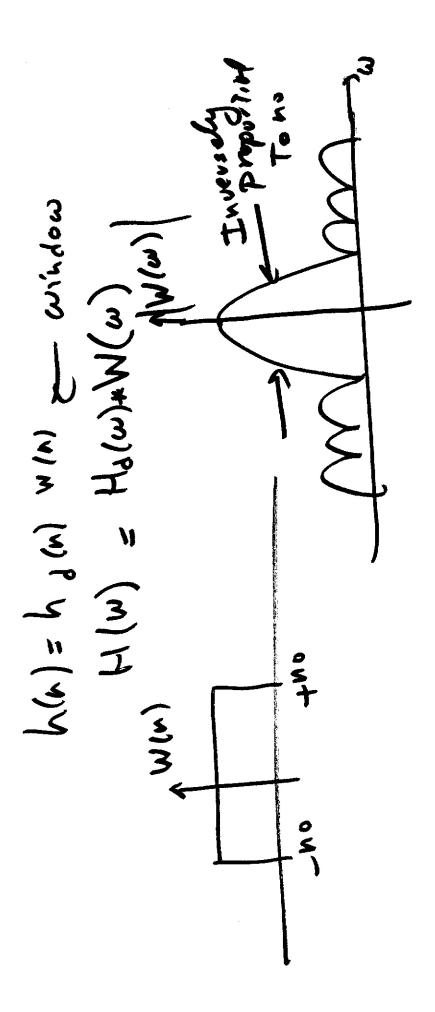
fixite length window 1) stad with desired fing Response Holews - Docined impulse (3) Compute IDTF 7 } Halen 3: halen) = 80 Timor. (x) = hace) N(x) をサア こまれ



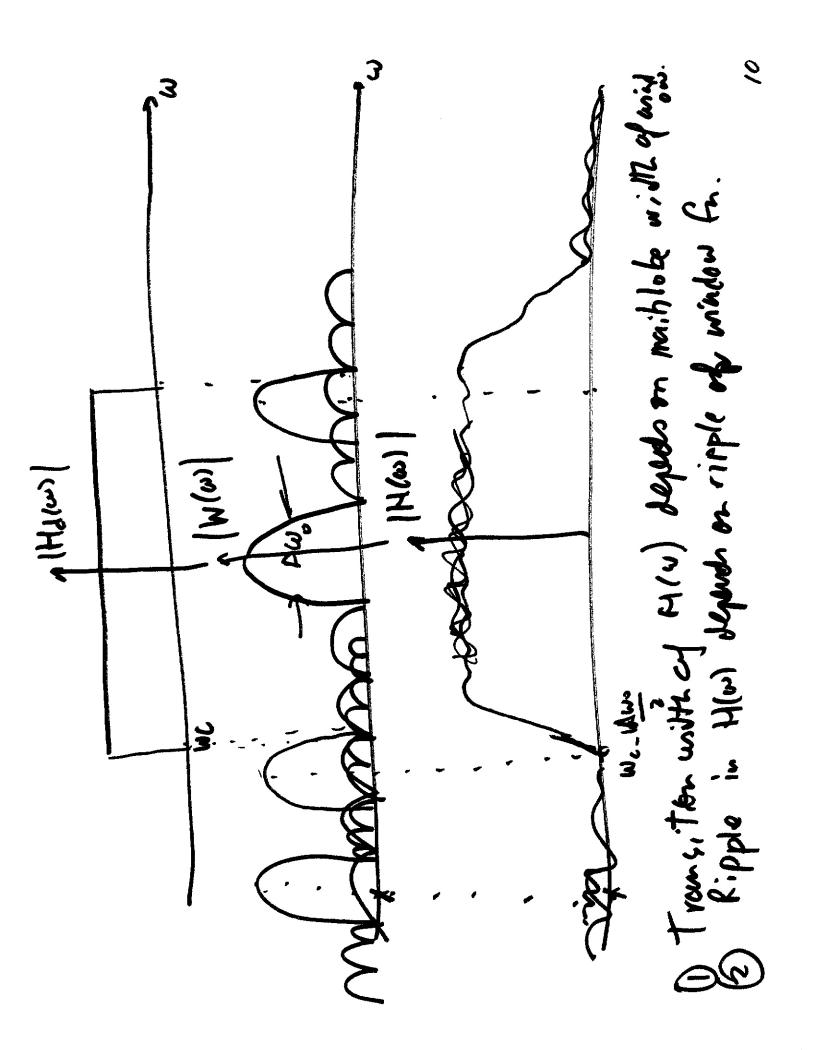
Assure linear Phase Fitter. (desimalideal, Ring FIR) ideal high pan J. H. (W) . H. (W) . H two Es High Pan Filter # stages (2) Compte #.D.T.FT. {Hum}} 0= Assur Type F.

(we (h-a) -jalu jun du Multiply hale) by a finite longh winder to get FIR filter F-Wc 人び CM+Wc #\_we < w < TI o tremis. esthewise. は (トーな) な (-1)n-K I. by FH&m3= 2 - jaw ト。 (3) H S=|(=)(H)

JOC. Under ag H(W) in Zha) -0.1 -0.3 -0.5 -0.5 7 4(4) 20.1 一下。一个人 = 0 = m[(m) 1) [H(W)] A h(a) , ho(a). W(r) -6.3



passbud. stop hand. A W = W = - WP = Transition Weiriam Wath dのプロプロ Terminagn Sp= passhadd ripple Sse stopbad ripple. Possban WP (3) H) C



Domain => small transition with Central Transition width R ر (ع) » ا M+1(4) i.e. Main (06. of) [W(4)] と~+ Sp small 65 small Youth dosign Withow w(h) main loble m namower orger Windows

highest main the -> snalled mainle be How to design wan to get good side (obe belavier for wan) i.e., a good ripple behavier for my Fixed size (doration) window.

Fixed shape hove differed Gind FIR Gitter. main lote midth suge of Window. 50 9 22.E Restople +3 2 بر برا

But size of Win) (dovation)
denot significantly that
side but betweening Guye of was control six lose Lowular.

Ange Sandre

mainlobe aful) 5)40 2150

1) Use stype to control sidelobe size to contral main lobe behaviour Strategy