

EE16B Fall 2017
 Lab Schedule

Week of	Lab Title	Description
8/21/17	No lab	
8/28/17	Introduction to Debugging	Review of lab equipment and lab safety. Learn debugging techniques, tips, and tricks. Applications: skills needed for the rest of lab.
9/4/17	Digital to Analog Converter	Build a simple D/A converter. Learn how to go from digital to analog signals. Applications: MP3 players, convert digital video for TVs
9/11/17	Analog to Digital Converter	Build a simple A/D converter Learn how to go from analog to digital signals. Applications: Music recording, signal processing.
9/18/17	Mystery Circuit Assembly of Mic Boards	Reverse-engineer a mystery circuit. Learn how to determine important characteristics of filters and amplifiers using your o-scope. Begin assembly of mic PCBs for use in Color Organ and Project Applications: Skills for project, frequency response analysis.
9/25/17	Color Organ: Part I	Build a circuit that is fed music and illuminates LEDs based on frequency content.
10/2/17	Color Organ: Part II	Learn how to apply filters and amplifiers Applications: Signal processing
Project: SIXT33N		
10/9/17	Mic Circuit: Part I	Build a microphone circuit that will be used to communicate with SIXT33N, including the microphone, filters, and amplifiers.
10/16/17	System Identification	Verify functionality of sensors and motors, and determine parameters of model for car.
10/23/17	Introduction to Controls: Part I	Build a car, collect open-loop data, and implement a closed-loop control scheme to make the car go straight. Learn how eigenvectors can affect the controllability of a system.
10/30/17	Introduction to Controls: Part II	
11/6/17	SVD / PCA	Implement PCA Algorithm to verbally control SIXT33N. Learn how to use PCA to distinguish between words or musical genre.
11/13/17	Thanksgiving: no lab	
11/20/17	Advanced Controls	Add to the closed-loop control scheme to make SIXT33N turn.
11/27/17	Integration	Refine the way that the different systems of SIXT33N will interact with each other. Finish the final SIXT33N robot.
12/4/17	RRR Week	
12/11/17	Finals Week	

Guide to Lab

Purpose:

Lab provides an opportunity to apply the material learned in lecture. Building circuits with real components helps build intuition and reinforce concepts, which in turn will help with lecture and exams. Lab requires critical thinking and problem solving skills that are useful in all areas of this course. Additionally, lab requires students to work in groups, fostering teamwork and collaboration.

Lab Expectations:

- Attendance is **mandatory**. You must attend the lab section that you are enrolled in.
- Lab will begin with a short introductory presentation by the lab GSI, which will contain important lab information. It is expected that students will arrive on time and refrain from distractions (phones, computers, etc.).
- Treat the lab equipment with respect – any damaged or missing equipment will negatively impact the ability of the course as a whole to complete lab assignments in a timely fashion.
- Clean up after yourself: put away equipment (leads, extra wires, parts, etc.) in the correct locations. Failure to clean up workstations will result in a grade penalty.
- No food or drink at the lab workstations!
- Be patient – there are other students who also need help. Ask your neighbors for help too.

Grading:

Lab grades account for a total of 40% of final grades. Within lab, the breakdown is as follows:

Labs (6 total)	40%
Project	60%

Lab check offs:

Each lab will be graded on a completion. Labs must be checked off by a Lab GSI within **1 week** of assignment late labs will not be accepted. If a conflict occurs (ie: illness, interview, etc.) it is the responsibility of the student to communicate with his/her lab GSI and make necessary arrangements to complete the lab. Check offs can be viewed on Gradescope.

Project Grading:

In order to encourage spreading out project work throughout the semester, each week will have checkpoints that must be met in order to receive credit for that lab. Checkpoints must be checked off within **1 week** of assignment to receive credit. The project breakdown is as follows:

Checkpoints (6 total)	50%
Integration/Final Demo	25%
Final Report	25%