

EECS 42 Introduction to Electronics

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Lecture #2

- **Announcements**
 - **HKN Tutoring**
 - **Discussion 103 Th 3 likely on**
 - **HW #1 $50x^2$; plot 0 to 0.010**
 - **Use newsgroup on web page**
 - **EE43 Read Lab; do Prelab; no Tu**
 - **Charge, Current, Energy, Voltage**
 - **Power**
- <http://inst.EECS.Berkeley.EDU/~ee42/>

Game Plan 01/22/03

Today 01/27/03

Discussion Sections:

- Electrical Quantities**
Schwarz and Oldham: 1.3-1.4

Wednesday 01/29/03

- Kirchhoff Laws**
Schwarz and Oldham: 2.1-2.2

Problem Set #1 - Out 1/22/03 - Due 1/29/03 2:30 in box near 275 Cory

Practice Skills needed for Electronics without Electronics

1.1 Flow; 1.2 Potential; 1.3 Truth Table; 1.4 Graphs

Problem Set #2 – Out 1/27/03 - Due 2/5/03 2:30 in box near 275 Cory

2.1 Flow; 2.2 KCL; 2.3 KVL; 2.4 Resistor circuit and I vs. V; 2.5 Power

REVIEW OF ELECTRICAL QUANTITIES AND BASIC CIRCUIT ELEMENTS

Free Charge

Most matter is macroscopically electrically neutral most of the time.
 Exceptions: clouds in thunderstorm, people on carpets in dry weather, plates of a charged capacitor, etc.

Microscopically, of course, matter is full of charges. Consider solids:

- Solids in which all charges are bound to atoms are called *insulators*.
- Solids in which outer-most atomic electrons are free to move around are called *metals*.
 - Metals typically have ~ 1 “free electron” per atom ($\sim 5 \times 10^{22}/\text{cm}^3$)
 - Charge on a free electron is $-e$ or $-q$, where $|e| = 1.6 \times 10^{-19} \text{ C}$ ← **C stands for the units of charge called Coulomb**
- *Semiconductors* are insulators in which electrons are not tightly bound and thus can be easily “promoted” to a free state (by heat or even by “doping” with a foreign atom).

Al or Cu – good metallic conductor – great for wires

Si or GaAs – classic semiconductors

Quartz – good insulator – great for dielectric

CHARGE (cont.)

Charge flow \Rightarrow Current

Charge storage \Rightarrow Energy, information

Definition of current i (or I)

i (in Amperes) = flow of 1 coulomb per second

$$i (\text{A}) = \frac{dq}{dt} \left(\frac{\text{C}}{\text{S}} \right) \quad \text{where } q \text{ is the charge in coulomb and } t \text{ is the time in sec}$$

Note: Current has **sign**

Examples:

- (a) 10^5 positive unit charges of value $e = 1.6 \times 10^{-19} \text{ C}$
 flow to right (+ x direction) every nanosecond

$$i = \frac{10^5 \times 1.6 \times 10^{-19}}{10^{-9}} = 1.6 \times 10^{-5} \text{ A} = 16 \mu\text{A} \text{ (left to right)}$$

- (b) 10^{10} electrons flow to right in a wire every microsecond

$$i = \frac{-10^{10} \times 1.6 \times 10^{-16}}{10^{-6}} = -1.6 \times 10^{-3} = -1.6 \text{ mA (left to right)}$$

Units and multipliers

Version Date 01/30/03

We use metric ("SI") units in electrical engineering.

The important ones are:

Energy -	E	Joules	(J)	
Power -	P	Watts	(W)	
Charge -	Q	Coulomb	(C)	
Current -	I	Ampere	(A)	
Potential -	V	Volt	(V)	
Resistance -	R	Ohm	(Ω)	(V/I)
Capacitance -	C	Farad	(F)	(C V)
Inductance -	L	Henry	(H)	(Vsec/A)

PREFIX

femto	f	10^{-15}
pico	p	10^{-12}
nano	n	10^{-9}
micro	m	10^{-6}
milli	m	10^{-3}
kilo	k	10^3
mega	M	10^6
giga	G	10^9

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POSSIBLE CONCEPTUAL ISSUES

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- 1 How does charge move through the wire?

Remember a wire has a huge number of free carriers moving very fast but randomly (because of thermal energy)

$$\langle v \rangle \sim C/1000 \text{ at } 20^\circ\text{C}$$

Drift concept: Now add even a modest electric field

Carriers "feel" an electric field along the wire and tend to drift with it (+ sign charge) or against it (- charge carrier). This drift is still small compared to the random motion.

- 2 Sign of the charge carriers: It is often negative (for metals); in silicon, it can be either negative or positive.

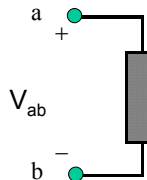
Ben Franklin: Picked/guessed that carriers in wires have a positive sign and move with the electric field. In fact electrons have a negative charge and go the other way (in a positive field). But of course the current is the same for either + or - Q since they move in opposite directions in a given field.

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THINKING ABOUT VOLTAGE (“electrical potential”)

Definition: Voltage (electrical potential) is the electrical energy per unit positive charge.

The Units are Volts = Joules/Coulomb



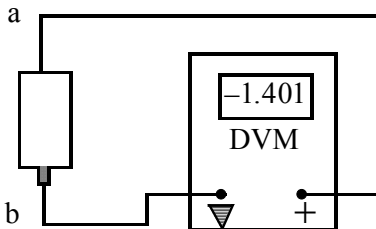
“ V_{ab} ” means the potential at a minus the potential at b.

Generalized circuit element with two terminals (wires) a and b, with a potential difference V_{ab} across the element

Potential is always referenced to some point (V_{ab} in the example; V_a is measured with respect to V_b)

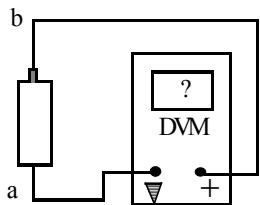
SIGN CONVENTIONS

Suppose you have an unlabelled battery and you measure its voltage with a digital voltmeter. It will tell you magnitude **and sign** of the voltage.



With this circuit, you are measuring V_a with respect to V_b (or $V_a - V_b$). DVM indicates -1.4 , so $V_a < V_b$ by 1.4 V. Which is the positive battery terminal?

☞ Answer: terminal b ($V_b > V_a$)

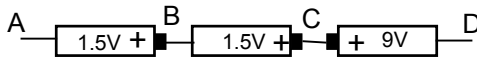


Now you make a change. What would this circuit measure?

☞ Answer: $+1.401$ V
Note that we have used “ground” symbol (∇) for reference node on DVM. Often it is labeled “C” or “common.”

SIGN CONVENTIONS (cont.)

Lets put a bunch of batteries, say 1.5V and 9V in series to see what we already know about sign conventions:

Example 1

$$V_{AB} = -1.5V$$

$$V_{BC} = -1.5V$$

$$V_{CD} = +9V$$

What is V_{AD} ?

Math Approach: Factor into known steps

$$V_{AD} = V_{AB} + V_{BC} + V_{CD} = (-1.5) + (-1.5V) + 9V = 6V$$

Physical Approach: Add the voltage (potential) drops

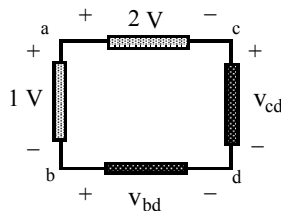
$$V_{AD} = (-1.5) + (-1.5V) + 9V = 6V$$

KEEPING THE VOLTAGE SIGNS STRAIGHT

Labeling Conventions

- Indicate + and – terminals clearly; or label terminals with letters
- The + sign corresponds to the first subscript; the – sign to the second subscript. Therefore, $V_{ab} = -V_{ba}$

Note: The labeling convention has nothing to do with whether or not $v > 0$ or $v < 0$



Using sign conventions:

$$V_{ab} = 1; V_{ca} = -2, \text{ thus}$$

$$V_{cb} = -2 + 1 = -1V$$

Obviously $V_{cd} + V_{db} = V_{cb}$ too. Then if $V_{bd} = 5V$, what is V_{cd} ?

$$\text{Answer: } V_{cd} - 5 = -1$$

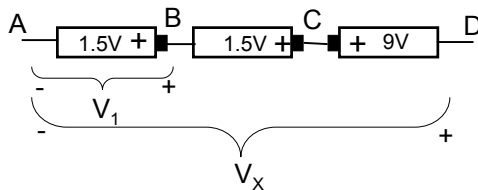
$$\text{so } V_{cd} = 4V$$

SIGN CONVENTIONS (cont.)

Normally we do not need two subscripts for voltages because:

- 1) We have defined a point in the circuit to the reference node (common or "ground"), i.e. the place where we will attach the common wire of the voltmeter. Thus all voltages are measured with respect to this point. OR
- 2) We use "brackets" with signs to indicate the polarity and symbol:

Example How are single-subscript voltages related to double-subscript voltages?



Answer: Clearly

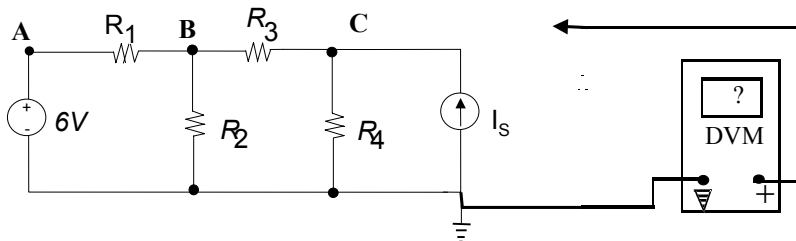
$$V_1 = 1.5 = -V_{AB} = V_{BA}$$

$$V_x = -6 = V_{DA}$$

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EXAMPLE OF SINGLE-SUBSCRIPT NODE VOLTAGES

Choose a reference (ground) and define the circuit voltages with respect to this point. This is equivalent to attaching common node of voltmeter to the reference node.



Thus we can connect the + lead of the DVM to point (Node) A and call the potential of point A " V_A ", similarly at points (Nodes) B, C.

Of course what V_A means is the potential of point (Node) A with respect the point connected to the common lead of the DVM. What is V_A ?

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POWER IN ELECTRIC CIRCUITS

Power: Transfer of energy per unit time (Joules per second = Watts)

Concept: in falling through a positive potential drop V , a positive charge q gains energy

- potential energy change = qV for each charge q
- Rate is given by # charges/sec

$$\text{Power} = P = V (dq/dt) = VI$$

$$P = V \times I \quad \text{Volt} \times \text{Amps} = \text{Volts} \times \text{Coulombs/sec} = \text{Joules/sec} = \text{Watts}$$

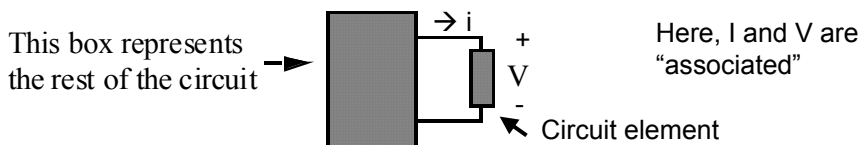
Circuit elements can *absorb* or *release* power (i.e., from or to the rest of the circuit); power can be a function of time.

How to keep the signs straight for absorbing and releasing power?

- + Power \equiv absorbed into element
- Power \equiv delivered from element

"ASSOCIATED REFERENCE DIRECTIONS"

It is often convenient to define the current *through* a circuit element as positive when entering the terminal associated with the + reference for voltage



For positive current and positive voltage, positive charge "falls down" a potential "drop" in moving through the circuit element: it *absorbs* power

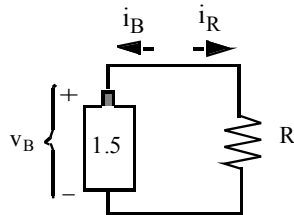
- $P = VI > 0$ corresponds to the element absorbing power if the definitions of I and V are associated.

How can a circuit element absorb power?

By converting electrical energy into heat (resistors in toasters); light (light bulbs); acoustic energy (speakers); by storing energy (charging a battery).

Negative power \Rightarrow releasing power to rest of circuit

“ASSOCIATED REFERENCE DIRECTIONS” (cont.)



Suppose $i_R = 1\text{mA}$

therefore $i_B = -1\text{mA}$

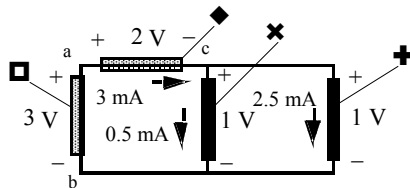
A) Resistor: $P = i_B v_B = +1.5\text{mW}$

B) Battery: i_B and v_B are associated, therefore $P = i_B v_B$.
Thus

$$P = 1.5 \times (-1 \times 10^{-3}) = -1.5\text{ mW}$$

EXAMPLES OF CALCULATING POWER

Find the power absorbed by each element



Element \square : flip current direction: $3\text{V}(-3\text{mA}) = -9\text{ mW}$

Element \blacklozenge : $= 2\text{V}(3\text{mA}) = 6\text{ mW}$

Element \times : $= 1\text{V}(0.5\text{mA}) = 0.5\text{ mW}$

Element $+$: $= 1\text{V}(2.5\text{mA}) = 2.5\text{ mW}$