

The Beauty and Joy of Computing

Lecture #8 Concurrency

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Lecturer
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2013Fa UC Berkeley CS10 Quest Histogram
Mean = 16.7, Median = 17.5, StDev = 2.0

Score	Students
0.0	0
0.5	0
1.0	0
1.5	0
2.0	0
2.5	0
3.0	0
3.5	0
4.0	0
4.5	0
5.0	0
5.5	0
6.0	0
6.5	0
7.0	1
7.5	1
8.0	1
8.5	1
9.0	1
9.5	1
10.0	1
10.5	1
11.0	1
11.5	1
12.0	1
12.5	1
13.0	1
13.5	1
14.0	1
14.5	1
15.0	1
15.5	1
16.0	1
16.5	1
17.0	1
17.5	4
18.0	7
18.5	8
19.0	8
19.5	17
20.0	18
20.5	21
21.0	35
21.5	40
22.0	40
22.5	39
23.0	29
23.5	14
24.0	4
24.5	0
25.0	0

Concurrency: A Definition

Concurrency: A property of computer systems in which several **computations** are **executing** simultaneously, and potentially interacting with each other.

Concurrency is Everywhere!

Examples:

- Mouse cursor movement while SNAP! calculates.
- Screen clock advances while typing in a text.
- Busy cursor spins while browser connects to server, waiting for response

Question: Is this real concurrency?

Concurrency & Parallelism

<p>Intra-computer</p> <ul style="list-style-type: none"> Today's lecture Multiple computing "helpers" are cores <u>within one machine</u> Aka "multi-core" <ul style="list-style-type: none"> Although GPU parallelism is also "intra-computer" 	<p>Inter-computer</p> <ul style="list-style-type: none"> Future lecture Multiple computing "helpers" are <u>different machines</u> Aka "distributed computing" <ul style="list-style-type: none"> Grid & cluster computing
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Anatomy: 5 components of any Computer

John von Neumann invented this architecture

a) Control
b) Datapath
c) Memory
d) Input
e) Output

What causes the most headaches for SW and HW designers with multi-core computing?

But what is INSIDE a Processor?

But what is INSIDE a Processor?

- Primarily Crystalline Silicon
- 1 mm – 25 mm on a side
- 2009 “feature size” (aka process) ~ 45 nm = 45×10^{-9} m (then 32, 22, and 16 [by yr 2013])
- 100 - 1000M transistors
- 3 - 10 conductive layers
- “CMOS” (complementary metal oxide semiconductor) - most common
- Package provides:
 - spreading of chip-level signal paths to board-level
 - heat dissipation.
 - Ceramic or plastic with gold wires.

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en.wikipedia.org/wiki/Moore's_Law

Moore's Law

Predicts: 2X Transistors / chip every 2 years

What is this “curve”?

- Constant
- Linear
- Quadratic
- Cubic
- Exponential

Gordon Moore
Intel Co-founder
B.S. Cal 1950!

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Moore's Law and related curves

Data partially collected by M. Horowitz, F. Labonte, O. Shacham, K. Oukutun, L. Hammond

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Moore's Law and related curves

Data partially collected by M. Horowitz, F. Labonte, O. Shacham, K. Oukutun, L. Hammond

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Power Density Prediction circa 2000

Source: S. Borkar (Intel)

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Background: Threads

- A **Thread** stands for “thread of execution”, is a single stream of instructions
 - A program / process can **split**, or **fork** itself into separate threads, which can (in theory) execute simultaneously.
 - An easy way to describe/think about parallelism
- A single CPU can execute many threads by **Time Division Multiplexing**
- **Multithreading** is running multiple threads through the same hardware

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en.wikipedia.org/wiki/Amdahl's_law

Speedup Issues : Amdahl's Law

- Applications can almost **never** be completely parallelized; some serial code remains

- s is serial fraction of program, P is # of cores (was processors)
- Amdahl's law:**

$$\text{Speedup}(P) = \text{Time}(1) / \text{Time}(P)$$

$$\leq 1 / (s + [(1-s) / P]), \text{ and as } P \rightarrow \infty$$

$$\leq 1 / s$$
- Even if the parallel portion of your application speeds up perfectly, your performance may be limited by the sequential portion

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en.wikipedia.org/wiki/Amdahl's_law

Speedup Issues : Overhead

- Even assuming no sequential portion, there's...
 - Time to think how to **divide the problem up**
 - Time to **hand out** small "work units" to workers
 - All workers may **not work equally fast**
 - Some **workers may fail**
 - There may be **contention for shared resources**
 - Workers could **overwriting each others' answers**
 - You may have to **wait until the last worker returns** to proceed (the slowest / weakest link problem)
 - There's **time to put the data back together** in a way that looks as if it were done by one

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en.wikipedia.org/wiki/Amdahl's_law

Life in a multi-core world...

- This "sea change" to multi-core parallelism means that the computing community has to rethink:
 - Languages
 - Architectures
 - Algorithms
 - Data Structures
 - All of the above

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en.wikipedia.org/wiki/Concurrent_computing

But parallel programming is hard!

- What if two people were calling **withdraw** at the same time?
 - E.g., balance=100 and two withdraw 75 each
 - Can anyone see what the problem *could* be?
 - This is a **race condition**
- In most languages, this is a problem.
 - In Scratch, the system doesn't let two of these run at once.

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en.wikipedia.org/wiki/Deadlock

Another concurrency problem ... **deadlock!**

- Two people need to draw a graph but there is only one pencil and one ruler.
 - One grabs the pencil
 - One grabs the ruler
 - Neither release what they hold, waiting for the other to release
- Livelock** also possible
 - Movement, no progress

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en.wikipedia.org/wiki/Concurrent_computing

Summary

- "Sea change" of computing because of inability to cool CPUs means we're now in multi-core world
- This brave new world offers lots of potential for innovation by computing professionals, but challenges persist

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