

SORTED BY USE

Pin	name	function	USE
77	_GIO_B	USER_Define	A0
84	_GIO_B	USER_Define	A1
5	_GIO_A	USER_Define	A2
78	_GIO_B	USER_Define	A3
81	_GIO_B	USER_Define	A4
80	_GIO_B	USER_Define	A5
83	_GIO_B	USER_Define	A6
82	_GIO_B	USER_Define	A7
10	_GIO_A	USER_Define	A8
9	_GIO_A	USER_Define	A9
6	_GIO_A	USER_Define	A10
7	_GIO_A	USER_Define	A11
67	_GIO	USER_Define	A12
14	_GIO	USER_Define	A13
18	_GIO	USER_Define	A14
79	_GIO_B	USER_Define	A15
37	LED_CR3	DecPtLED_L	A16
3	_GIO_A	USER_Define	A17
4	_GIO_A	USER_Define	A18
70	_GIO	USER_Define	Amp shutdown
41	LED_CR4	DecPtLED_R	DAC Clk
72	_GIO	USER_Define	DAC LD
69	_GIO	USER_Define	DAC LL
8	_GIO_A	USER_Define	Eprom Bank 0 output enable (active low)
19	SW5	dipswitch	Eprom Bank 1 output enable (active low)
60	LED_CR1-2	BarLED0	q0
59	LED_CR1-2	BarLED1	q1
58	LED_CR1-2	BarLED2	q2
57	LED_CR1-2	BarLED3	q3
66	LED_CR1-2	BarLED4	q4
65	LED_CR1-2	BarLED5	q5
62	LED_CR1-2	BarLED6	q6
61	LED_CR1-2	BarLED7	q7
68	_GIO	USER_Define	Spare button
71	_GIO	USER_Define	UART IN
56	RESET	RESET	
45	LED_CR4	NumLED_R	
46	LED_CR4	NumLED_R	
47	LED_CR4	NumLED_R	
48	LED_CR4	NumLED_R	
49	LED_CR4	NumLED_R	
50	LED_CR4	NumLED_R	
51	LED_CR4	NumLED_R	
29	LED_CR3	NumLED_L	
35	LED_CR3	NumLED_L	
36	LED_CR3	NumLED_L	
38	LED_CR3	NumLED_L	
39	LED_CR3	NumLED_L	
40	LED_CR3	NumLED_L	
44	LED_CR3	NumLED_L	

20	SW5	dipswitch
23	SW5	dipswitch
24	SW5	dipswitch
25	SW5	dipswitch
26	SW5	dipswitch
27	SW5	dipswitch
28	SW5	dipswitch
13	OSCILLATOR	16Mhz CLK
1	GND	Reserved
2	VCC	Reserved
11	VCC	Reserved
12	GND	Reserved
15	TDI	Reserved
16	TCK	Reserved
17	TMS	Reserved
21	GND	Reserved
22	VCC	Reserved
30	M1	Reserved
31	GND	Reserved
32	M0	Reserved
33	VCC	Reserved
34	M2	Reserved
42	VCC	Reserved
43	GND	Reserved
52	GND	Reserved
53	XC_DONE	Reserved
54	VCC	Reserved
55	XC_PROG	Reserved
63	VCC	Reserved
64	GND	Reserved
73	XC_CLK	Reserved
74	VCC	Reserved
75	TDO	Reserved
76	GND	Reserved

SORTED BY PIN #

Pin	name	function	USE
3	_GIO_A	USER_Define	A17
4	_GIO_A	USER_Define	A18
5	_GIO_A	USER_Define	A2
6	_GIO_A	USER_Define	A10
7	_GIO_A	USER_Define	A11
8	_GIO_A	USER_Define	Eprom Bank 0 output enable (active low)
9	_GIO_A	USER_Define	A9
10	_GIO_A	USER_Define	A8
13	OSCILLATOR	16Mhz CLK	
14	_GIO	USER_Define	A13
18	_GIO	USER_Define	A14
19	SW5	dipswitch	Eprom Bank 1 output enable (active low)
20	SW5	dipswitch	
23	SW5	dipswitch	
24	SW5	dipswitch	
25	SW5	dipswitch	
26	SW5	dipswitch	
27	SW5	dipswitch	
28	SW5	dipswitch	
29	LED_CR3	NumLED_L	
35	LED_CR3	NumLED_L	
36	LED_CR3	NumLED_L	
37	LED_CR3	DecPtLED_L	A16
38	LED_CR3	NumLED_L	
39	LED_CR3	NumLED_L	
40	LED_CR3	NumLED_L	
41	LED_CR4	DecPtLED_R	DAC Clk
44	LED_CR3	NumLED_L	
45	LED_CR4	NumLED_R	
46	LED_CR4	NumLED_R	
47	LED_CR4	NumLED_R	
48	LED_CR4	NumLED_R	
49	LED_CR4	NumLED_R	
50	LED_CR4	NumLED_R	
51	LED_CR4	NumLED_R	
56	RESET	RESET	
57	LED_CR1-2	BarLED3	q3
58	LED_CR1-2	BarLED2	q2
59	LED_CR1-2	BarLED1	q1
60	LED_CR1-2	BarLED0	q0
61	LED_CR1-2	BarLED7	q7
62	LED_CR1-2	BarLED6	q6
65	LED_CR1-2	BarLED5	q5
66	LED_CR1-2	BarLED4	q4
67	_GIO	USER_Define	A12
68	_GIO	USER_Define	Spare button
69	_GIO	USER_Define	DAC LL
70	_GIO	USER_Define	Amp shutdown
71	_GIO	USER_Define	UART IN

72	_GIO	USER_Define	DAC LD
77	_GIO_B	USER_Define	A0
78	_GIO_B	USER_Define	A3
79	_GIO_B	USER_Define	A15
80	_GIO_B	USER_Define	A5
81	_GIO_B	USER_Define	A4
82	_GIO_B	USER_Define	A7
83	_GIO_B	USER_Define	A6
84	_GIO_B	USER_Define	A1