

47

EntryHi
10*EntryLo0
2*EntryLo1
3*Index
0*Context
4*BadVAddr
8*Random
1*Count
9*Compare
11*Page Mask
5*Status
12*Cause
13*

TLB

Wired
6*EPC
14*WatchLo
18*PRId
15*WatchHi
19*XContext
20*Config
16*ECC
26*CacheErr
27*

("Safe" entries)
(See Random Register,
contents of TLB Wired)

127

0

LLAddr
17*TagLo
28*TagHi
29*ErrorEPC
30*

Used with memory
management system.

Used with exception
processing. See
Chapter 5 for details.

*Register number