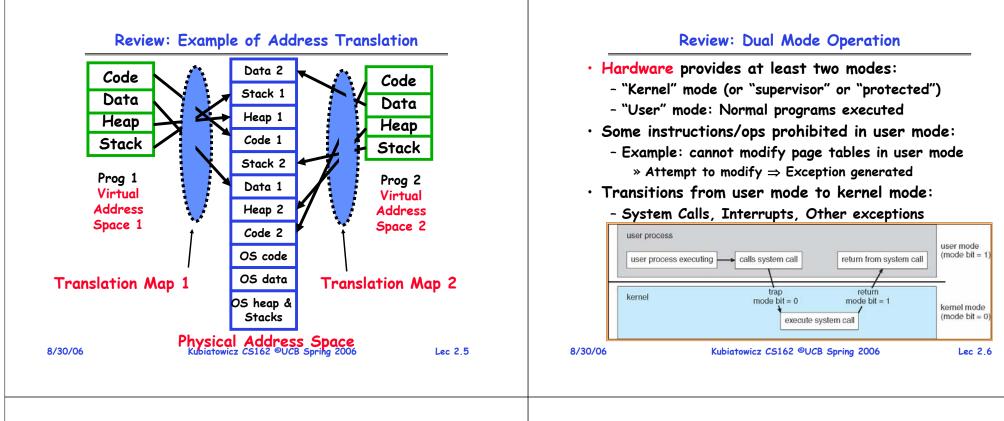
	Review: Virtual Machine Abstraction		
CS162 Openating Systems and	Application		
Operating Systems and Systems Programming Lecture 2	Operating System		
History of the World Parts 1—5 Operating Systems Structures August 30 <sup>th</sup> , 2006 Prof. John Kubiatowicz http://inst.eecs.berkeley.edu/~cs162	<ul> <li>Physical Machine Interface</li> <li>Hardware</li> <li>Software Engineering Problem: <ul> <li>Turn hardware/software quirks ⇒ what programmers want/need</li> <li>Optimize for convenience, utilization, security, reliability, etc</li> <li>For Any OS area (e.g. file systems, virtual memory, networking, scheduling): <ul> <li>What's the hardware interface? (physical reality)</li> <li>What's the application interface? (nicer abstraction)</li> </ul> </li> </ul></li></ul>		
<ul> <li>Review: Protecting Processes from Each Other</li> <li>Problem: Run multiple applications in such a way that they are protected from one another</li> <li>Goal: <ul> <li>Keep User Programs from Crashing OS</li> <li>Keep User Programs from Crashing each other</li> <li>[Keep Parts of OS from crashing other parts?]</li> </ul> </li> <li>(Some of the required) Mechanisms: <ul> <li>Address Translation</li> <li>Dual Mode Operation</li> </ul> </li> <li>Simple Policy: <ul> <li>Programs are not allowed to read/write memory of other Programs or of Operating System</li> </ul> </li> </ul>	<ul> <li>Review: Address Translation</li> <li>Address Space         <ul> <li>A group of memory addresses usable by something</li> <li>Each program (process) and kernel has potentially different address spaces.</li> </ul> </li> <li>Address Translation:         <ul> <li>Translate from Virtual Addresses (emitted by CPU) into Physical Addresses (of memory).</li> <li>Mapping often performed in Hardware by Memory Management Unit (MMU)</li></ul></li></ul>		

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## Goals for Today

- History of Operating Systems
  - Really a history of resource-driven choices
- Operating Systems Structures
- Operating Systems Organizations

## Moore's Law Change Drives OS Change

	1981	2006	Factor
CPU MHz,	10	3200x4	1,280
Cycles/inst	3—10	0.25-0.5	6—40
DRAM capacity	128KB	4GB	32,768
Disk capacity	10MB	1TB	100,000
Net bandwidth	9600 b/s	1 Gb/s	110,000
# addr bits	16	32	2
#users/machine	10s	≤ <b>1</b>	≤ 0.1
Price	\$25,000	\$4,000	0.2

### Typical academic computer 1981 vs 2006

Note: Some slides and/or pictures in the following are adapted from slides ©2005 Silberschatz, Galvin, and Gagne. Many slides generated from my lecture notes by Kubiatowicz.

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#### Moore's law effects

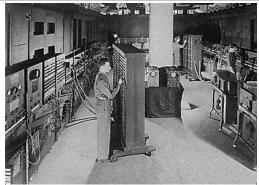
- Nothing like this in any other area of business
- Transportation in over 200 years:
  - 2 orders of magnitude from horseback @10mph to Concorde @1000mph
  - Computers do this every decade!
- What does this mean for us?
  - Techniques have to vary over time to adapt to changing tradeoffs
- I place a lot more emphasis on principles
  - The key concepts underlying computer systems
  - Less emphasis on facts that are likely to change over the next few years...
- Let's examine the way changes in \$/MIP has radically changed how OS's work

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#### Dawn of time ENIAC: (1945—1955)



• "The machine designed by Drs. Eckert and Mauchly was a monstrosity. When it was finished, the ENIAC filled an entire room, weighed thirty tons, and consumed two hundred kilowatts of power."

• http://ei.cs.vt.edu/~history/ENIAC.Richey.HTML

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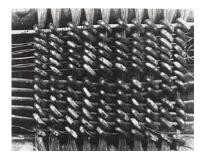
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History Phase 1 (1948—1970) Hardware Expensive, Humans Cheap

- When computers cost millions of \$'s, optimize for more efficient use of the hardware!
   Lack of interaction between user and computer
- User at console: one user at a time
- Batch monitor: load program, run, print
- Optimize to better use hardware
  - When user thinking at console, computer idle $\Rightarrow$ BAD!
  - Feed computer batches and make users wait
  - Autograder for this course is similar
- No protection: what if batch program has bug?

### Core Memories (1950s & 60s)



The first magnetic core memory, from the IBM 405 Alphabetical Accounting Machine.

- Core Memory stored data as magnetization in iron rings
  - Iron "cores" woven into a 2-dimensional mesh of wires
  - Origin of the term "Dump Core"
  - Rumor that IBM consulted Life Saver company
- See: http://www.columbia.edu/acis/history/core.html

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#### History Phase $1\frac{1}{2}$ (late 60s/early 70s)

Data channels, Interrupts: overlap I/O and compute • - DMA - Direct Memory Access for I/O devices - I/O can be completed asynchronously • Multiprogramming: several programs run simultaneously - Small jobs not delayed by large jobs - More overlap between I/O and CPU - Need memory protection between programs and/or OS Complexity gets out of hand: - Multics: announced in 1963, ran in 1969 » 1777 people "contributed to Multics" (30-40 core dev) » Turing award lecture from Fernando Corbató (key researcher): "On building systems that will fail' - OS 360: released with 1000 known bugs (APARs) » "Anomalous Program Activity Report" • OS finally becomes an important science: - How to deal with complexity??? - UNIX based on Multics, but vastly simplified 8/30/06 Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.13

#### A Multics System (Circa 1976)

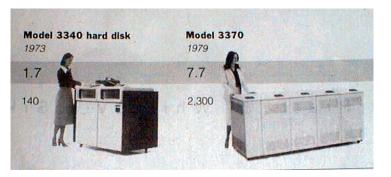


• The 6180 at MIT IPC, skin doors open, circa 1976:

- "We usually ran the machine with doors open so the operators could see the AQ register display, which gave you an idea of the machine load, and for convenient access to the EXECUTE button, which the operator would push to enter BOS if the machine crashed."

• http://www.multicians.org/multics-stories.html 8/30/06 Kubiatowicz C5162 ©UCB Spring 2006 Lec 2.14

#### Early Disk History



1973: 1. 7 Mbit/sq. in 140 MBytes



source: New York Times, 2/23/98, page C3, "Makers of disk drives crowd even more data into even smaller spaces" History Phase 2 (1970 – 1985) Hardware Cheaper, Humans Expensive

- Computers available for tens of thousands of dollars instead of millions
- OS Technology maturing/stabilizing
- Interactive timesharing:
  - Use cheap terminals (~\$1000) to let multiple users interact with the system at the same time
  - Sacrifice CPU time to get better response time
  - Users do debugging, editing, and email online
- Problem: Thrashing
  - Performance very non-linear response with load
  - Thrashing caused by many factors including
    - » Swapping, queueing



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#### Administriva: Almost Time for Project Signup

- Section time change
  - Section 104 (3-4pm) will change to earlier time
  - Still a bit up in the air
- Project Signup: Watch "Group/Section Assignment Link"
  - 4-5 members to a group
  - Only submit once per group!
  - Everyone in group must have logged into their cs162-xx accounts once before you register the group
  - Make sure that you select at least 2 potential sections
  - Due date: Thursday 9/7 by 11:59pm

Section	Time	Location	TA
101	Th 9:00-10:00P	3111 Etcheverry	TBA
102	Th 11:00-12:00P	85 Evans	TBA
103	Th 2:00-3:00P	87 Evans	TBA
104	Th 3:00-4:00P	87 Evans	TBA
/ <sub>80/0</sub> 105	Th 4:00-5:00P	C5162 75 Evans 2006	TBALec 2.17

#### Administrivia (2)

- Cs162-xx accounts:
  - Make sure you got an account form
  - If you haven't logged in yet, you need to do so
- Nachos readers:
  - TBA
  - Will include lectures and printouts of all of the code
- Web cast archives available off lectures page
  - Just click on the title of a lecture for webcast
  - Only works for lectures that I have already given!
- No slip days on first design document for each phase
  - Need to get design reviews in on time
- Don't know Java well?
  - Talk CS 9G self-paced Java course

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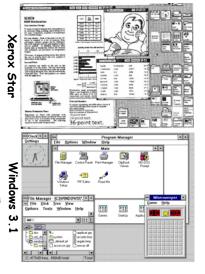
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#### History Phase 3 (1981— ) Hardware Very Cheap, Humans Very Expensive

- Computer costs \$1K, Programmer costs \$100K/year
  - If you can make someone 1% more efficient by giving them a computer, it's worth it!
  - Use computers to make people more efficient
- Personal computing:
  - Computers cheap, so give everyone a PC
- Limited Hardware Resources Initially:
  - OS becomes a subroutine library
  - One application at a time (MSDOS, CP/M, ...)
- Eventually PCs become powerful:
  - OS regains all the complexity of a "big" OS
  - multiprogramming, memory protection, etc (NT,OS/2)
- Question: As hardware gets cheaper does need for OS go away?

#### History Phase 3 (con't) Graphical User Interfaces

- · CS160  $\Rightarrow$  All about GUIs
- Xerox Star: 1981
  - Originally a research project (Alto)
  - First "mice", "windows"
- Apple Lisa/Machintosh: 1984
  "Look and Feel" suit 1988
- Microsoft Windows:
  - Win 1.0 (1985) Single
  - Win 3.1 (1990) Level
  - Win 95 (1995)
  - Win NT (1993) HAL/Protection
  - Win 2000 (2000)
  - Win XP (2001) Full Prot



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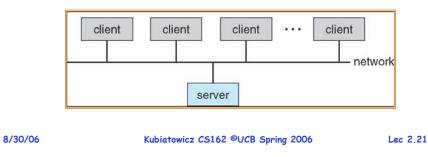
#### History Phase 4 (1989—): Distributed Systems

- · Networking (Local Area Networking)
  - Different machines share resources
  - Printers, File Servers, Web Servers
  - Client Server Model
- Services

MFM

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- Computing
- File Storage



## A Societal Scale Information System Center for Information Technology Research in the Interest of Society • The Network is the OS - Functionality spread throughout network calable, Reliable, ecure Services

CITRIS's Model:



## History Phase 5 (1995—): Mobile Systems

- Ubiguitous Mobile Devices
  - Laptops, PDAs, phones
  - Small, portable, and inexpensive
    - » Recently twice as many smart phones as PDAs
    - » Many computers/person!
  - Limited capabilities (memory, CPU, power, etc...)
- Wireless/Wide Area Networking
  - Leveraging the infrastructure
  - Huge distributed pool of resources extend devices
  - Traditional computers split into pieces. Wireless keyboards/mice, CPU distributed, storage remote
- · Peer-to-peer systems
  - Many devices with equal responsibilities work together
  - Components of "Operating System" spread across globe

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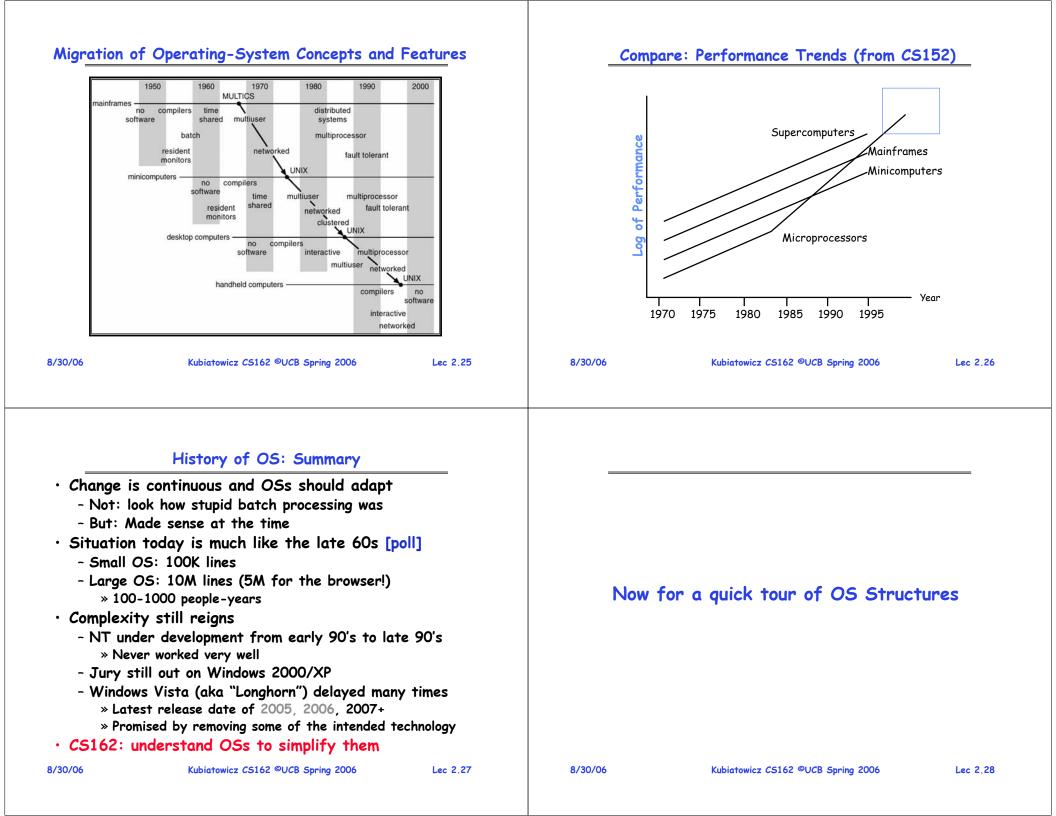
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## Moore's Law Reprise: Modern Laptop

	1981	2005	2006 Ultralight Laptop
CPU MHz,	10	3200×4	1830
Cycles/inst	3—10	0.25-0.5	0.25-0.5
DRAM capacity	128KB	4GB	2GB
Disk capacity	10MB	1TB	100 <i>G</i> B
Net bandwidth	9600 b/s	1 <i>G</i> b/s	1 Gb/s (wired) 54 Mb/s (wireless) 2 Mb/s (wide-area)
# addr bits	16	32	32
#users/machine	10s	≤ <b>1</b>	$\leq \frac{1}{4}$
Price	\$25,000	\$4,000	\$2500

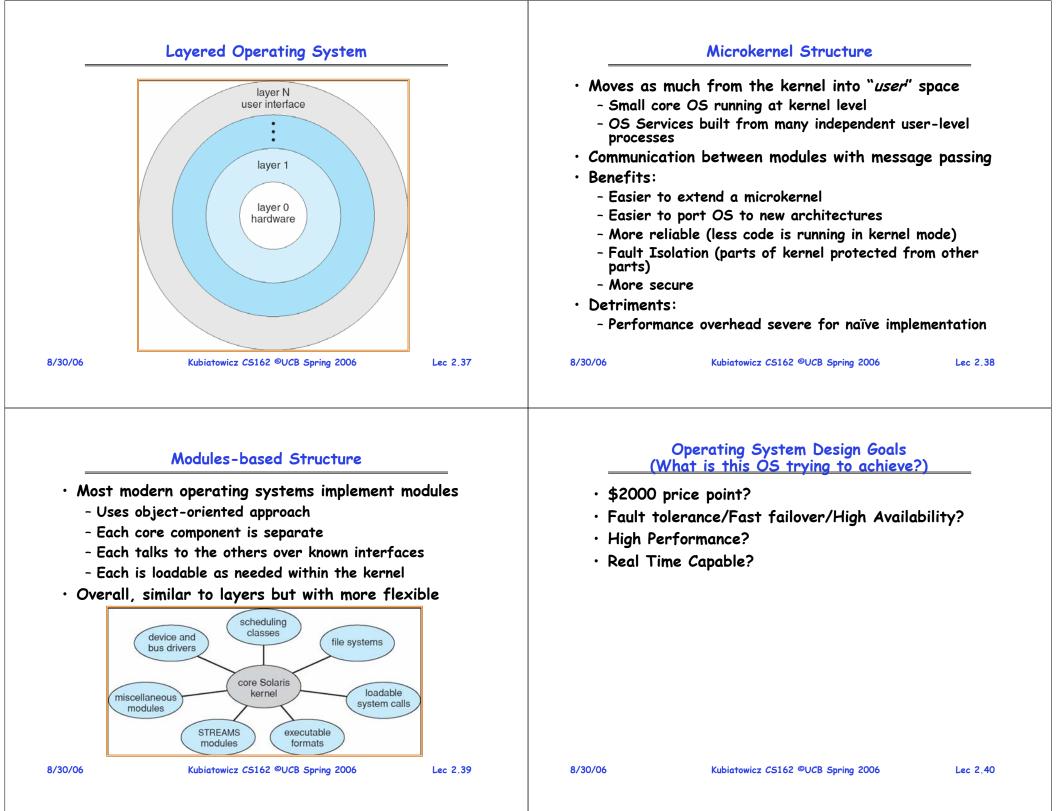
Lec 2,23



<ul> <li>Process Management</li> <li>Main-Memory Management</li> <li>L/O System management</li> <li>File Management</li> <li>Networking</li> <li>User Interfaces</li> </ul> <ul> <li>We do you execute concurrent sequences of instruction whow do you execute concurrent sequences of instructions whom do you execute concurrent sequences devices whom do you execute concurrent sequences of instructions whom do you execute concurrent sequences of concurrent sequences do you execute concurrent sequences of power sequences of the</li></ul>	Operating Systems Components (What are the pieces of the OS)	Operating System Services (What things does the OS do?)	
System Calls (What is the API) • See Chapter 2 of 7 <sup>th</sup> edition or Chapter 3 of 6 <sup>th</sup>	<ul> <li>Main-Memory Management</li> <li>I/O System management</li> <li>File Management</li> <li>Networking</li> </ul>	<ul> <li>Services that (more-or-less) map onto components         <ul> <li>Program execution</li> <li>How do you execute concurrent sequences of instructions?</li> <li>I/O operations</li> <li>Standardized interfaces to extremely diverse devices</li> <li>File system manipulation</li> <li>How do you read/write/preserve files?</li> <li>Looming concern: How do you even find files???</li> <li>Communications</li></ul></li></ul>	
<ul> <li>See Chapter 2 of 7<sup>th</sup> edition or Chapter 3 of 6<sup>th</sup></li> <li>Simple <ul> <li>Only one or two levels of code</li> </ul> </li> <li>Layered <ul> <li>Lower levels independent of upper levels</li> </ul> </li> <li>Microkernel <ul> <li>OS built from many user-level processes</li> </ul> </li> <li>Modular <ul> <li>Core kernel with Dynamically loadable modules</li> </ul> </li> </ul>	/30/06 Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.29	8/30/06 Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.30	
i for open () system call : return	• See Chapter 2 of 7 <sup>th</sup> edition or Chapter 3 of 6 <sup>th</sup>	<ul> <li>(What is the organizational Principle?)</li> <li>Simple <ul> <li>Only one or two levels of code</li> </ul> </li> <li>Layered <ul> <li>Lower levels independent of upper levels</li> </ul> </li> <li>Microkernel <ul> <li>OS built from many user-level processes</li> <li>Modular</li> </ul> </li> </ul>	

# Simple Structure

	d into modules and levels of functionality not well	<ul> <li>UNIX - limited by hardware functionality</li> <li>Original UNIX operating system consists of two separable parts: <ul> <li>Systems programs</li> <li>The kernel</li> <li>Consists of everything below the system-call interface and above the physical hardware</li> <li>Provides the file system, CPU scheduling, memory management, and other operating-system functions;</li> <li>Many interacting functions for one level</li> </ul> </li> </ul>
30/06	ROM BIOS device drivers Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.33	8/30/06 Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.34
	UNIX System Structure	Layered Structure
User Mode	UNIX System Structure         Applications       (the users)         Standard Libs       shells and commands compilers and interpreters system libraries	<ul> <li>Operating system is divided many layers (levels)</li> <li>Each built on top of lower layers</li> <li>Bottom layer (layer 0) is hardware</li> <li>Highest layer (layer N) is the user interface</li> </ul>
User Mode	Applications       (the users)         Standard Libs       shells and commands compilers and interpreters	<ul> <li>Operating system is divided many layers (levels)</li> <li>- Each built on top of lower layers</li> <li>- Bottom layer (layer 0) is hardware</li> </ul>



Implementation Issues (How is the OS implemented?)	Conclusion
Policy vs. Mechanism - Policy: What do you want to do? - Mechanism: How are you going to do it? - Should be separated, since both change Algorithms used - Linear, Tree-based, Log Structured, etc Event models used - threads vs event loops Backward compatability issues - Very important for Windows 2000/XP System generation/configuration - How to make generic OS fit on specific hardware	<ul> <li>Rapid Change in Hardware Leads to changing OS <ul> <li>Batch ⇒ Multiprogramming ⇒ Timeshare ⇒ Graphical UI ⇒ Ubiquitous Devices ⇒ Cyberspace/Metaverse/??</li> </ul> </li> <li>OS features migrated from mainframes ⇒ PCs</li> <li>Standard Components and Services <ul> <li>Process Control</li> <li>Main Memory</li> <li>I/O</li> <li>File System</li> <li>UI</li> </ul> </li> <li>Policy vs Mechanism <ul> <li>Crucial division: not always properly separated!</li> </ul> </li> <li>Complexity is always out of control <ul> <li>However, "Resistance is NOT Useless!"</li> </ul> </li> </ul>
06 Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.41	8/30/06 Kubiatowicz CS162 ©UCB Spring 2006 Lec 2.