Great Ideas in Computer Architecture

Course Intro, Number Representation

Instructors: Steven Ho, Nick Riasanovsky
Introducing Steven

• Instructor
• **Upbringing:** Born and raised in LA
• **Education:** Newly-graduated CS & Ling double major

• **Teaching:** 61C course staff for 6 semesters, 188 TA last semester
• **Interests:**
Introducing Nick

• **Upbringing:** Born and raised in Orange County
• **Education:** Rising fourth year majoring in CS
• **Teaching:**
  – TA for 61C for 3 semesters
• **Interests:** Eating, Watching Sports, Video games
Introducing Your TAs

Sean

Damon: Head TA

Jonathan

Suvansh

Sruthi

Emaan

Sukrit
Introducing Your Tutors

Hersh

Alex

Anusha

Spencer

Stephan
Agenda

• Course Overview
• Course Policies
• Administrivia
• Number Representation
  – Number Bases
  – Signed Representations
  – Overflow
  – Sign Extension
61C Mission Statement

“CS61C introduces students to foundational concepts that form the backbone of different branches of computer science such as computer architecture, low-level software, computer security, and large-scale computing. This course demystifies some of the vulnerabilities inherent in abstraction barriers that programmers of all levels confide in when the performance and correctness of high-level code is under fire. After taking this course, students will be able to see computing through a bit-level perspective and understand the central trade-offs and choices involved in making fast hardware and software of all levels that work together efficiently and safely.”

— Steven and Nick
Hardware-Software Interface

CS61C
CS61C For Software Development

• Knowing the tools of the trade – computers!
  – “Computers” come in all shapes and sizes
  – Computing achieved in many different ways nowadays

• When performance matters
  – Ex: taking advantage of parallelism

• Differences between programming languages under the hood

• Design large systems – abstraction in hardware

• Security

• Design methodology – limitations and tradeoffs
Course Learning Objectives

- After taking this class students should be able to:
  - Identify and explain the various layers of abstraction that allow computer users to perform complex software tasks without understanding what the computer hardware is actually doing
  - Judge the effect of changing computer components (e.g. processor, RAM, HDD, cache) on the performance of a computer program
  - Explain how the memory hierarchy creates the illusion of being almost as fast as the fastest type of memory and almost as large as the biggest memory
  - Construct a working CPU from logic gates for a specified instruction set architecture
  - Identify the different types of parallelism and predict their effects on different types of applications
Great Idea #1: Abstraction
(Levels of Representation/Interpretation)

High Level Language Program (e.g., C)

Compiler

Assembly Language Program (e.g., RISC V)

Assembler

Machine Language Program (RISC V)

Machine Interpretation

Hardware Architecture Description (e.g., block diagrams)

Architecture Implementation

Logic Circuit Description (Circuit Schematic Diagrams)

temp = v[k];
v[k] = v[k+1];
v[k+1] = temp;

lw $t0, 0($2)
lw $t1, 4($2)
sw $t1, 0($2)
sw $t0, 4($2)

Anything can be represented as a number, i.e., data or instructions

0000 1001 1100 0110 1010 1111 0101 1000
1010 1111 0101 1000 0000 1001 1100 0110
1100 0110 1010 1111 0101 1000 0000 1001
0101 1000 0000 1001 1100 0000 0000 1111
Course Learning Objectives

• In addition, this class will requires students to work on the following skills:
  – Creating and modifying designs to meet a given set of specifications
  – Identifying unexpected or problematic situations and create test cases to ensure proper behavior
  – Defending design choices based on tradeoffs and limitations
Six Great Ideas in Computer Architecture

1) Abstraction
2) Technology Trends
3) Principle of Locality/Memory Hierarchy
4) Parallelism
5) Performance Measurement & Improvement
6) Dependability via Redundancy
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Course Information

More info found on the course policies page

• **Website:** [http://inst.eecs.berkeley.edu/~cs61c/su18](http://inst.eecs.berkeley.edu/~cs61c/su18)
  – Check for assignments, weekly schedule, contact info

• **Textbooks:**

• **Piazza:** [http://piazza.com/class#summer2018/cs61c](http://piazza.com/class#summer2018/cs61c)
  – Will have every announcement, discussion, clarification

• **Inst account:** [http://inst.eecs.berkeley.edu/webacct](http://inst.eecs.berkeley.edu/webacct)
  – Submit assignments, check grades, access lab files
Course Assignments and Grading

- **Homework** (10%) – 8 total on edX
- **Labs** (10%) – 13 total
- **Projects** (30%) – 5 total, weighted by difficulty
  - Proj1 7% – Proj2-1 3% – Proj2-2 4%
  - Proj3 9% – Proj4 7%
- **Exams**
  - **Midterm 1** (13%): Monday, July 3 during class time
  - **Midterm 2** (13%): Monday, July 25 during class time
  - **Final** (23%): Thursday, August 9 @ 7pm
- **EPA** (5%) – Effort, Participation, and Altruism
  - Extra credit awarded to encourage learning environment
Lab Grading Policy

• Out of 2 points. A new lab is assigned every Tue/Thu
  – In order to receive credit for a lab, you must get it checked off by a staff member
  – You will receive full points if you get it checked off the day the lab was assigned, or one lab session late
  – You will receive half credit if you get it checked off two lab sessions late.
  – You will receive no points on a lab that is more than 2 labs late.
  – Note: You will only have one attempt to check off any lab that is late (aka not assigned that day)
Project Late Policy – Slip Days

• Project submissions due at 11:59:59pm
  – Count lateness in *days* (even if just by a second)
• Late penalty is 33% deduction of score per day
• You are given *3 slip day tokens*
  – Will be distributed amongst your late submissions at the end of the semester to maximize your grade
  – No benefit to having leftover tokens
• Use at own risk – don’t want to fall too far behind
• Encourage class-wide learning!

• Effort
  – Attendance and active engagement in additional course events (eg: office hours, guerilla sessions, project parties, etc)

• Participation
  – Attendance and active engagement in required course events (eg: discussion, lab…)
  – …and lecture: iClickers!

• Altruism
  – Helping others anywhere (eg: lab, discussion, OH and Piazza)
EECS Grading Policy

- [http://www.eecs.berkeley.edu/Policies/ugrad.grading.shtml](http://www.eecs.berkeley.edu/Policies/ugrad.grading.shtml)
  
  “A typical GPA for a lower division course will fall in the range 2.8 - 3.3, depending on the course and the students who enroll. For example, a GPA of 3.0 would result from 35% A's, 45% B's, 13% C's, and 7% D's and F's.”

- Spring 2018: GPA 3.08
- 36% A's, 43% B's, 17% C's, 3% D's, 1% F's
- Graded using fixed bins

<table>
<thead>
<tr>
<th>Raw Score</th>
<th>290+</th>
<th>[270,290)</th>
<th>[260,270)</th>
<th>[250,260)</th>
<th>[230,250)</th>
<th>[220,230)</th>
<th>[210,220)</th>
<th>[190,210)</th>
<th>[180,190)</th>
<th>[140,160)</th>
<th>[0,140)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Grade</td>
<td>A+</td>
<td>A</td>
<td>A-</td>
<td>B+</td>
<td>B</td>
<td>B-</td>
<td>C+</td>
<td>C</td>
<td>C-</td>
<td>D</td>
<td>F</td>
</tr>
</tbody>
</table>

- If the grade bins result in an average GPA that is too low, the course will be curved to match department guidelines BEFORE adding EPA.
Peer Instruction

- Increase real-time learning in lecture, test your understanding, increase student interactions
  - Lots of research supports its effectiveness
- Multiple choice questions sprinkled throughout lecture
  - 1 minute to decide on your own
  - 2 minutes in pairs to reach consensus
  - Learn through discussion
- Vote using i>clicker remotes
  - Can borrow for summer with $40 check as collateral
**Question:** Which statement is FALSE about the CS61C policies this summer?

(A) Both homework and labs are being graded on completion, not correctness

(B) Activities in person (lab, disc) and online (Piazza) can only help your class grade

(C) There are no dropped homeworks or labs

(D) Labs checked off two sections after they were assigned will be given half credit
Working with Others

• We are encouraging partner/group work
• Beneficial for the class
  – Someone to bounce ideas off of and work with and keep you on task
  – Don’t have to go it alone – meet new people!
• Valuable life skill: working well with others
  – Distribution of work, communication skills, time management, teaching skills
Policy on Assignments and Independent Work

• All submissions are expected to be yours and yours alone (or group’s when applicable)
• You are encouraged to discuss your assignments with other students (ideas), but we expect that what you turn in is yours
• It is NOT acceptable to copy solutions from other students
• It is NOT acceptable to copy (or start your) solutions from the Web (including Github)
Policy on Assignments and Independent Work

• We have tools and methods, developed over many years, for detecting this. You WILL be caught, and the penalties WILL be severe.

• Both the *cheater* and the *enabler* receive –100% for the assignment. Letter to your university record documenting the incidence of cheating.
  – Possible automatic F in the course

• People are caught every semester of 61C
Course Resources

• Weekly Guerrilla Sessions
  – Led by tutors
  – Review sessions coming exam problems on specific topics

• Small-group tutoring
  – Sign up for a weekly tutoring session w/ a tutor
  – Groups are as small as 3-6 people; personalized help

• Project Parties
  – (Near) weekly events staffed by TAs, tutors, and possibly
    lab assistants to help you with your projects

• Office hours
  – Please come chat with us about any questions or concerns!
Successful Behaviors

• Practice, practice, practice
  – Learn by doing: deep learning doesn’t happen in lec
  – Growth mindset: success through effort

• Find a learning community
  – Learning is much more fun with friends
  – Learn via teaching or different perspectives

• Avoid electronic screens during lecture

• You learn best from your mistakes
  – Don’t be afraid to be wrong; you lose if you remain silent
Architecture of a Lecture

- Administrivia + stretch break
- Meet the Staff
- Summary + Bonus

Time (minutes)

Attention

Full
Administrivia

- Discussions, labs, and OHs start immediately
  - Yes, that means today!
  - Switching labs: if you find another 61C student willing to swap lab, talk to your TAs
  - Attend whichever discussion you want, as long as there is enough physical room

- HW0 and mini-bio due next Monday, June 25th
  - Find a digital image of yourself
  - Both worth points, neither can be dropped
## Meet the Staff

<table>
<thead>
<tr>
<th></th>
<th>Steven</th>
<th>Nick</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bribe Food</td>
<td>Pomegranates</td>
<td>Candy</td>
</tr>
<tr>
<td>Biggest Fear</td>
<td>Ramses' Curse</td>
<td>Heights</td>
</tr>
<tr>
<td>Unpopular Opinion</td>
<td>Pizza is terrible</td>
<td>Tabs &gt; Spaces</td>
</tr>
<tr>
<td>Café Getaway</td>
<td>Asha Tea House</td>
<td>Nefali's</td>
</tr>
</tbody>
</table>
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Number Representation

Example:

28 = (1 x 20) + 8 = ••

433 = (1 x 400) + (1 x 20) + 13 = •••
Computers: Base 2

Analog signal

Digital Signal
Number Base Examples

9472_{ten} = 9000 + 400 + 70 + 2
= 9 \times 1000 + 4 \times 100 + 7 \times 10 + 2 \times 1
= 9 \times 10^3 + 4 \times 10^2 + 7 \times 10^1 + 2 \times 10^0

General Formula:

d_{n-1} d_{n-2} \ldots d_1 d_0 \text{ (n-digit number in base B)}

= d_{n-1} \times B^{n-1} + d_{n-2} \times B^{n-2} + \ldots + d_1 \times B^1 + d_0 \times B^0

a digit d can take on values from 0 to B - 1
Commonly Used Number Bases

• **Decimal** (base 10)
  – Symbols: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9
  – Notation: $9472_{\text{ten}} = 9472$

• **Binary** (base 2)
  – Symbols: 0, 1
  – Notation: $101011_{\text{two}} = 0b101011$

• **Hexadecimal** (base 16)
  – Symbols: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, A, B, C, D, E, F
  – Notation: $2A5D_{\text{hex}} = 0x2A5D$

<table>
<thead>
<tr>
<th>Decimal</th>
<th>Binary</th>
<th>Hex</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0000</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0001</td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>0010</td>
<td>2</td>
</tr>
<tr>
<td>3</td>
<td>0011</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>0100</td>
<td>4</td>
</tr>
<tr>
<td>5</td>
<td>0101</td>
<td>5</td>
</tr>
<tr>
<td>6</td>
<td>0110</td>
<td>6</td>
</tr>
<tr>
<td>7</td>
<td>0111</td>
<td>7</td>
</tr>
<tr>
<td>8</td>
<td>1000</td>
<td>8</td>
</tr>
<tr>
<td>9</td>
<td>1001</td>
<td>9</td>
</tr>
<tr>
<td>10</td>
<td>1010</td>
<td>A</td>
</tr>
<tr>
<td>11</td>
<td>1011</td>
<td>B</td>
</tr>
<tr>
<td>12</td>
<td>1100</td>
<td>C</td>
</tr>
<tr>
<td>13</td>
<td>1101</td>
<td>D</td>
</tr>
<tr>
<td>14</td>
<td>1110</td>
<td>E</td>
</tr>
<tr>
<td>15</td>
<td>1111</td>
<td>F</td>
</tr>
</tbody>
</table>
i-not-Clicker Question

Convert 0b11110 to decimal.

(A) 11
(B) 30
(C) -2
(D) I’m so lost
(E) Who even cares?

\[1 \times 2^4 + 1 \times 2^3 + 1 \times 2^2 + 1 \times 2^1 + 0 \times 2^0\]
BIG Idea:

Bits Can Represent ANYTHING

• ...entirely depending on our *interpretation*

• **Characters?!**
  – 26 letters require 5 bits ($2^5 = 32 > 26$)
  – upper/lower case + punctuation → 7 bits (“ASCII”)

• **Logical values?!** 0 → False, 1 → True

• **Students in this class?!** $2^8 = 256 > 220$ → 8 bits

• **Colors?!**
  - Red (00)
  - Green (01)
  - Blue (11)

• **ONLY ONE constraint:**
  \[ n \text{ digits (base } B) \Rightarrow \leq B^n \text{ things} \]
  – Each of the \( n \) digits is one of \( B \) possible symbols
  – Have more things? Add more digits!

6/19/2017
How do we assign values to the Binary numerals available to us?
Unsigned Integers

Represent only non-negative (unsigned) integers:

\[ \begin{align*}
000_\text{two} &= 0_{\text{ten}} \\
001_\text{two} &= 1_{\text{ten}} \\
010_\text{two} &= 2_{\text{ten}} \\
011_\text{two} &= 3_{\text{ten}} \\
100_\text{two} &= 4_{\text{ten}} \\
101_\text{two} &= 5_{\text{ten}} \\
110_\text{two} &= 6_{\text{ten}} \\
111_\text{two} &= 7_{\text{ten}} \\
\end{align*} \]

Zero?
\[ 0...0_\text{two} = 0_{\text{ten}} \]

Most neg number?
\[ 0...0_\text{two} = 0_{\text{ten}} \]

Most pos number?
\[ 1...1_\text{two} = (2^n-1)_{\text{ten}} \]

Increment?
Signed Integers

- $n$ bits $\Rightarrow 2^n$ things
  - half positive, half negative?
- First logical step:
  - $+534$ $-256$
- Let the first bit be interpreted as a sign!
Sign and Magnitude

“first” bit gives sign, rest treated as unsigned (magnitude):

\[
\begin{align*}
000_{\text{two}} &= +0_{\text{ten}} \\
001_{\text{two}} &= +1_{\text{ten}} \\
010_{\text{two}} &= +2_{\text{ten}} \\
011_{\text{two}} &= +3_{\text{ten}} \\
100_{\text{two}} &= -0_{\text{ten}} \\
101_{\text{two}} &= -1_{\text{ten}} \\
110_{\text{two}} &= -2_{\text{ten}} \\
111_{\text{two}} &= -3_{\text{ten}}
\end{align*}
\]

Zero?

\[
0\ldots0_{\text{two}} \text{ and } 10\ldots0_{\text{two}} = \pm0_{\text{ten}}
\]

TWO ZEROS?!

Most pos number?

\[
01\ldots1_{\text{two}} = (2^{(n-1)}-1)_{\text{ten}}
\]

Most neg number?

\[
1\ldots1_{\text{two}} = -(2^{(n-1)}-1)_{\text{ten}}
\]

Increment?
Biased Notation

Like unsigned, but “shifted” so zero is (roughly) in the middle: (value = “unsigned value” - bias)

\[
\begin{align*}
000_{\text{two}} &= -3_{\text{ten}} \\
001_{\text{two}} &= -2_{\text{ten}} \\
010_{\text{two}} &= -1_{\text{ten}} \\
011_{\text{two}} &= 0_{\text{ten}} \\
100_{\text{two}} &= +1_{\text{ten}} \\
101_{\text{two}} &= +2_{\text{ten}} \\
110_{\text{two}} &= +3_{\text{ten}} \\
111_{\text{two}} &= +4_{\text{ten}}
\end{align*}
\]

Zero?
\[
01\ldots1_{\text{two}} = 0_{\text{ten}}
\]

Most neg number?
\[
0\ldots0_{\text{two}} = -(2^{(n-1)}-1)_{\text{ten}}
\]

Most pos number?
\[
1\ldots1_{\text{two}} = 2^{(n-1)}_{\text{ten}}
\]

Increment?
just like unsigned!
One’s Complement

New negation procedure – complement the bits:

\[
\begin{align*}
000_{\text{two}} &= +0_{\text{ten}} \\
001_{\text{two}} &= +1_{\text{ten}} \\
010_{\text{two}} &= +2_{\text{ten}} \\
011_{\text{two}} &= +3_{\text{ten}}
\end{align*}
\]

\[
\begin{align*}
100_{\text{two}} &= -3_{\text{ten}} \\
101_{\text{two}} &= -2_{\text{ten}} \\
110_{\text{two}} &= -1_{\text{ten}} \\
111_{\text{two}} &= -0_{\text{ten}}
\end{align*}
\]

Zero?

\[
0\ldots0_{\text{two}} \text{ and } 1\ldots1_{\text{two}} = \pm 0_{\text{ten}}
\]

TWO ZEROS AGAIN…….

Most neg number?

\[
10\ldots0_{\text{two}} = -(2^{(n-1)}-1)_{\text{ten}}
\]

Most pos number?

\[
01\ldots1_{\text{two}} = (2^{(n-1)}-1)_{\text{ten}}
\]

Increment?
Two’s Complement

Like One’s Complement, but “shift” negative #s by 1:

\[
\begin{array}{l}
000_{\text{two}} = +0_{\text{ten}} \\
001_{\text{two}} = +1_{\text{ten}} \\
010_{\text{two}} = +2_{\text{ten}} \\
011_{\text{two}} = +3_{\text{ten}} \\
100_{\text{two}} = -4_{\text{ten}} \\
101_{\text{two}} = -3_{\text{ten}} \\
110_{\text{two}} = -2_{\text{ten}} \\
111_{\text{two}} = -1_{\text{ten}}
\end{array}
\]

Zero?

\[0\ldots0_{\text{two}} = 0_{\text{ten}}\]

Most neg number?

\[10\ldots0_{\text{two}} = -2^{(n-1)}_{\text{ten}}\]

Most pos number?

\[01\ldots1_{\text{two}} = (2^{(n-1)}-1)_{\text{ten}}\]

Increment?

just like unsigned!
Two’s Complement Summary

• Used by all modern hardware

• Roughly evenly split between positive and negative
  – One more negative # because positive side has 0

• Can still use MSB as sign bit

• To negate: Flip the bits and add one
  – Example: +7 = 0b 0000 0111, -7 = 0b 1111 1001
Two’s Complement Review

• Suppose we had 5 bits. What range of integers can be represented in two’s complement?
  
  (A)  -31 to +31  ← need 6 bits
  (B)   -15  to +15 ← one’s complement
  (C)    0  to +31 ← unsigned
  (D)  -16  to +15 ← two’s complement
  (E)  -32  to +31 ← need 6 bits
Numbers in a Computer

- Numbers really have $\infty$ digits, but hardware can only store a finite number of them (fixed)
  - Usually ignore *leading zeros*
  - Leftmost is *most significant bit* (MSB)
  - Rightmost is *least significant bit* (LSB)
Overflow

- **Overflow** is when the result of an arithmetic operation can’t be represented by the (FINITE) hardware bits
  - i.e. the result is mathematically incorrect

- **Examples:**
  - Unsigned: $0b1\ldots1 + 1_{10} = 0b0\ldots0 = 0$?
  - Two’s: $0b01\ldots1 + 1_{10} = 0b10\ldots0 = -2^{(n-1)}_{\text{ten}}$?

- **Solution:** add more bits
Sign Extension

• Want to represent the same number using more bits than before
  – Easy for positive #s (add leading 0’s), more complicated for negative #s
  – Sign and magnitude: add 0’s after the sign bit
  – One’s complement: copy MSB
  – Two’s complement: copy MSB

• Example:
  – Sign and magnitude: 0b 11 = 0b 1001
  – One’s/Two’s complement: 0b 11 = 0b 1111
Summary

• Number Representation: How to represent positive and negative integers using binary
  – Unsigned: Interpret numeral in base 2
  – Signed: Two’s Complement
  – Biased: Subtract bias
  – Sign extension must preserve signed number