EE43 Lab #1
Introduction to Lab: Report

Name(s): ________________________
TA: ________________________
Section: ________________________

i. Breadboard
a. Are the two wires connected? Check Yes or No: Yes  No
b. Are the two wires connected? Check Yes or No: Yes  No
c. Are the two wires connected? Check Yes or No: Yes  No

ii. Use DMM to measure power supply voltages.
Supply Readout Voltage Value: 5 V
Measured Voltage Value:

Supply Readout Voltage Value: 14 V
Measured Voltage Value:

iii. Use DMM to measure some resistors and pots.
Nominal Resistance: 1 kΩ
Measured Resistance: _______________

Measured resistance between the outer two legs of pot:
Measured resistance between the middle leg and one of the outer two legs:

iv. Series resistor circuit.

<table>
<thead>
<tr>
<th></th>
<th>Expected</th>
<th>Measured</th>
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</thead>
<tbody>
<tr>
<td>Voltage across R1</td>
<td></td>
<td></td>
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<tr>
<td>Current through R1</td>
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v. Parallel resistor circuit.

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<tr>
<th></th>
<th>Expected</th>
<th>Measured</th>
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<tbody>
<tr>
<td>Voltage across R2</td>
<td></td>
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<tr>
<td>Current through R2</td>
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On a concluding remark, notice that we ALWAYS say “voltage across” and “current through”. We NEVER say “voltage through” and “current across”. Because a voltage is a potential difference across two points and a current always flows through a device.