

## FE102 Discussion

Q:)



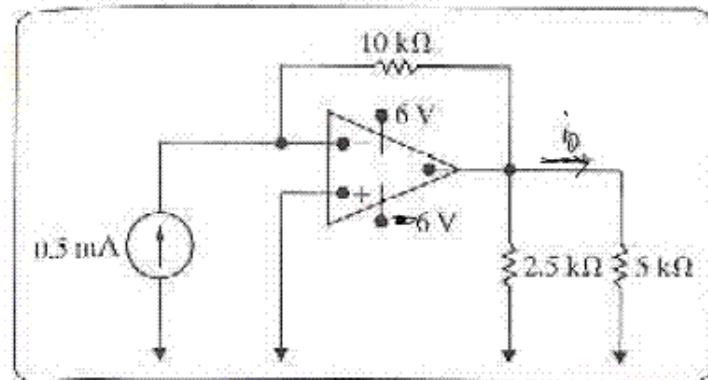
} Circuit analysis  
 $i_o \neq 0$ !

e.g.: From Op-amp review.

5.3 Find  $i_o$  in the circuit in Fig. P5.3 if the op amp is ideal.

[P]

Figure P5.3

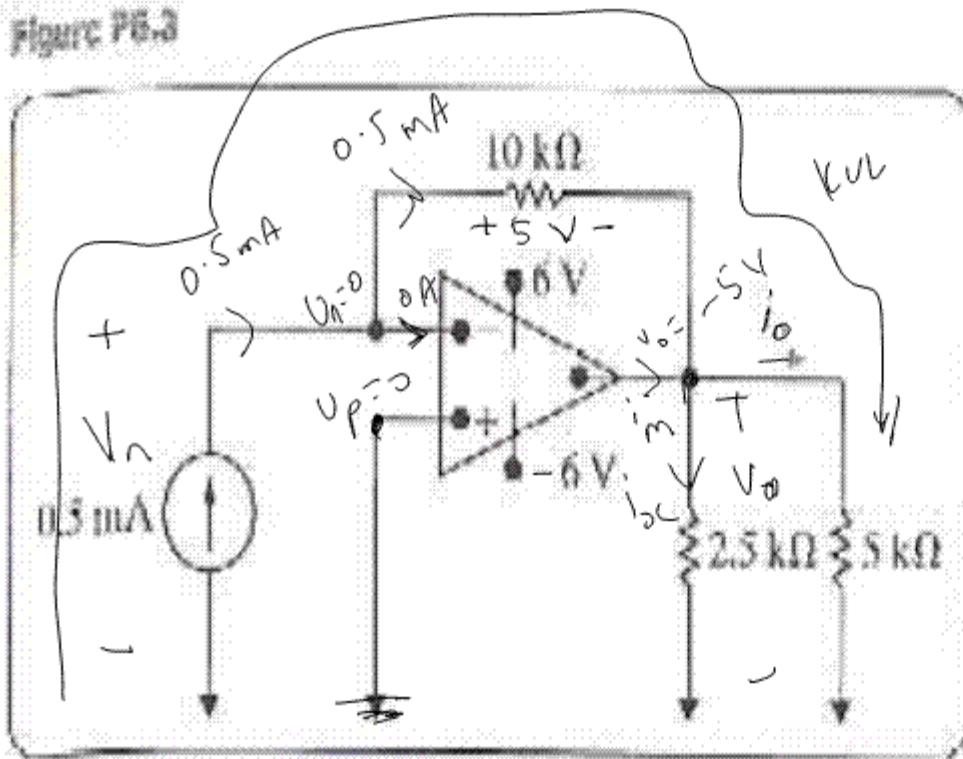


5.3

Find  $i_o$  in the circuit in Fig. P5.3 if the op amp is ideal.

P

Figure P5.3



Assume  $V_P \approx V_N$   
 (Do Not ignore rail voltage)

$$[CVL] \quad V_o + (-5V) + (-V_o) = 0 \Rightarrow V_o = -5V \quad (V_n = 0)$$

Since  $-5 > -6 \Rightarrow$  op-amp does not sat.

$$i_o = -\frac{5V}{5k} = -\underline{\underline{1mA}}$$

Bonus: What's  $i_m$ ? [CCl @  $V_o$ ]:  $0.5mA + i_m = i_o + i_{dc}$

$$\Rightarrow i_m = -\cancel{1mA} + (-0.5mA) + \left( \cancel{-5} \right) \cancel{\frac{1mA}{2.5k}}$$

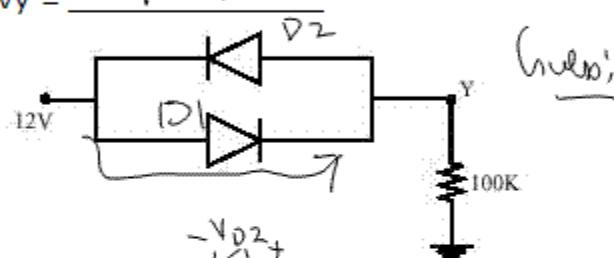
$$= -1mA + -0.5mA + -2mA$$

$i_m = -3.5mA$

## (2) Diodes

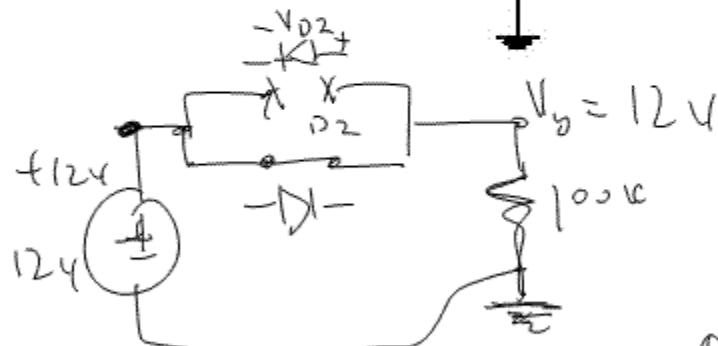
(From 1999 Fall 99, MT2, HKW Online Exams website)  
 (http://hkn.eecs)

b)  $V_y =$



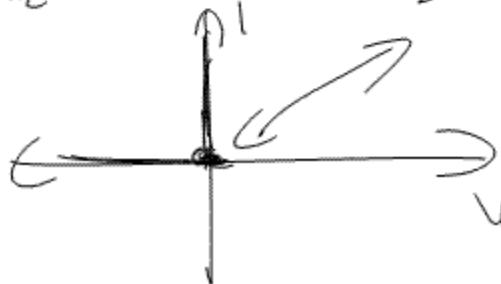
(Ans:)

D1 on,  
 D2 off ] Current from 12V  
 flows left to right

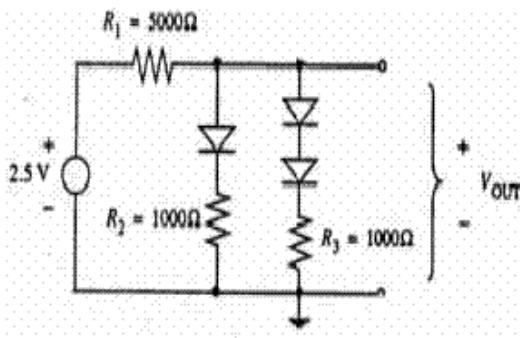


(Q: Is D2 off?)

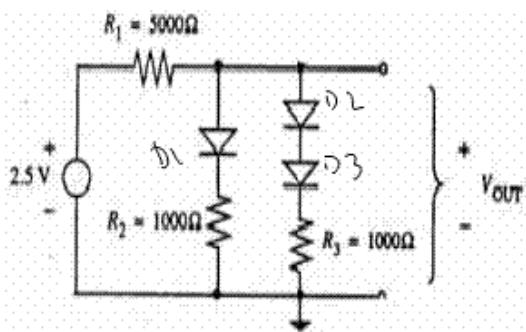
$$V_{D2} = 0 \text{ V}$$



(3) EE40, mi #2, Sp. 98.



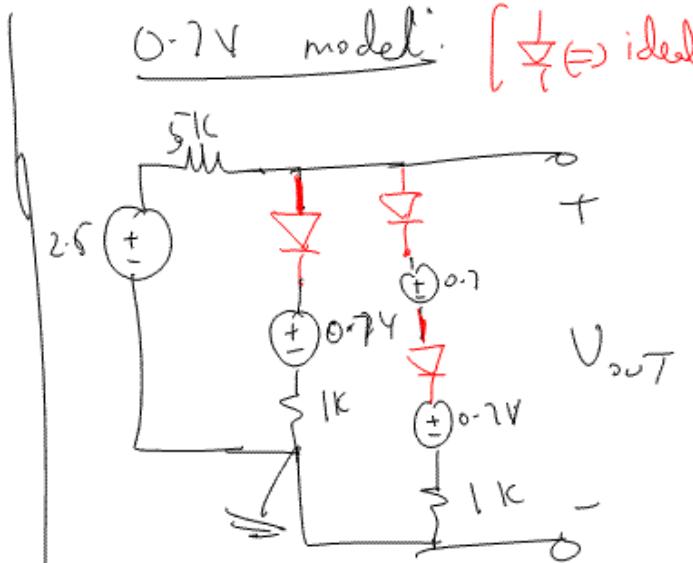
IDR



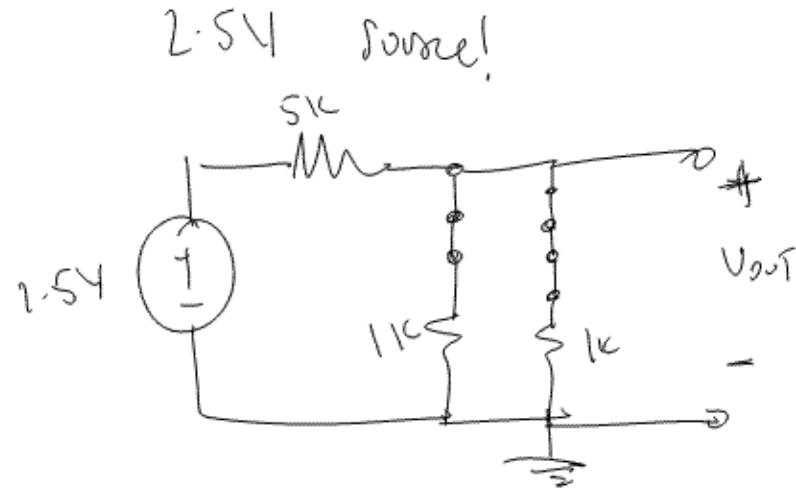
Gives: All diodes are on  $\rightarrow$   
because of current flow

(Q.) Find  $V_{out}$ . Let's use the ideal  
diode model & 0.7V model.

0.7V model: [ $\frac{1}{\text{---}} \Rightarrow$  ideal diode]



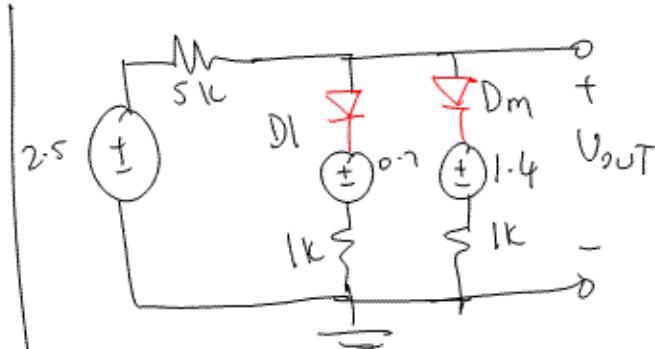
Simplify



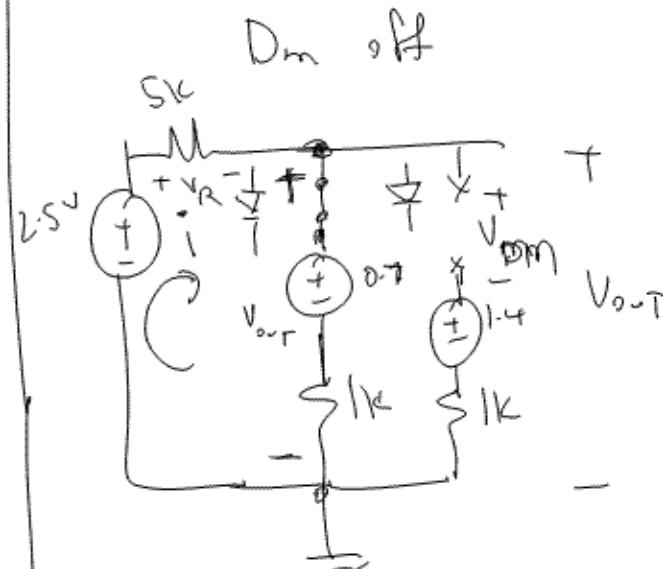
$$V_{out} = \left( \frac{500\text{ }\Omega}{500 + 500} \right) 2.5 \left[ \frac{1\text{ k}\Omega || 1\text{ k}\Omega}{V_{out} \text{ (negative)}} \right]$$

Voltage divider

$$= \frac{500}{500+500} 2.5 = \frac{2.5}{11} \approx 0.23\text{ V}$$



Assume D1 on,



kV around loop 1:  
 $2.5 - i(5k) - 0.7 - i(1k) = 0$

$$\Rightarrow i = \frac{2.5 - 0.7}{6k} = \frac{1.8V}{6k} = 0.3mA$$

$$\therefore 2.5 - V_{out} = V_R \Rightarrow V_{out} = 2.5 - (0.3 \text{ mA})(5k)$$

$V_{out} = 1.0V$

$$V_{DM} = V_{out} - 1.44V = -0.44V$$