

Lecture 23: MOS Model and Common Emitter Amp

• **Announcements:**

- HW#7 online and due Friday via Gradescope
- Lab#5 online (this is your first project)
 - ↳ Due Tuesday, Oct. 30, 5 p.m.

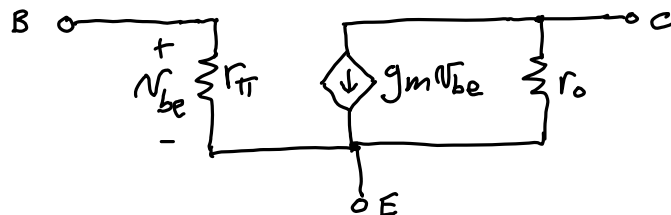
• **Lecture Topics:**

- ↳ T Model
- ↳ Pnp Transistor Hybrid- π Model
- ↳ Saturated NMOS Hybrid- π Model
- ↳ Example: Common Emitter Amplifier

• **Last Time:**

- Derived the BJT Hybrid- π Model
- Now, continue with models ...

Hybrid- π Model Summary (for npn BJT)



$$r_{\pi} = \frac{\beta}{g_m} = \frac{V_T}{I_B}$$

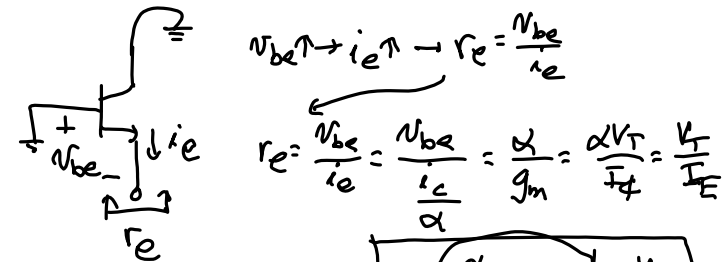
$$g_m = \frac{I_C}{V_T}$$

$$r_o = \frac{V_A}{I_C}$$

• **Remarks:**

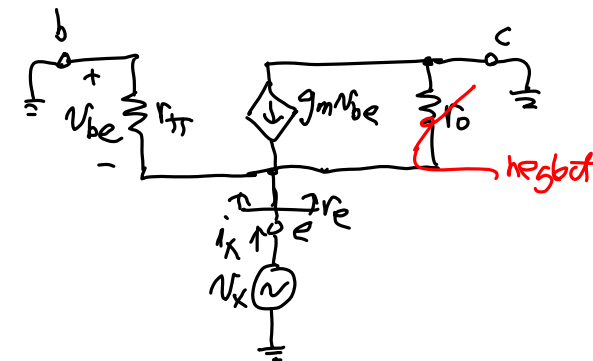
- g_m is independent of device specifics, i.e., β , I_s
- Depends only on temperature (via V_T) and biasing (I_C)
- Small-signal model valid for $v_{be} \ll V_T$

What about emitter resistance?



Why is this not included in the hybrid- π model?

↳ well... it is!



$$i_x = -\frac{v_{be}}{r_{\pi}} - g_m v_{be}$$

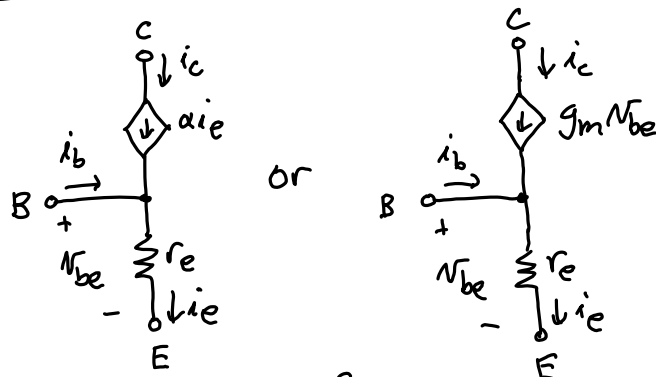
$$(v_x = -v_{be}) \Rightarrow i_x = v_x \left(\frac{1}{r_{\pi}} + g_m \right)$$

$$r_e = \frac{v_x}{i_x} = \frac{1}{\frac{1}{r_{\pi}} + g_m} = \frac{r_{\pi}}{1 + g_m r_{\pi}} = \frac{r_{\pi}}{1 + \beta} = \frac{\beta}{g_m(1 + \beta)}$$

$$r_e = \frac{\alpha}{g_m} \quad \checkmark$$

- To explicitly show the emitter resistance in the small-signal model, use the T-model:

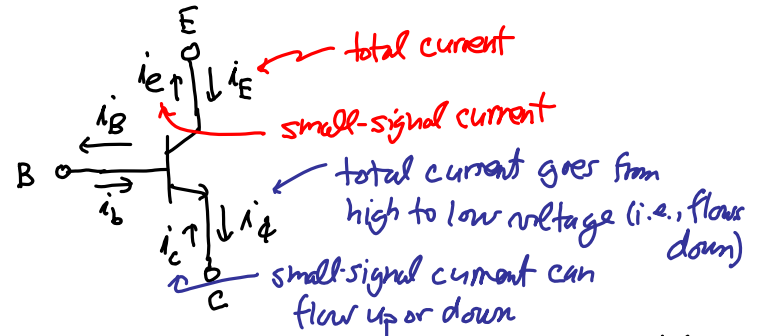
T-Model (Common Base Model)



Where (as before): $g_m = \frac{I_C}{V_T}$
 $r_e = \frac{V_T}{I_E} = \frac{\alpha}{g_m}$

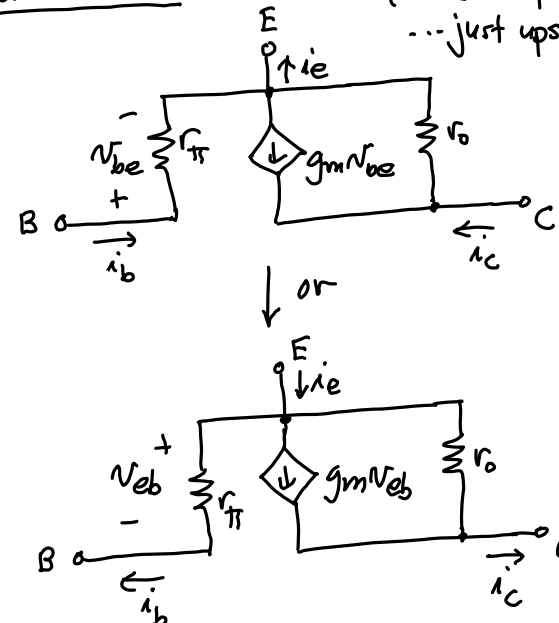
Small-Signal Models for Forward-Active pnp Transistor

- For pnp transistors, use the same small-signal models as npn with NO change in polarities



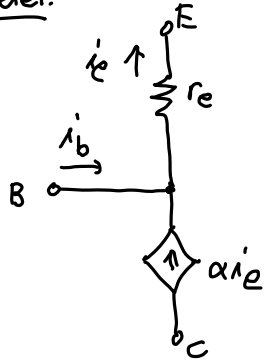
Hybrid- π Model:

(same as npn model
 ... just upside down)



(again, same as npn model,
 but upside down)

T-Model:



- Note that in the above small-signal models, the current directions are the same as used for npn, i.e., no change in polarities
- Large signal directions, however, are as before

Need Proof?

$$i_b = I_s \exp\left(\frac{V_{BE}}{V_T}\right)$$

$$I_C - i_c = I_s \exp\left(\frac{V_{BE} - V_{be}}{V_T}\right)$$

$$= I_s \exp\left(\frac{V_{BE}}{V_T}\right) \exp\left(-\frac{V_{be}}{V_T}\right) = I_b \exp\left(-\frac{V_{be}}{V_T}\right)$$

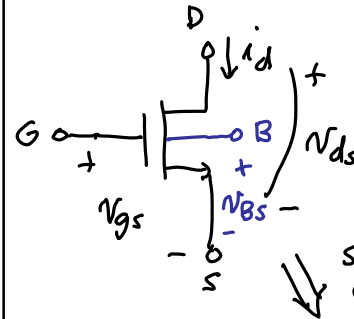
(2 terms of Taylor expansion) $\Rightarrow I_C - i_c = I_b \left(1 - \frac{V_{be}}{V_T}\right) = I_b - \frac{I_b}{V_T} V_{be}$

$$i_c = \frac{I_b}{V_T} V_{be} \quad \therefore g_m = \frac{i_c}{V_{be}} = \frac{I_b}{V_T}$$

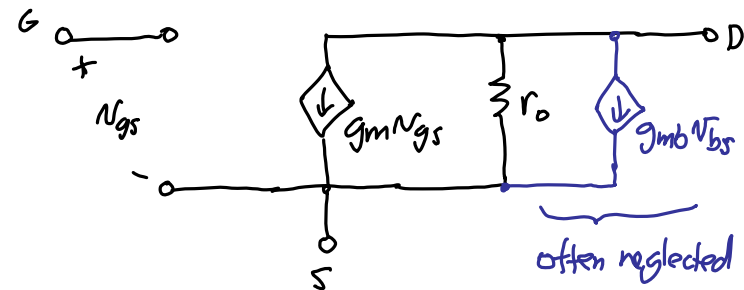
(as with npn, and with the same directions as npn)

Small-Signal Model for Saturated NMOS Transistor

for analog applications



small-signal equivalent ckt.



often neglected

where:

$$g_m = \frac{i_d}{v_{gs}} = \left. \frac{\partial i_D}{\partial v_{GS}} \right|_{Q \text{ pt}} = \left. \frac{\partial}{\partial v_{GS}} \left(\frac{1}{2} \mu_n C_{ox} \frac{W}{L} (v_{GS} - v_{TN})^2 \right) \right|_{Q \text{ pt}}$$

$$= \mu_n C_{ox} \frac{W}{L} (v_{GS} - v_{TN}) \Big|_{v_{GS} = v_{GS}}$$

$$\therefore g_m = \mu_n C_{ox} \frac{W}{L} (v_{GS} - v_{TN}) = \sqrt{2 \mu_n C_{ox} \frac{W}{L} I_D}$$

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (v_{GS} - v_{TN})^2$$

$$(v_{GS} - v_{TN}) = \sqrt{\frac{2 I_D}{\mu_n C_{ox} \frac{W}{L}}} = v_{ov} \triangleq \text{Overdrive Voltage}$$

$$r_o = \frac{v_{ds}}{i_d} = \left[\frac{\partial i_D}{\partial v_{DS}} \Big|_{Q\text{-pt.}} \right]^{-1}$$

$$= \left[\frac{\partial}{\partial v_{DS}} \left(\frac{1}{2} \mu_n C_{ox} \frac{W}{L} (v_{GS} - V_{TN})^2 (1 + \lambda v_{DS}) \right) \Big|_{Q\text{-pt.}} \right]^{-1}$$

$$= \left[i_D \lambda \Big|_{i_D = I_D} \right]^{-1} = [I_D \lambda]^{-1}$$

$$\Rightarrow r_o = \frac{1}{\lambda I_D}$$

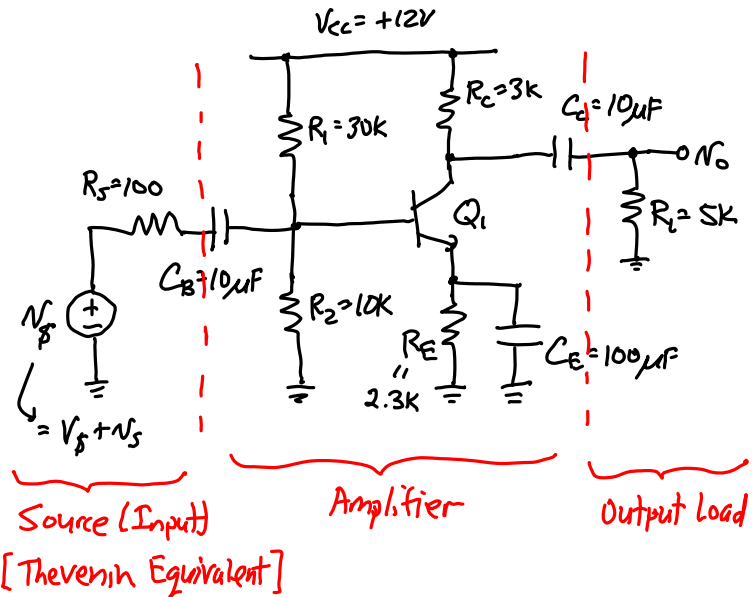
Threshold Voltage $V_t = f(V_{BS})$:

\Rightarrow gives rise to a Body effect transconductance:

$$g_{mb} = \frac{\partial i_D}{\partial v_{BS}} \Big|_{Q\text{-pt.}} = \frac{g_m \theta}{2\sqrt{2\phi_f + V_{SB}}} = g_{mb}$$

Procedure for Small-Signal Analysis

• (Common Emitter Example)



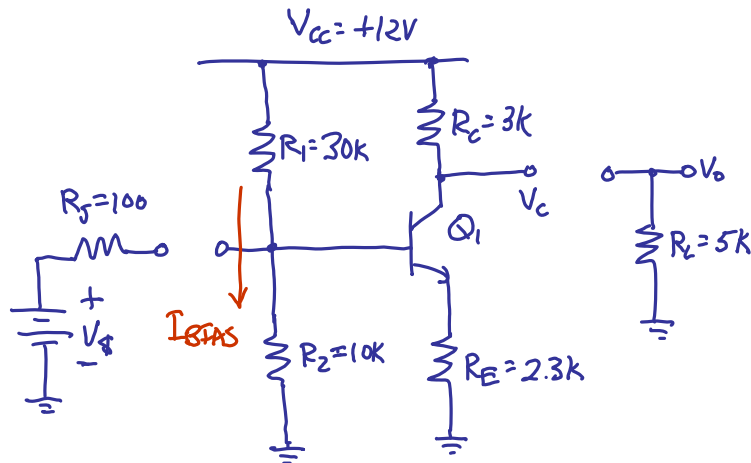
For Q : $\beta = 100$, $V_A = 100V$

Find the voltage gain, $\frac{v_o}{v_s}$.

Draw the collector voltage waveform for

$$v_s = \underbrace{(0.014) \cos \omega_0 t}_{\text{Ac small-signal component}} + \underbrace{1V}_{\text{DC component}}$$

- ① Determine the DC operating point
- i.e., find the relevant DC voltages at all nodes and DC currents through all branches
 - Draw the DC circuit
 - ↳ Eliminate independent AC small-signal sources
 - Short AC voltage sources
 - Open AC current sources
 - ↳ Open all capacitors (in particular, open the bypass/coupling capacitors)
 - ↳ Use DC transistor models
 - this might entail nonlinearity in some cases, but approximations can alleviate



Accurate (but slow):

⇒ Get Thevenin equivalent:

$$V_{BB} = V_{CC} \left(\frac{R_2}{R_1 + R_2} \right) = (12) \left(\frac{30k}{40k} \right) = 3V$$

$$R_{BB} = 10k \parallel 30k = 7.5k$$

$$I_C = \frac{V_{BB} - V_{BE}}{\frac{R_{FE}}{\alpha} + \frac{R_{BB}}{\beta}} = \frac{3 - 0.7}{2.3k + \frac{7.5k}{100}} = 0.97mA \approx 1mA$$

$$I_B = \frac{I_C}{\beta} = 0.01mA$$

$$\therefore I_E \approx 0.97mA \approx 1mA$$

$$V_B = V_{BB} - R_B I_B = 3 - (7.5k)(0.01mA) = 2.92V$$

$$\therefore V_E = 2.92 - 0.7 = 2.22V$$

$$V_C = V_{CC} - I_C R_C = 12 - 3 = 9V$$

Faster Way:

$$\text{Ignore } I_B \rightarrow V_B = V_{CC} \left(\frac{R_2}{R_1 + R_2} \right) = 3V$$

$$V_E = V_B - V_{BE(on)} = 3 - 0.7 = 2.3V$$

$$\therefore I_E = \frac{V_E}{R_E} = \frac{2.3}{2.3k} = 1mA = I_C$$

$$I_B = \frac{I_C}{\beta} = \frac{1mA}{100} = 0.01mA$$

$$V_C = V_{CC} - I_C R_C = 9V$$

$$I_{B(BIAS)} = \frac{V_{CC}}{R_1 + R_2} = \frac{12}{40k} = 0.3mA > 10 I_B \checkmark$$

For stable bias pt.