

Lecture 24: Common Emitter Amplifier

- **Announcements:**
- HW#7 online and due Friday via Gradescope
- Lab#5 online (this is your first project)
 - ↳ Due Tuesday, Oct. 30, 5 p.m.
- Shortly after lecture last time, my hard drive died
 - ↳ Lost the PowerPoint and video
 - ↳ Rewrote the PowerPoint - this is online
 - ↳ Looking for a video from ETS ... we'll see
- Next Monday: No lecture
 - ↳ I will be on travel (as indicated in the schedule shown on the first day)
 - ↳ Lecture will be by video

 • **Lecture Topics:**

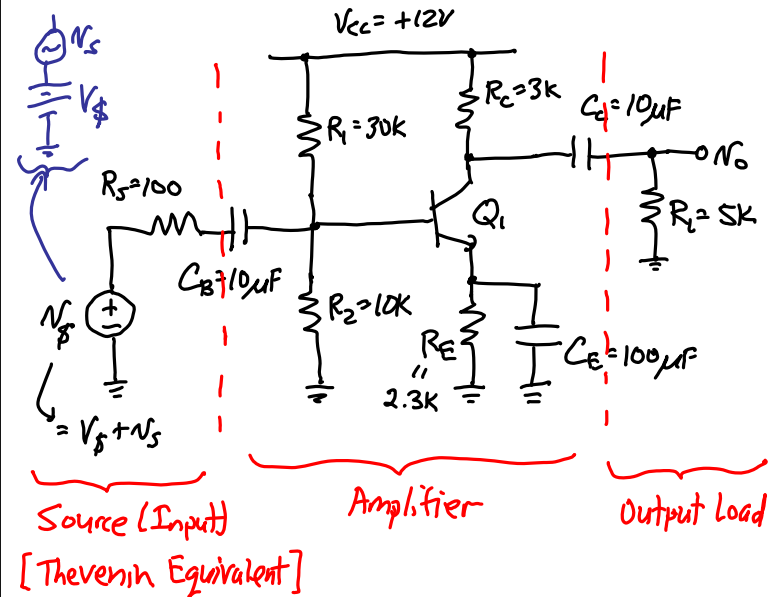
- ↳ Example: Common Emitter Amplifier
- ↳ Frequency Response

 • **Last Time:**

- Going through a Common Emitter Amplifier small-signal analysis example
- Now, continue with this ...

Procedure for Small-Signal Analysis

- (Common Emitter Example)



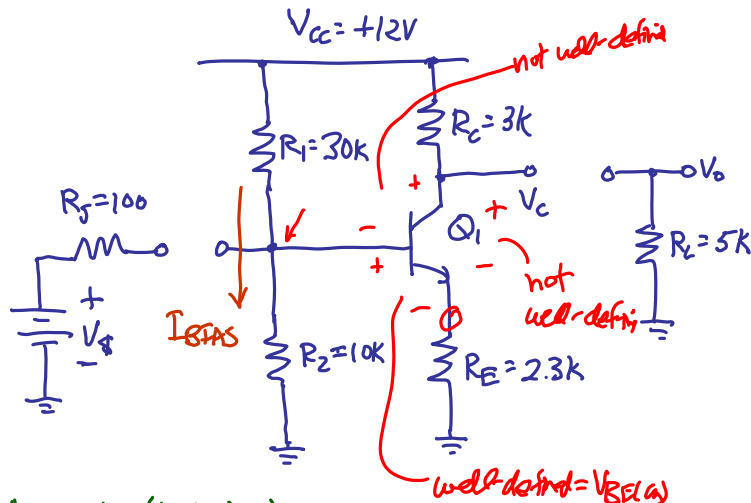
For Q: $\beta = 100$, $V_A = 100V$

Find the voltage gain, $\frac{V_o}{V_s}$.

Draw the collector voltage waveform for

$$V_s = \underbrace{(0.014) \cos \omega_0 t}_{\text{Ac small-signal component}} + \underbrace{1V}_{\text{DC component}}$$

- ① Determine the DC operating point
- i.e., find the relevant DC voltages at all nodes and DC currents through all branches
 - Draw the DC circuit
 - ↳ Eliminate independent AC small-signal sources
 - Short AC voltage sources
 - Open AC current sources
 - ↳ Open all capacitors (in particular, open the bypass/coupling capacitors)
 - ↳ Use DC transistor models
 - this might entail nonlinearity in some cases, but approximations can alleviate



Accurate (but slow):

⇒ Get Thevenin equivalent:

$$V_{BB} = V_{CC} \left(\frac{R_2}{R_1 + R_2} \right) = (12) \left(\frac{30k}{40k} \right) = 3V$$

$$R_{BB} = 10k || 30k = 7.5k$$

$$I_C = \frac{V_{BB} - V_{BE}}{\frac{R_{BB}}{\alpha} + \frac{R_E}{\beta}} = \frac{3 - 0.7}{2.3k + \frac{7.5k}{100}} = 0.97mA \approx 1mA$$

$$I_B = \frac{I_C}{\beta} = 0.01mA$$

$$\therefore I_E \approx 0.97mA \approx 1mA$$

$$V_B = V_{BB} - R_B I_B = 3 - (7.5k)(0.01mA) = 2.92V$$

$$\therefore V_E = 2.92 - 0.7 = 2.22V$$

$$V_C = V_{CC} - I_C R_C = 12 - 3 = 9V$$

Faster Way:

$$\text{Ignore } I_B \rightarrow V_B = V_{CC} \left(\frac{R_2}{R_1 + R_2} \right) = 3V$$

$$V_E = V_B - V_{BE(on)} = 3 - 0.7 = 2.3V$$

$$\therefore I_E = \frac{V_E}{R_E} = \frac{2.3}{2.3k} = 1mA = I_C$$

$$I_B = \frac{I_C}{\beta} = \frac{1mA}{100} = 0.01mA$$

$$V_C = V_{CC} - I_C R_C = 9V$$

$$I_{B(DS)} = \frac{V_{CC}}{R_1 + R_2} = \frac{12}{40k} = 0.3mA > 10 I_B \checkmark$$

For stable bias pt.

② Determine the elements in the small-signal transistor model(s)

↳ If more than one transistor, might need to determine SS element values for several of them

$$g_m = \frac{I_E}{V_T} = \frac{1\text{m}}{25\text{m}} = 0.04\text{S} \quad r_o = \frac{V_A}{I_E} = \frac{100}{1\text{m}} = 100\text{k}\Omega$$

$$r_{\pi} = \frac{\beta}{g_m} = \frac{100}{0.04} = 2.5\text{k}\Omega \quad r_e = \frac{\alpha}{g_m} \approx \frac{1}{g_m} = 25\Omega$$

I_E gives all of these!

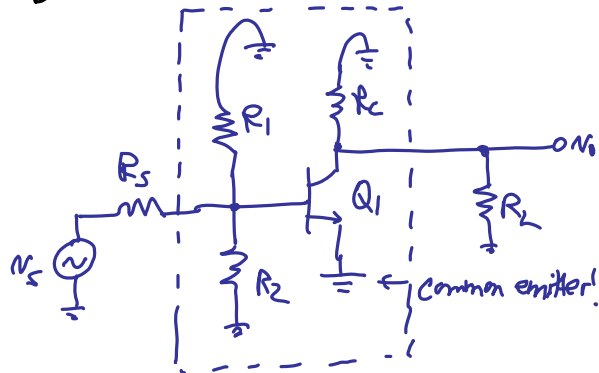
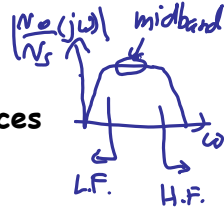
③ Obtain the small-signal circuit

↳ Eliminate independent DC sources

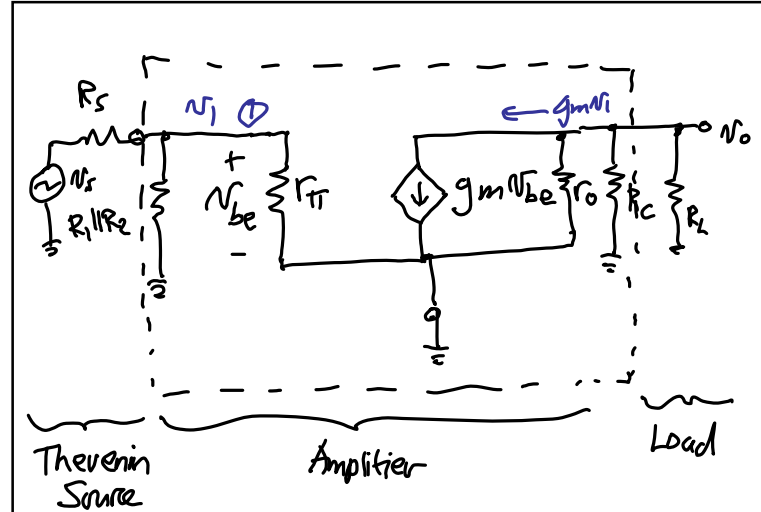
- Short DC voltage sources
- Open DC current sources

↳ Short large coupling capacitors ($C's > 10\text{ nF}$)

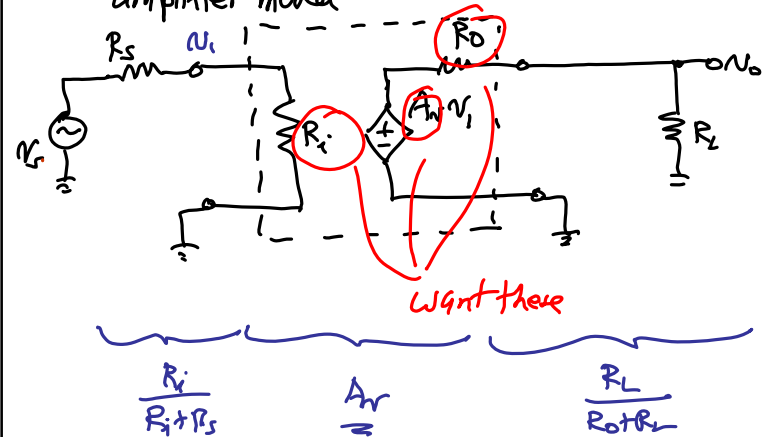
↳ Use small-signal transistor models



↓ replace the transistor w/ S.S. model



⇒ would like to get this in the form of our simple amplifier model



Want these

$$\frac{R_i}{R_i + R_s} \quad A_v \quad \frac{R_L}{R_o + R_L}$$

④ Use standard circuit analysis (i.e., KCL or KVL with superposition) to determine the parameters of interest

• Usually, the parameters of interest include

- ↳ Gain, A_v
- ↳ Input Resistance, R_i
- ↳ Output Resistance, R_o
- ↳ Low Frequency Cut-off, ω_b
- ↳ High Frequency Cut-off, ω_h

• Determine all of these during small-signal analysis
• The total gain of the simplified amplifier circuit takes the form

$$\frac{V_o}{V_s} = \frac{R_i}{R_i + R_s} A_v \frac{R_L}{R_L + R_o}$$

For ideal work: $R_i = \infty$ $R_o = 0$
($R_i \gg R_s$) ($R_o \ll R_L$)

Amplifier Gain

$$A_v = \frac{V_o}{V_i} \Big|_{R_L = \infty}$$

(as $i_o = 0$)

$$V_o = -(g_m v_i)(r_o \parallel R_c) \Rightarrow A_v = \frac{V_o}{V_i} \Big|_{R_L = \infty} = -g_m(r_o \parallel R_c) = A_{vT}$$

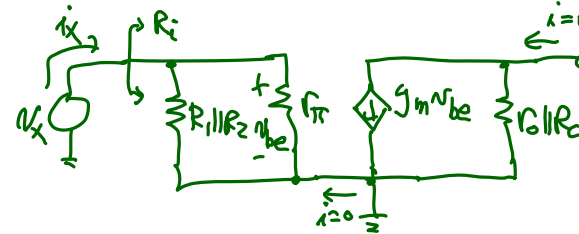
$$A_v \approx -g_m R_c = -120$$

$r_o \gg R_c$

\Rightarrow max gain occur when $R_c = \infty$:

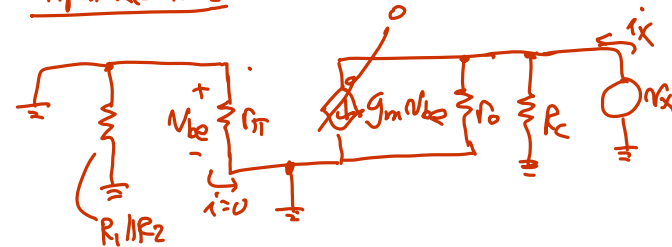
$$\text{Max Gain} = -g_m r_o = -\frac{I_c}{V_T} \frac{V_A}{I_c} = -\frac{V_A}{V_T} = \text{Max Gain}$$

Input Resistance



$$R_i = \frac{v_x}{i_x} = r_{\pi} \parallel R_1 \parallel R_2 = 1875 \Omega$$

Output Resistance



$$R_o = \frac{v_x}{i_x} = r_o \parallel R_c \approx R_c = 3 \text{ k}\Omega$$

$[r_o \gg R_c]$

For the Total Ckt:

$$\begin{aligned} \text{Gain} &= \frac{V_o}{V_s} = \frac{R_i}{R_i + R_s} A_v \frac{R_L}{R_L + R_o} \\ &= \frac{r_{\pi} \| R_1 \| R_2}{r_{\pi} \| R_1 \| R_2 + R_s} (-g_m R_c) \left(\frac{R_L}{R_L + R_c} \right) \\ &= - \frac{r_{\pi} \| R_1 \| R_2}{r_{\pi} \| R_1 \| R_2 + R_s} g_m (R_c \| R_L) \\ &= - \frac{1875}{1875 + 100} (0.04) (3k \| 5k) \\ &= \boxed{-71.2 = \frac{V_o}{V_s}} \end{aligned}$$