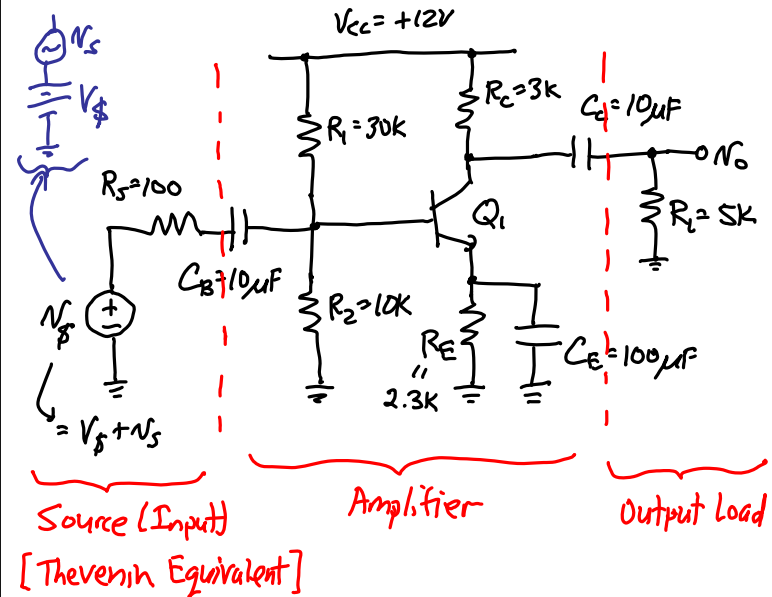


Lecture 25: High Frequency Small-Signal Models

- **Announcements:**
 - HW#8 online and due Friday via Gradescope
 - Lab#5 due Tuesday, Oct. 30, 5 p.m.
 - Monday's lecture as recorded by ETS is online in your CalCentral EE105 course page
 - ↳ Good news!
 - ↳ Remember, this is the lecture that I lost due to a hard drive failure
 - ↳ You'll need to turn your sound volume up
 - Next Monday: No lecture
 - ↳ I will be on travel (as indicated in the schedule shown on the first day)
 - ↳ Lecture will be by video
-
- **Lecture Topics:**
 - ↳ Finish Common Emitter Amplifier
 - ↳ Frequency Response
 - ↳ High Frequency Model for BJT
-
- **Last Time:**
 - Going through a Common Emitter Amplifier small-signal analysis example
 - Now, continue with this ...

Procedure for Small-Signal Analysis

- (Common Emitter Example)



For Q: $\beta = 100$, $V_A = 100V$

Find the voltage gain, $\frac{V_o}{V_s}$.

Draw the collector voltage waveform for

$$V_s = \underbrace{(0.014) \cos \omega_0 t}_{\text{Ac small-signal component}} + \underbrace{1V}_{\text{DC component}}$$

② Determine the elements in the small-signal transistor model(s)

↳ If more than one transistor, might need to determine SS element values for several of them

$$g_m = \frac{I_C}{V_T} = \frac{1\text{m}}{25\text{m}} = 0.04\text{S} \quad r_o = \frac{V_A}{I_C} = \frac{100}{1\text{m}} = 100\text{k}\Omega$$

$$r_{\pi} = \frac{\beta}{g_m} = \frac{100}{0.04} = 2.5\text{k}\Omega \quad r_e = \frac{\alpha}{g_m} \approx \frac{1}{g_m} = 25\Omega$$

I_C gives all of these!

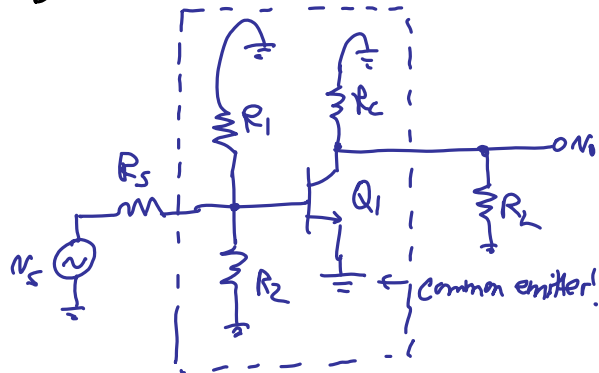
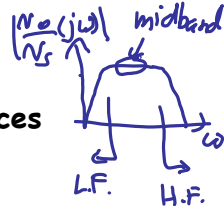
③ Obtain the small-signal circuit

↳ Eliminate independent DC sources

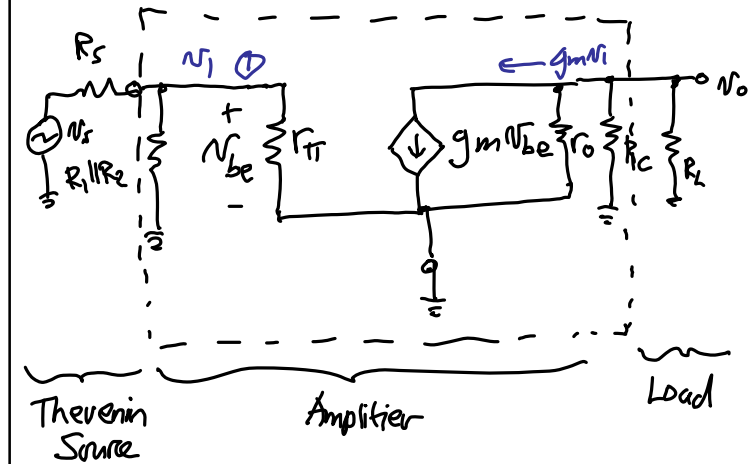
- Short DC voltage sources
- Open DC current sources

↳ Short large coupling capacitors ($C's > 10\text{ nF}$)

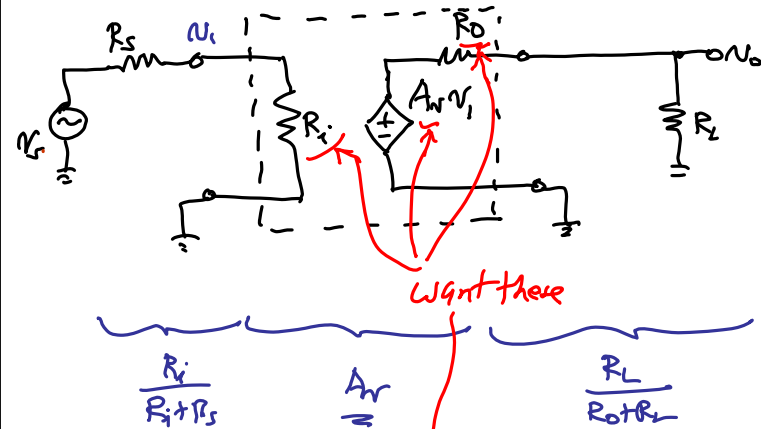
↳ Use small-signal transistor models



↓ replace the transistor w/ S.S. model



⇒ would like to get this in the form of our simple amplifier model



Specifically, want values for the amplifier in the box, i.e., w/ $R_s = 0$ & $R_L = \infty$.

④ Use standard circuit analysis (i.e., KCL or KVL with superposition) to determine the parameters of interest

• Usually, the parameters of interest include

- ↳ Gain, A_v
- ↳ Input Resistance, R_i
- ↳ Output Resistance, R_o
- ↳ Low Frequency Cut-off, ω_b
- ↳ High Frequency Cut-off, ω_h

• Determine all of these during small-signal analysis

• The total gain of the simplified amplifier circuit takes the form

$$\frac{V_o}{V_s} = \frac{R_i}{R_i + R_s} \overset{\text{gain of dkt. in the box (i.e., not including } R_s \text{ or } R_L)}{A_v} \frac{R_L}{R_L + R_o}$$

For ideal work: $R_i = \infty$ $R_o = 0$
 ($R_i \gg R_s$) ($R_o \ll R_L$)

Amplifier Gain - (for the dkt. in the box)

$$A_v = \frac{V_o}{V_i} \Big|_{R_L = \infty} \quad (\text{as } i_o = 0)$$

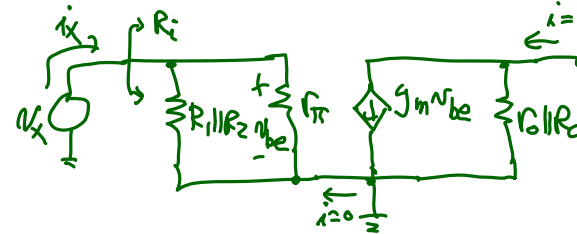
$$V_o = -(g_m V_i)(r_o \parallel R_c) \Rightarrow A_v = \frac{V_o}{V_i} \Big|_{R_L = \infty} = -g_m(r_o \parallel R_c) = A_{vT}$$

$$A_v \approx -g_m R_c = -120 \quad (r_o \gg R_c)$$

\Rightarrow max gain occur when $R_c = \infty$:

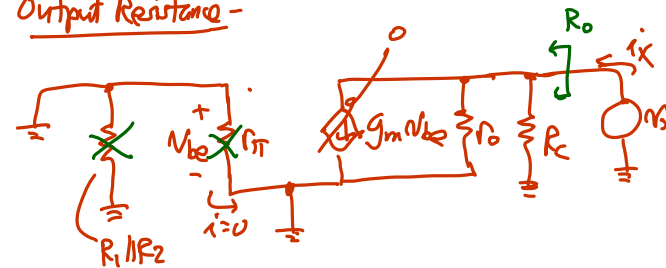
$$\text{Max Gain} = -g_m r_o = -\frac{I_c V_A}{V_T I_q} = -\frac{V_A}{V_T} = \text{Max Gain}$$

Input Resistance -



$$R_i = \frac{V_x}{i_x} = r_{\pi} \parallel R_1 \parallel R_2 = 1875 \Omega$$

Output Resistance -



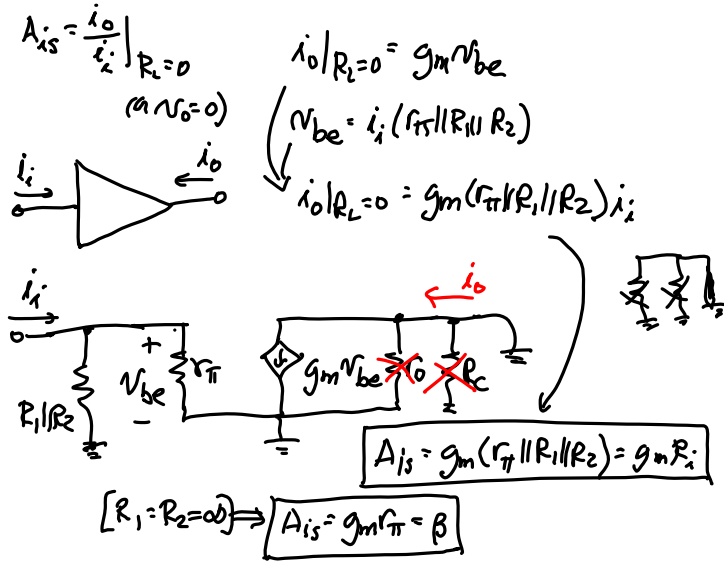
$$R_o = \frac{V_x}{i_x} = r_o \parallel R_c \approx R_c = 3 \text{ k}\Omega \quad (r_o \gg R_c)$$

For the Total Ckt:

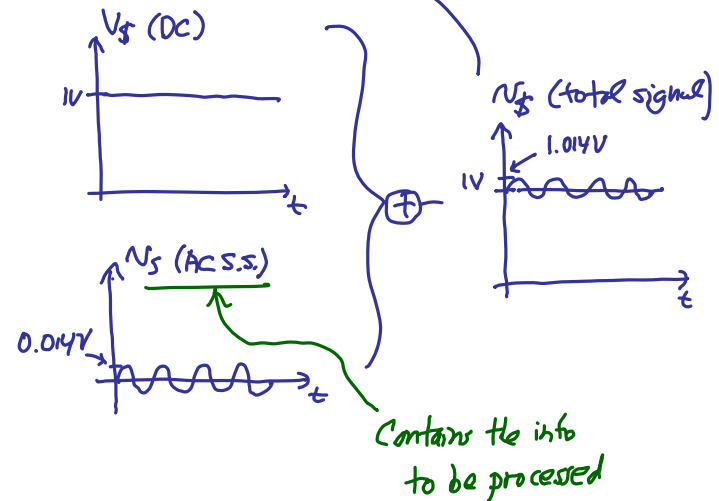
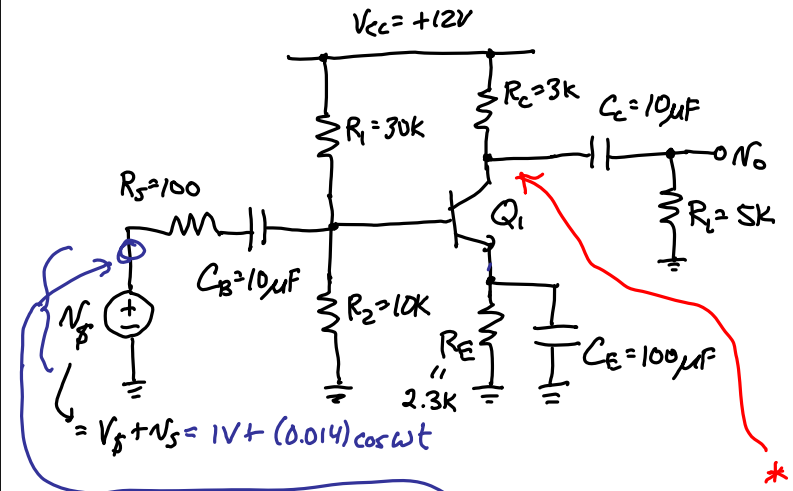
$$\begin{aligned}
 \text{Gain} &= \frac{V_o}{V_s} = \frac{R_i}{R_i + R_s} A_v \frac{R_L}{R_L + R_o} \\
 &= \frac{r_{\pi} || R_1 || R_2}{r_{\pi} || R_1 || R_2 + R_s} (-g_m R_c) \left(\frac{R_L}{R_L + R_c} \right) \\
 &= - \frac{r_{\pi} || R_1 || R_2}{r_{\pi} || R_1 || R_2 + R_s} g_m (R_c || R_L) \\
 &= - \frac{1875}{1875 + 100} (0.04) (3k || 5k) \\
 &= \boxed{-71.2 = \frac{V_o}{V_s}}
 \end{aligned}$$

⇒ just feedbacks...

Short Ckt. Current Gain for the C.E. Amp - (ckt. in the box)



- ⑤ Use the gain and resistances determined above to obtain the small-signal voltages and currents at each node/branch of the circuit
- To obtain the actual node & branch signals, superpose the DC and small-signal AC solutions



Set amplification properties, e.g., R_i, R_o, A_v, f_H

* $V_{\phi}(DC)$

9V

Contains the information

$N_c(AC\ s.s.)$

1V

$N_c = N_s \left(\frac{N_o}{N_s} \right) = (0.014)(-71.2) \cos \omega t = -(1V) \cos \omega t$

10V
9V

N_{ϕ} (total signal)

What happens from a device perspective?

$V_{ce} = V_{cc} - I_c(R_c + R_e)$

$V_{be} = V_{be0} + N_{be}$

ac sinusoid

i_c

Slope = g_m

V_{ce0}

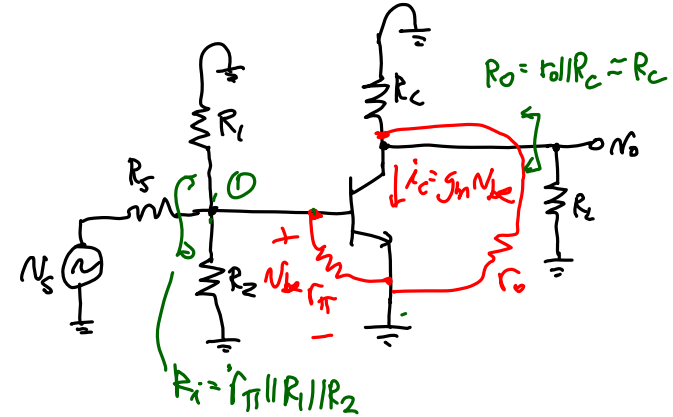
V_{be0}

$V_{ce} = V_{cc} - I_c(R_c + R_e)$

N_{ce}

An Inking of Inspection Analysis

= for fast s.s. ckt analysis:



$$\frac{N_o}{N_s} = \frac{N_D}{N_s} \cdot \frac{N_o}{N_D} = \frac{r_{\pi} || R_1 || R_2}{r_{\pi} || R_1 || R_2 + R_s} (-g_m (r_o || R_c))$$

...but more later...

Frequency Response of Amplifiers

General Form: $A(s) = A_M F_L(s) F_H(s)$

Where $A_M =$ midband gain (constant w/ freq.)

$F_L(s)$ models the low frequency behavior generally governed by coupling capacitors:

For $\omega \rightarrow 0$: $F_L(s) \rightarrow 0$
 $\omega \rightarrow \infty$: $F_L(s) \rightarrow 1$ } It's a HPF

$F_H(s)$ models the high frequency behavior governed by parasitic capacitors (often inside the transistor):

For $\omega \rightarrow 0$: $F_H(s) \rightarrow 1$
 $\omega \rightarrow \infty$: $F_H(s) \rightarrow 0$ } It's a LPF

High Frequency Hybrid- π Model

$r_b \triangleq$ base resistance

action happens here

Capacitor

Linear Capacitor

$q = CV$

Nonlinear Capacitor

$q = f(v)$

generalize

C_{μ} - Base-to-Collector Capacitance

From before:

- The depletion region width x_d is a function of the applied reverse-bias voltage v_{CB} : $x_d = f(v_{CB})$
- $Q = qN_d x_d A$, where A = cross sectional area
- Since Q is a function of x_d , it is also a function of v_{CB} : $Q = g(x_d) = g[f(v_{CB})]$

In general, for a pn junction:

$$C_j = \frac{dQ}{dv_R} = \frac{C_{j0}}{\sqrt{1 + \frac{v_R}{\phi_j}}} ; C_{j0} = \frac{\epsilon_s A}{x_{d0}} @ v_R = 0V$$

C_{μ} is a junction capacitance:

$$C_{\mu} = \frac{C_{\mu 0}}{\sqrt{1 + \frac{v_{CB}}{\phi_j}}} \quad (\text{for an abrupt junction})$$

Where $C_{\mu 0}$ = capacitance for $v_{CB} = 0V$
 ϕ_j = built-in potential between p & n type semiconductors
 $= \frac{kT}{q} \ln \left(\frac{N_{A8} N_{dC}}{n_i^2} \right) ; n_i = 1.45 \times 10^{10} \text{ cm}^{-3}$

More generally: $C_{\mu} = \frac{C_{\mu 0}}{\left(1 + \frac{v_{CB}}{\phi_j}\right)^m}$; m = fcn of junction interface

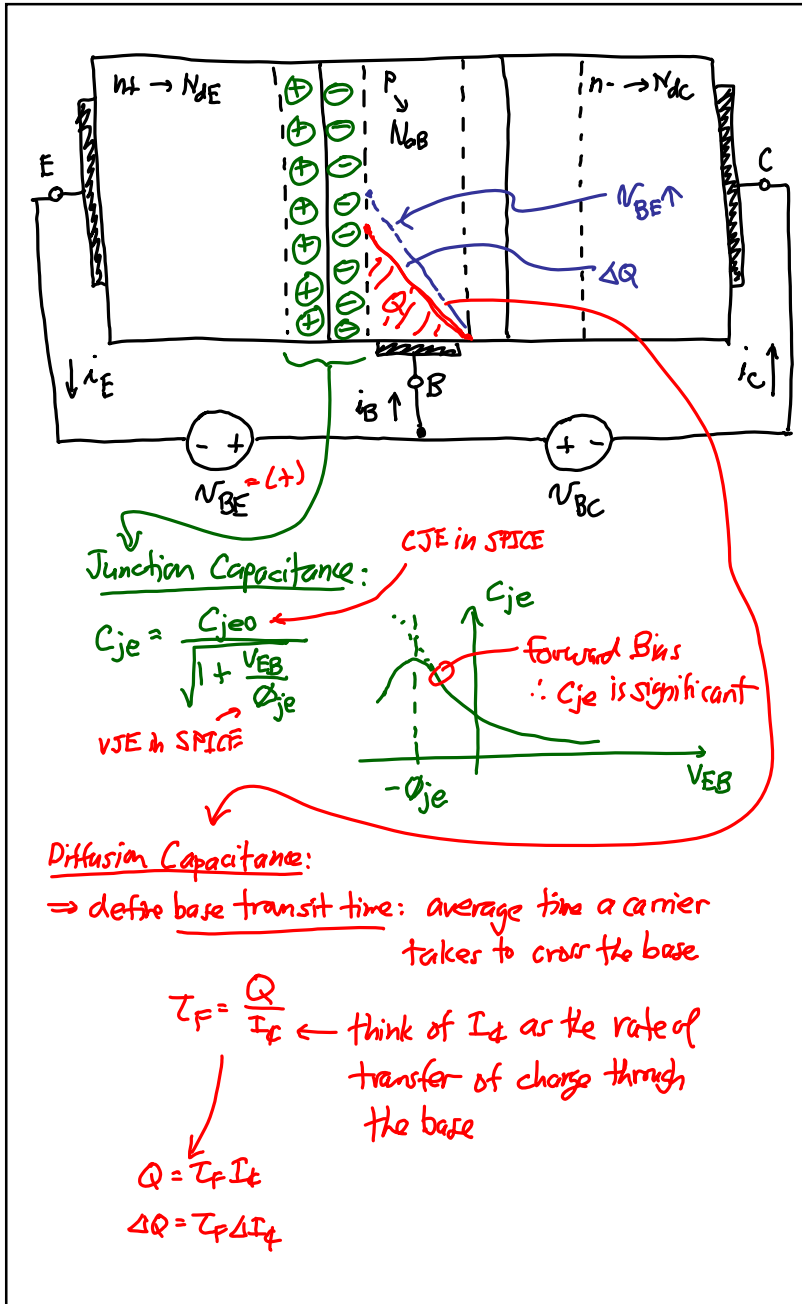
CJC in SPICE (pointing to $C_{\mu 0}$)
MJC in SPICE (pointing to m)
VJC in SPICE (pointing to ϕ_j)

We assume this $\rightarrow (= \frac{1}{2}$ for abrupt)

C_{π} - Base-to-Emitter Capacitance

\Rightarrow two components comprise C_{π} :

- Junction capacitance, C_{je}
- Diffusion capacitance, C_b



\Rightarrow switch to small-signal parameters:

$$q = \tau_F i_c$$

$$C_b = \frac{q}{V_{be}} = \tau_F \frac{i_c}{V_{be}} = \tau_F g_m = \tau_F \frac{I_C}{V_T} = C_b$$

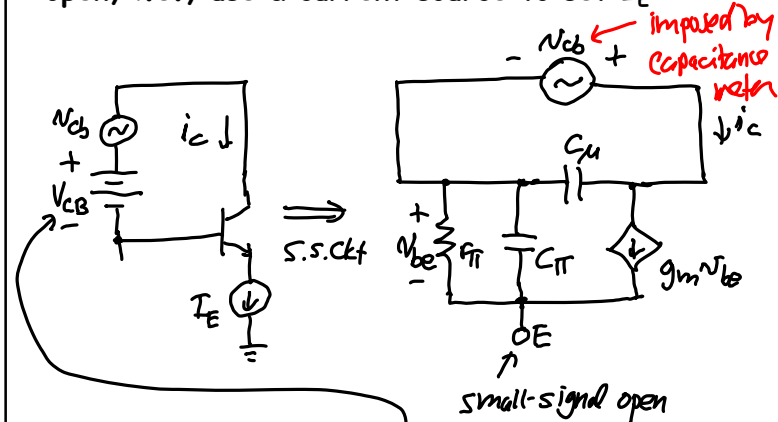
$$\therefore C_b \propto I_C$$

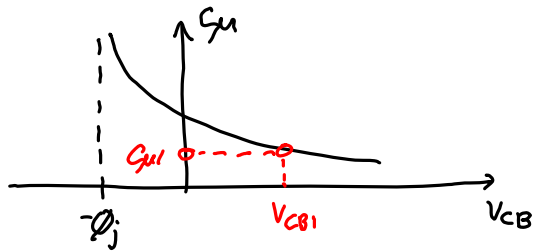
Putting it all together:

$$C_{\pi} = C_b + C_{je} = \tau_F g_m + \frac{C_{je0}}{\sqrt{1 + \frac{V_{BE}}{\phi_{je}}}}$$

Determining C_{π} and C_{μ}

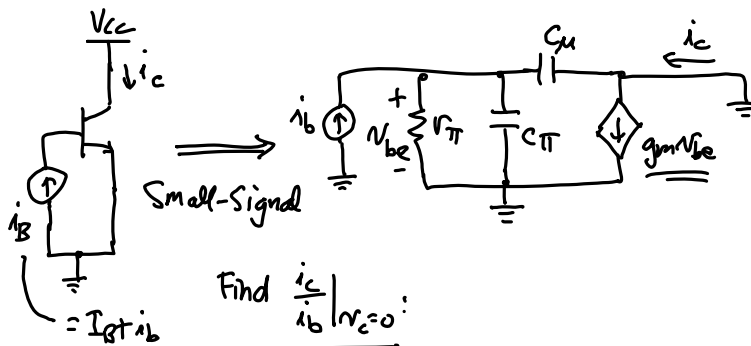
- Can experimentally determine C_{μ} by measuring the small-signal capacitance between the base and collector terminals with the emitter incrementally open, i.e., use a current source to set I_E





- To find C_π , find an expression for C_π in terms of C_μ and known measurable parameters
- One parameter we can conveniently measure is the short-circuit current gain:

$$h_{fe} = \left. \frac{i_c}{i_b} \right|_{N_c=0}$$



$$v_{be} = i_b \left(r_\pi \parallel \frac{1}{sC_\pi} \parallel \frac{1}{sC_\mu} \right) \quad [g_m \gg sC_\mu]$$

$$i_c = g_m v_{be} - sC_\mu v_{be} = (g_m - sC_\mu) v_{be} \approx g_m v_{be}$$