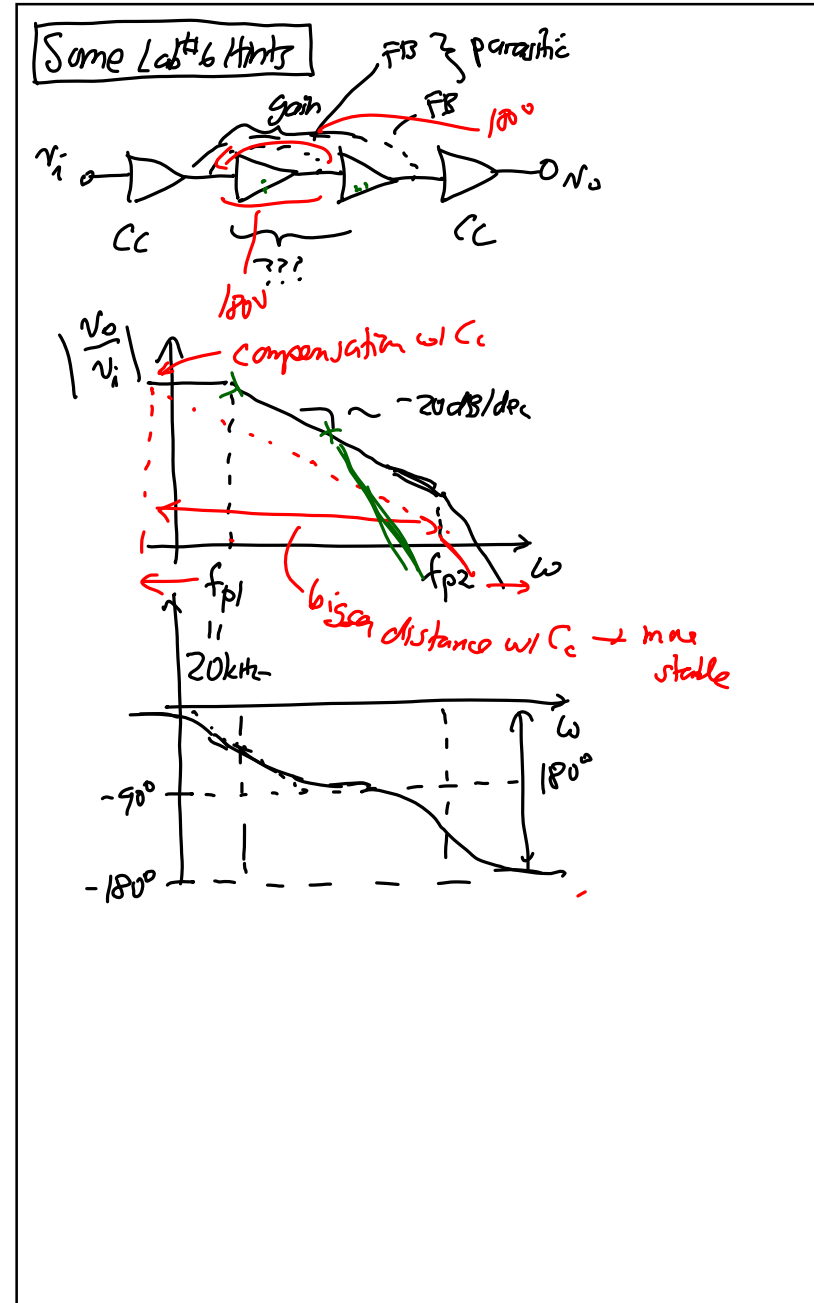


EE 105: Microelectronic Devices & Circuits

Lecture 39w: Propagation Delay II

Lecture 39: Propagation Delay II

- Announcements:
- HKN course evaluation facilitators took the first 15 minutes of class
- HW#11 online and due during RRR week
- Lab 6 online and due 5 p.m., Friday, Dec. 7
- -----
- **Lecture Topics: (over the next few days)**
 - ↳ Static CMOS Inverter Behavior
 - V_{OL} and V_{OH}
 - V_{IL} and V_{IH}
 - ↳ Dynamic CMOS Inverter Behavior
 - Propagation Delay
 - Capacitance
 - ↳ Astable Ring Oscillator
 - ↳ CMOS Inverter Propagation Delay
- -----
- Last Time:
- Just starting to derive expressions for propagation delay
- Now continue with this ...

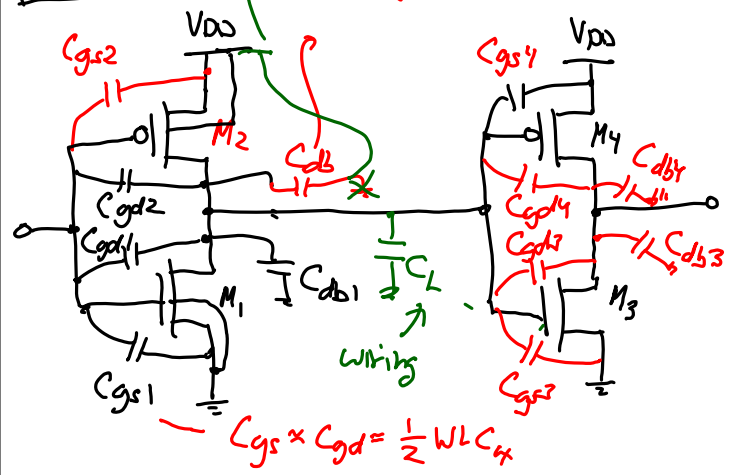


EE 105: Microelectronic Devices & Circuits
Lecture 39w: Propagation Delay II

- **Remarks**
- Propagation delay is the delay experienced by a signal passing through a gate as measured between the 50% transition points between input and output waveforms
- In general, a gate displays different response times for rising and falling input waveforms
- Thus, define:
 - ↳ t_{pLH} : response time of a gate making a low \rightarrow high output transition
 - ↳ t_{pHL} : response time of a gate making a high \rightarrow low output transition
- Propagation delay then defined as the average of t_{pLH} and t_{pHL}
- What causes switching delay?
 - ↳ Finite current transistor current drive (i.e., finite on resistance R_{on})
 - ↳ Output node capacitance

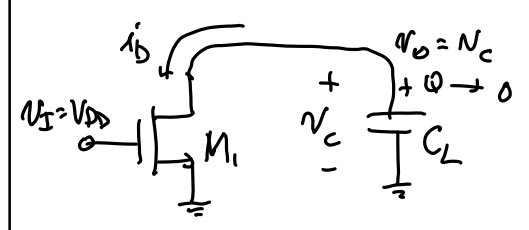
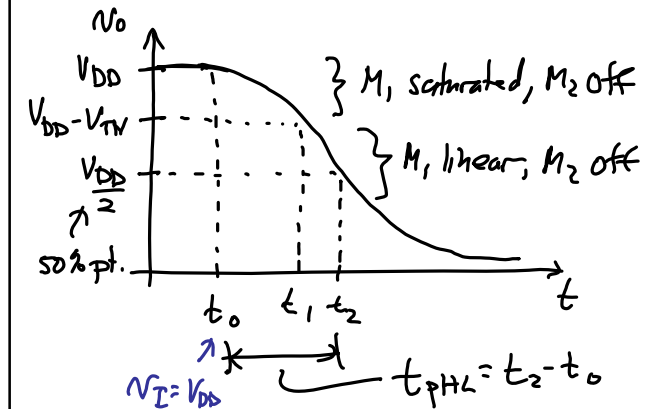
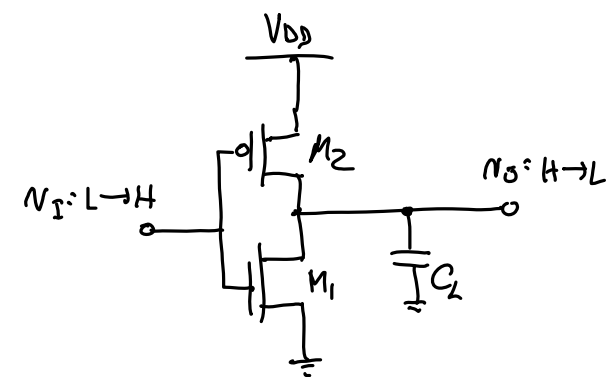
Capacitance

$$C_{db} = \frac{C_{db0}}{\sqrt{f(V_{DD})}} = \left(H \frac{W_{DB}}{\phi_j} \right)^m$$



$$C_{gs} \times C_{gd} = \frac{1}{2} WLC_{ox}$$

Propagation Delay in CMOS Inverters



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Lecture 39w: Propagation Delay II

Case: $V_0 \geq V_{DD} - V_{TN} \rightarrow M_1$ saturated

$$i_{D(sat)} = \frac{K_N}{2} (V_{GS} - V_{TN})^2 = -C_L \frac{dv_c}{dt}$$

$$dt = \frac{-2C_L dv_c}{K_N (V_{GS} - V_{TN})^2} = \frac{-2C_L dv_c}{K_N (V_{DD} - V_{TN})^2}$$

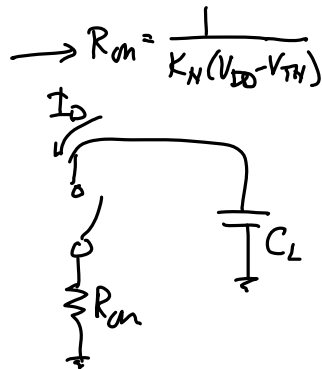
$$[V_{GS} = V_{OH} = V_{DD} = V_I]$$

$$\int_{t_0}^{t_1} dt = \int_{V_{DD}}^{V_{DD} - V_{TN}} \frac{2C_L}{K_N (V_{DD} - V_{TN})^2}$$

$$(t_1 - t_0) = - \frac{2C_L}{K_N (V_{DD} - V_{TN})^2} (V_{DD} - V_{TN} - V_{DD})$$

$$(t_1 - t_0) = \frac{2C_L}{K_N (V_{DD} - V_{TN})} \frac{V_{TN}}{V_{DD} - V_{TN}} = 2C_L R_{on} \frac{V_{TN}}{V_{DD} - V_{TN}}$$

effective
"on resistance"
of the transistor



Case: $V_0 < V_{DD} - V_{TN} \rightarrow M_1$ linear

$$i_{D(lin)} = -C_L \frac{dv_c}{dt}$$

$$K_N (V_{GS} - V_{TN} - \frac{V_{DS}}{2}) V_{DS} = -C_L \frac{dv_c}{dt}$$

$$[V_{DS} = V_c, V_{GS} = V_{DD}] \Rightarrow \left\{ K_N (V_{DD} - V_{TN} - \frac{V_c}{2}) V_c = -C_L \frac{dv_c}{dt} \right\} \times 2$$

$$\int_{t_1}^{t_2} \frac{K_N}{2C_L} dt = - \int_{V_1}^{V_2} \frac{dv_c}{[2(V_{DD} - V_{TN}) - V_c] V_c}$$

$$\left[\int \frac{dx}{(a-x)x} = \int \frac{dx}{a(a-x)} + \int \frac{dx}{ax} \right]$$

$$= \frac{1}{a} \int \left[\frac{1}{a-x} + \frac{1}{x} \right] dx = \frac{1}{a} \ln \left(\frac{x}{a-x} \right)$$

$$[V_2 = \frac{V_{DD}}{2}, V_1 = V_{DD} - V_{TN}] \Rightarrow$$

$$\frac{K_N}{2C_L} (t_2 - t_1) = - \frac{1}{2(V_{DD} - V_{TN})} \ln \left[\frac{V_c}{2(V_{DD} - V_{TN}) - V_c} \right] \Bigg|_{V_{DD} - V_{TN}}^{\frac{V_{DD}}{2}}$$

; algebra

$$= - \frac{1}{2(V_{DD} - V_{TN})} \ln \left[\frac{V_{DD}}{4V_{DD} - 4V_{TN} - V_{DD}} \right]$$