

- ~~PS 10~~ CHAPTER 8
 SINGLE STAGE AMPS /
 2-PORT MODELS

Lecture 26 MOS + BJT.

→ LAST PS < MIDTERM.

IMPORTANT!

- Last time:
 - Finish methods for finding two-port model parameters **CS**
 - Start common-source amplifier
- Today :
 - Current-source supplies
 - Common-gate amplifier

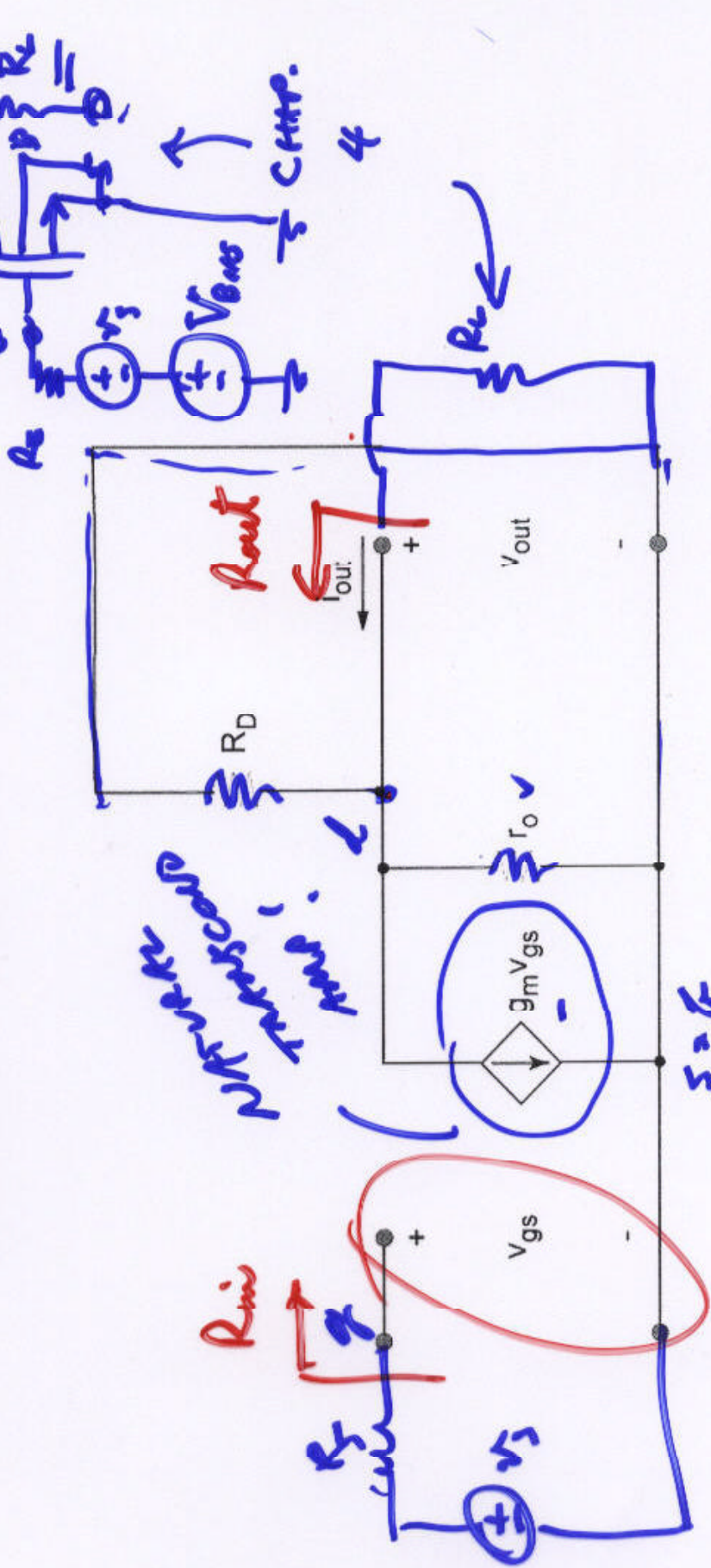
↖ **CG**

$$\left. \begin{array}{l} R_{in}, R_{out} \\ G_m - CS \\ A_v - CG \\ A_v - CD \end{array} \right\}$$

USE THESE RESULTS!

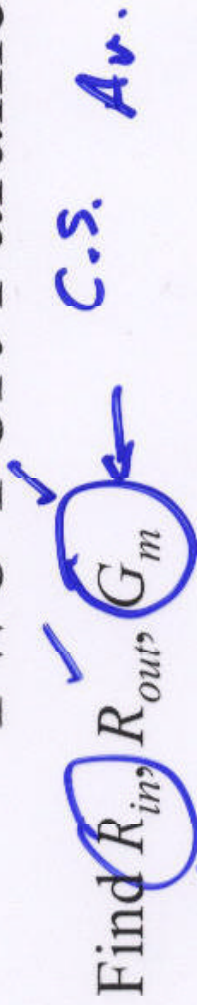
low- ω . OPEN CAPS! R. T. Howe, UoD

Small-Signal Analysis



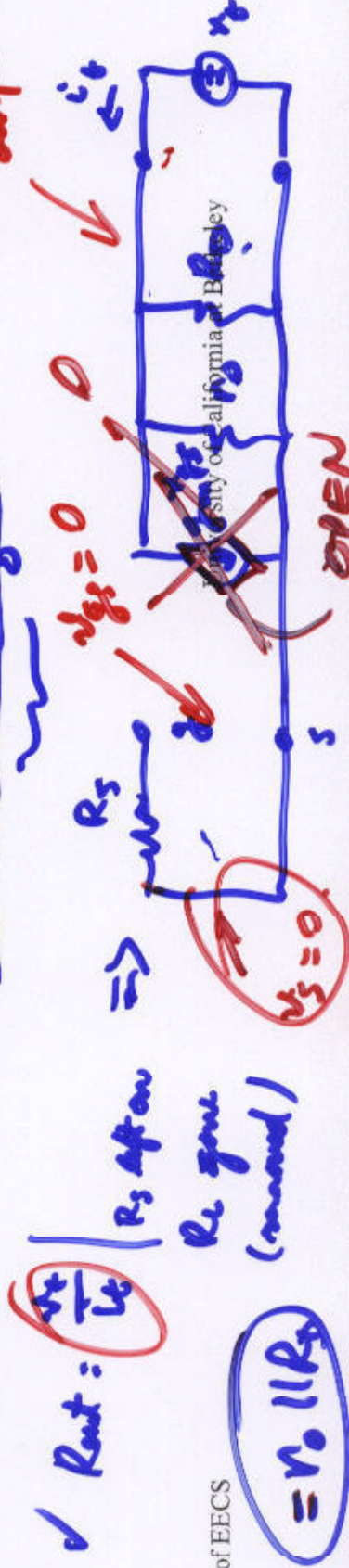
$v_{sb} = 0.$

Two-Port Parameters:



$$R_{in} = \frac{v_e}{i_e} \quad \left| \quad R_3 = 0 \right. \quad \left. \frac{v_e}{i_e} = \infty! \right.$$

Left R_e on



$R_{out} = \frac{v_e}{i_e} \Rightarrow R_3 \text{ after } R_e \text{ gone (removed)}$

Dept. of EECS

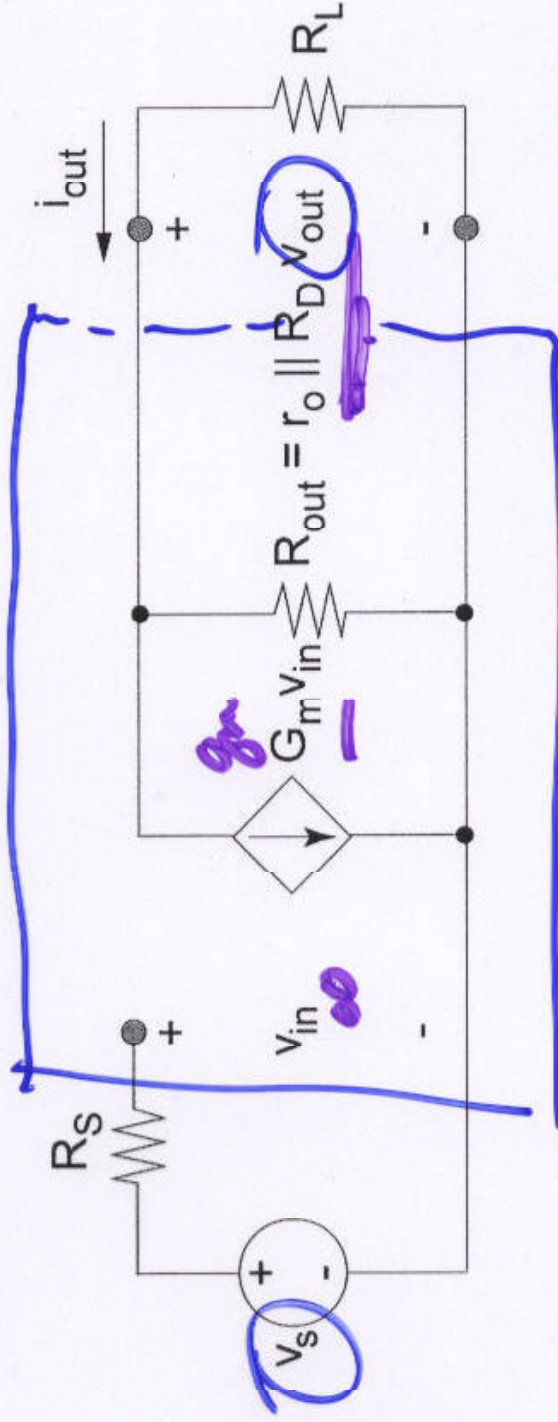
$= R_0 || R_3$

Finding $G_m = \frac{i_{out}}{v_s} / R_s = 0 \text{ V} = G_m$
 $R_s = 0 \text{ V}$
 $R_i = 0 \text{ V}$
 $i_{out} = g_m v_s$



Two-Port CS Model

Reattach source and load one-ports:



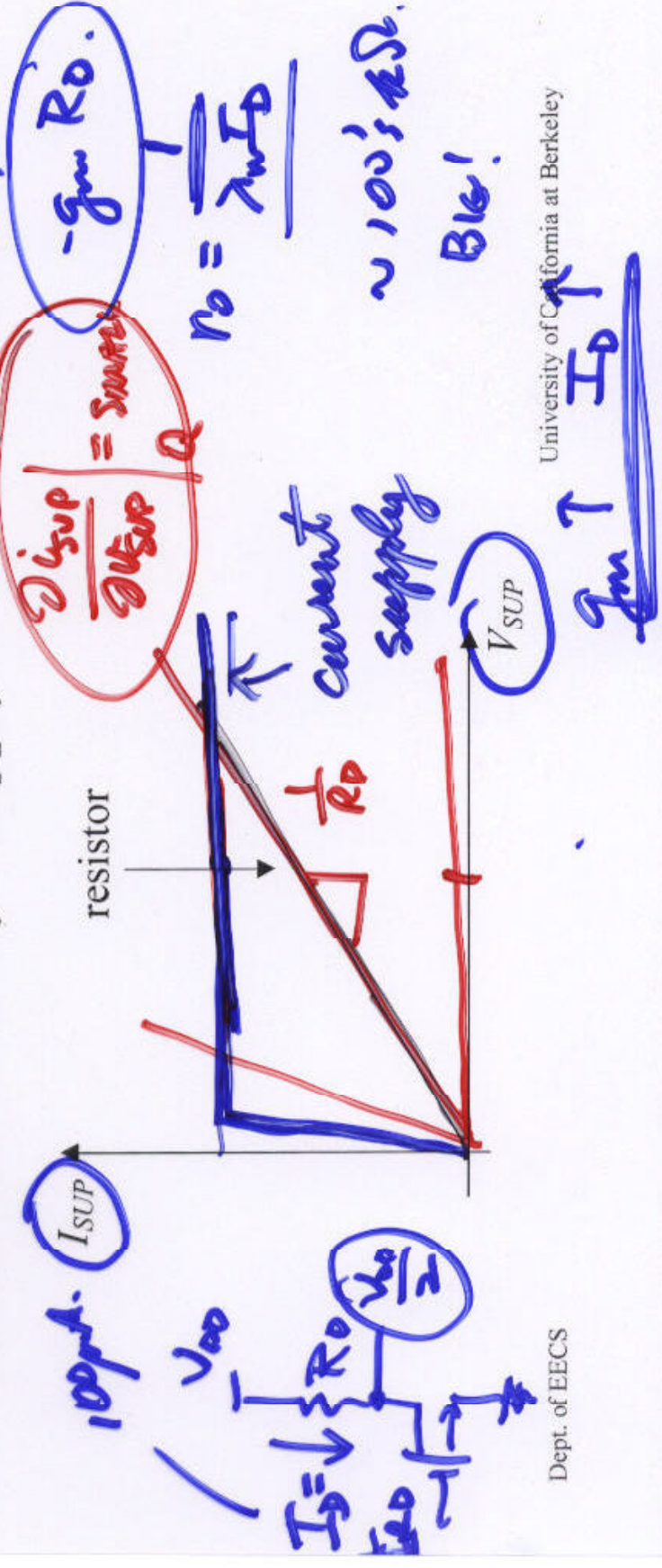
$$v_{out} = -G_m [R_{out} \parallel R_L] \cdot v_{in} \quad v_3$$

$$A_v^* = \frac{v_{out}}{v_3} = -g_m [r_o \parallel R_D \parallel R_L]$$

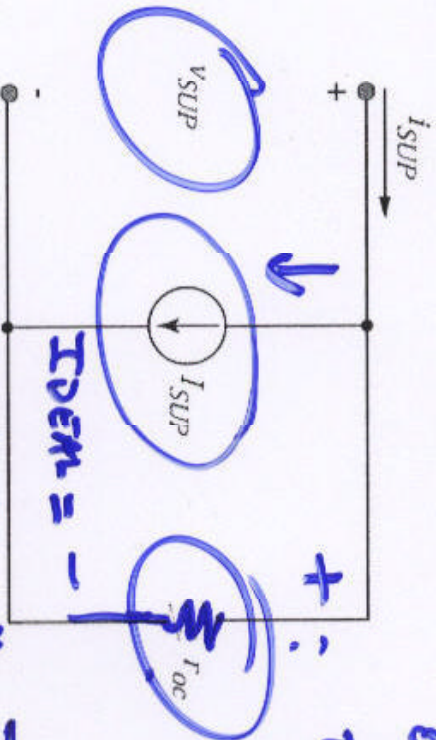
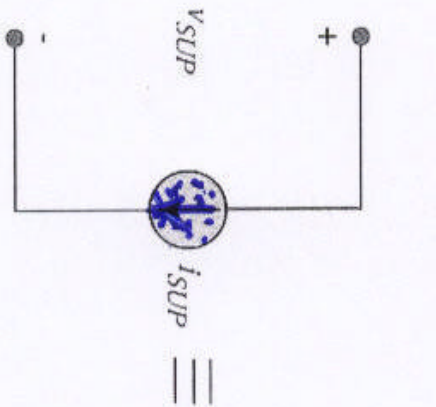
MORE GAIN!!

Non-Ideal Current Sources

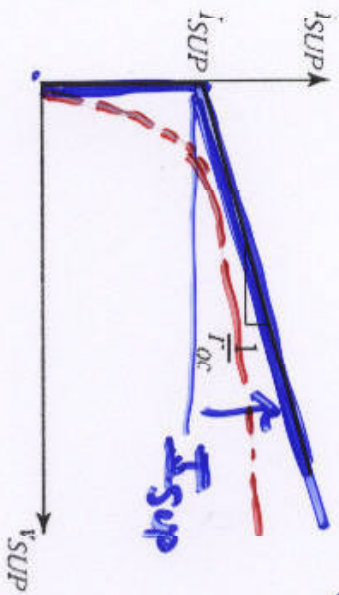
- We want to have both R_D and a very large g_m at the same time ... how to do it? $A_v = -g_m [R_D || R_o] \approx \infty$
- The gain depends on the small-signal resistance; the DC current can be set by a supply \rightarrow modify load line



Current Source Supply

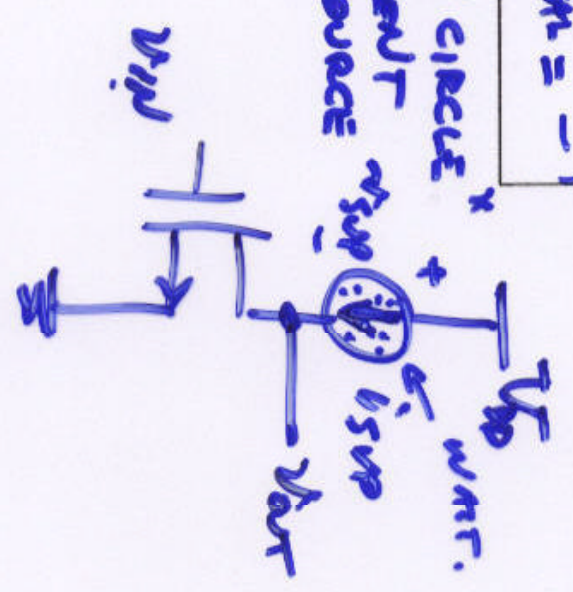


ONLY V_{th} FOR
EAE
V_{sup} > 0.



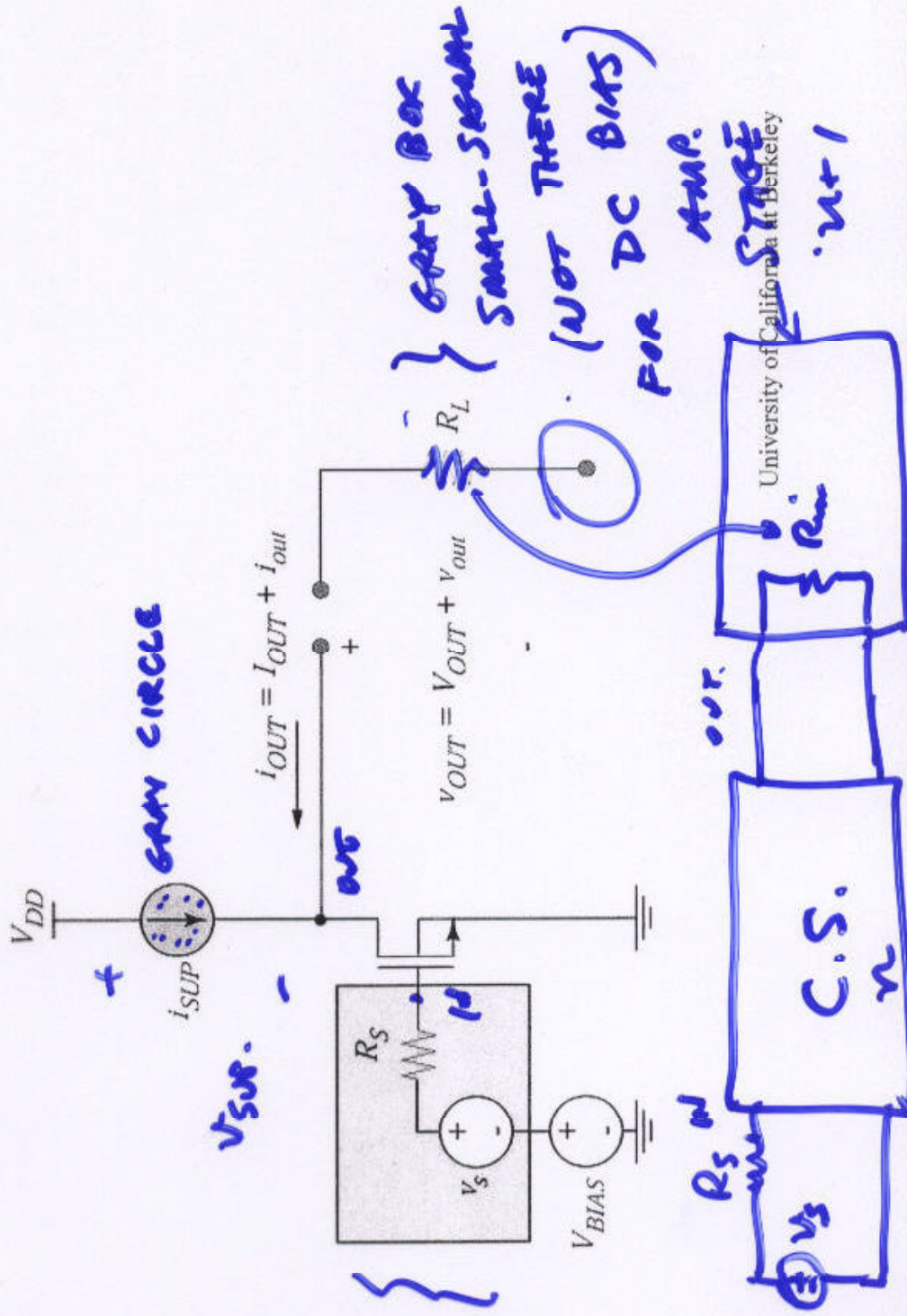
WRITE CIRCLE

CURRENT SOURCE

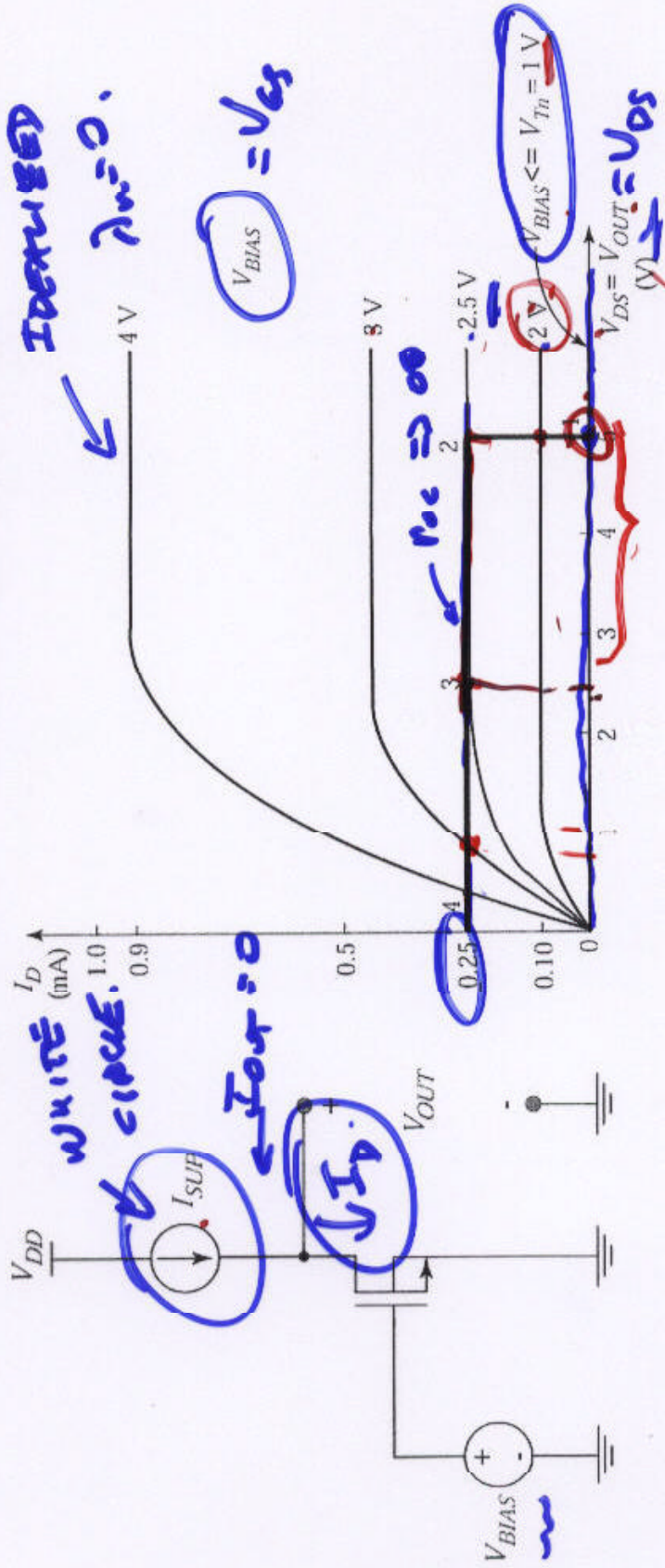


Common-Source Amplifier with Current Source Supply

RE-DO C.S.



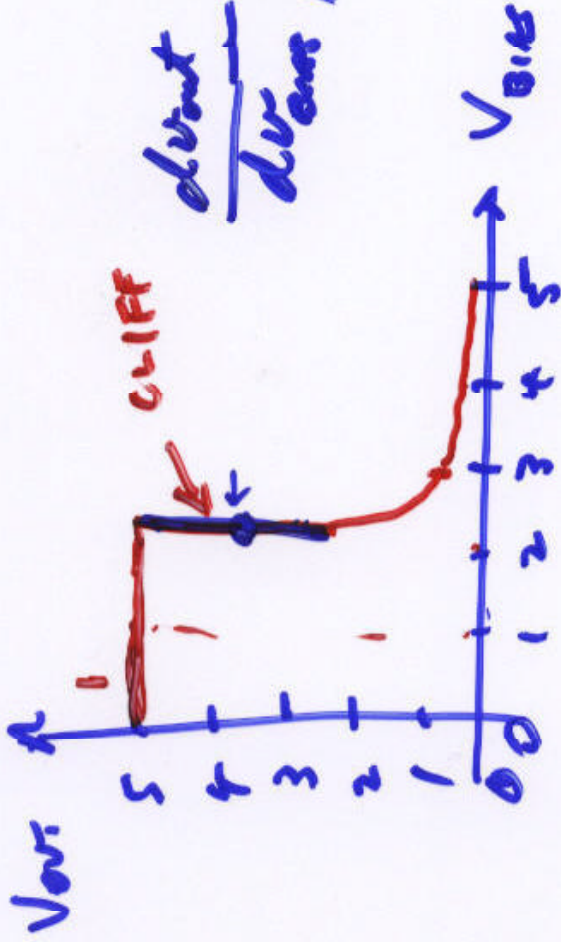
Load Line for DC Biasing



Both the I-source and the transistor are idealized for DC bias analysis

$$I_D = I_{SUP} = 250 \mu A = 0.25 \text{ mA}$$

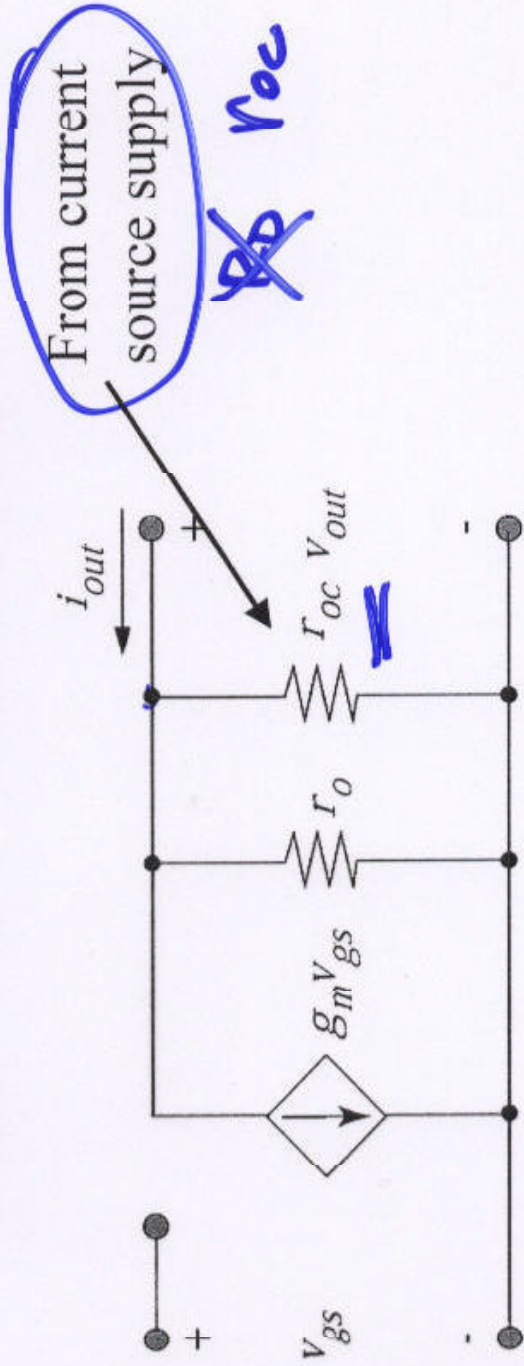
TRANSFER CURVE



$$\frac{dV_{out}}{dV_{in}} = -\infty !!$$

NOT DONE...
UNSTABLE BJT.

Two-Port Parameters



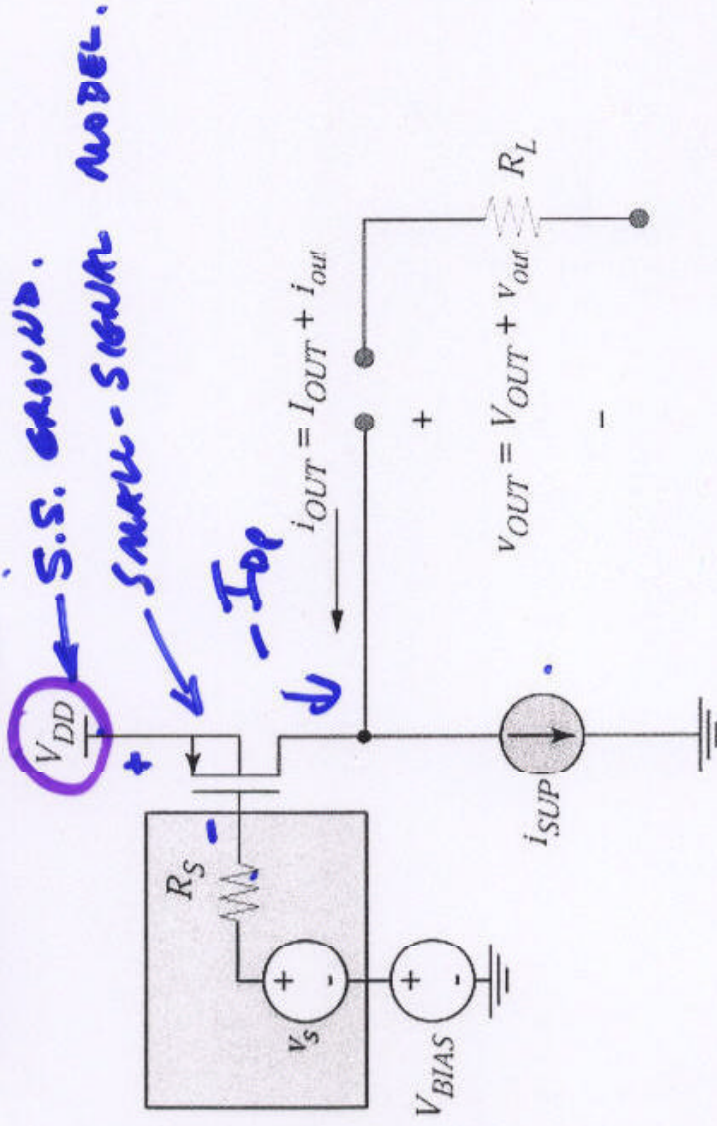
$$R_{in} = \infty$$

$$G_m = g_m$$

$$R_{out} = r_o \parallel r_{oc}$$



P-Channel CS Amplifier

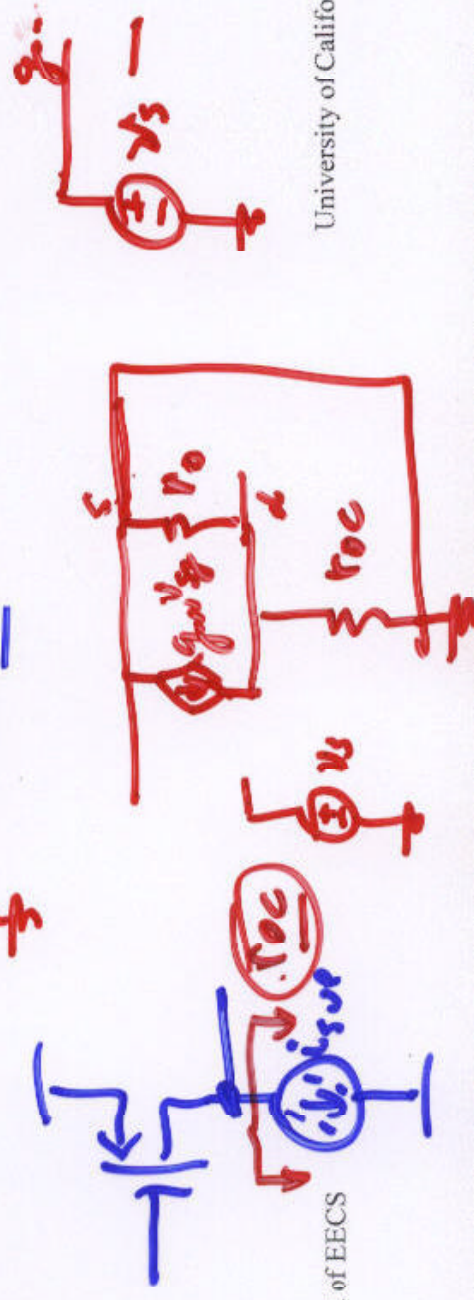
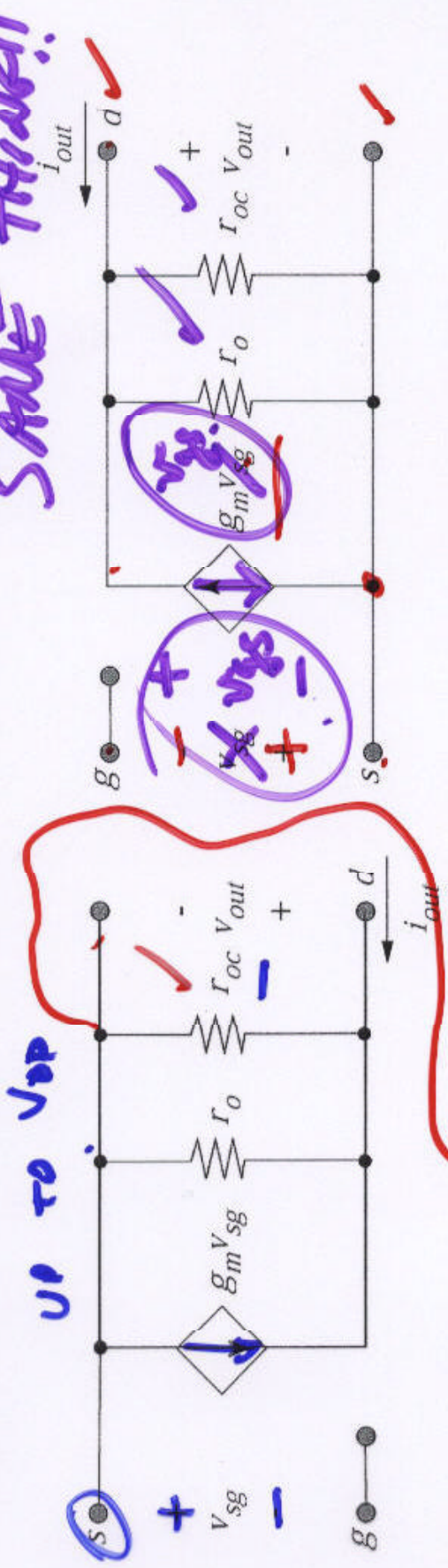


DC bias: $V_{SG} = V_{DD} - V_{BIAS}$ sets drain current $-I_{DP} = I_{SUP}$

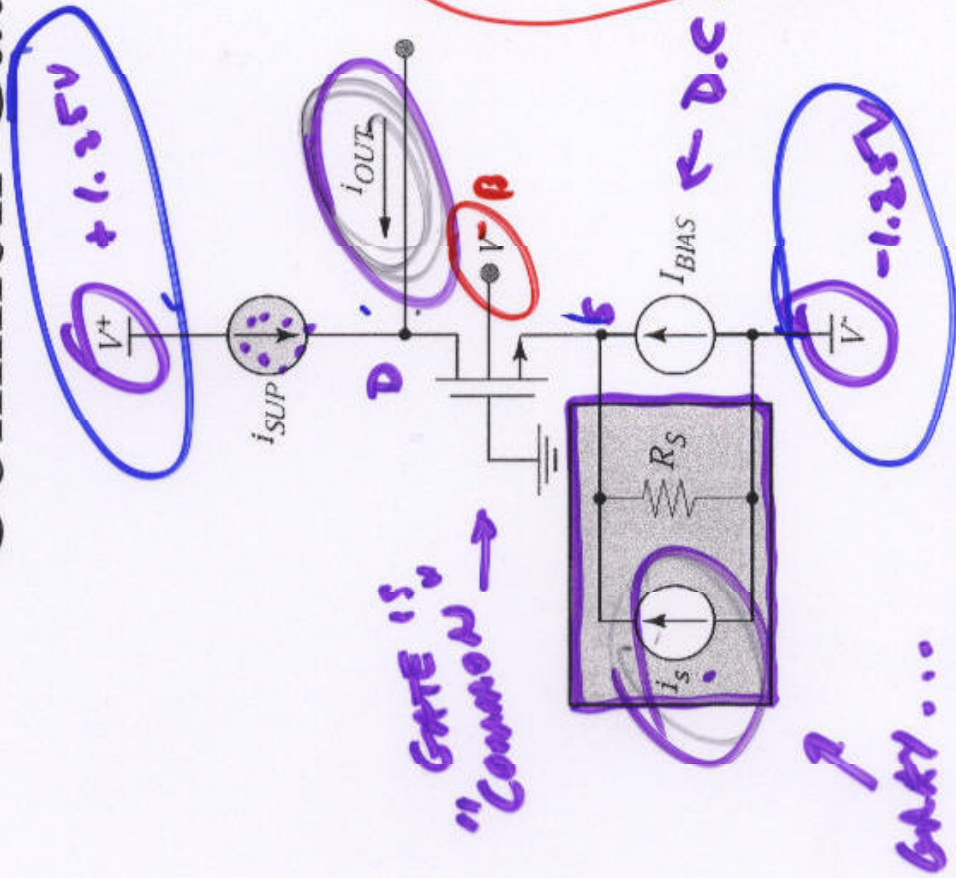
Two-Port Model Parameters

Small-signal model for PMOS and for rest of circuit

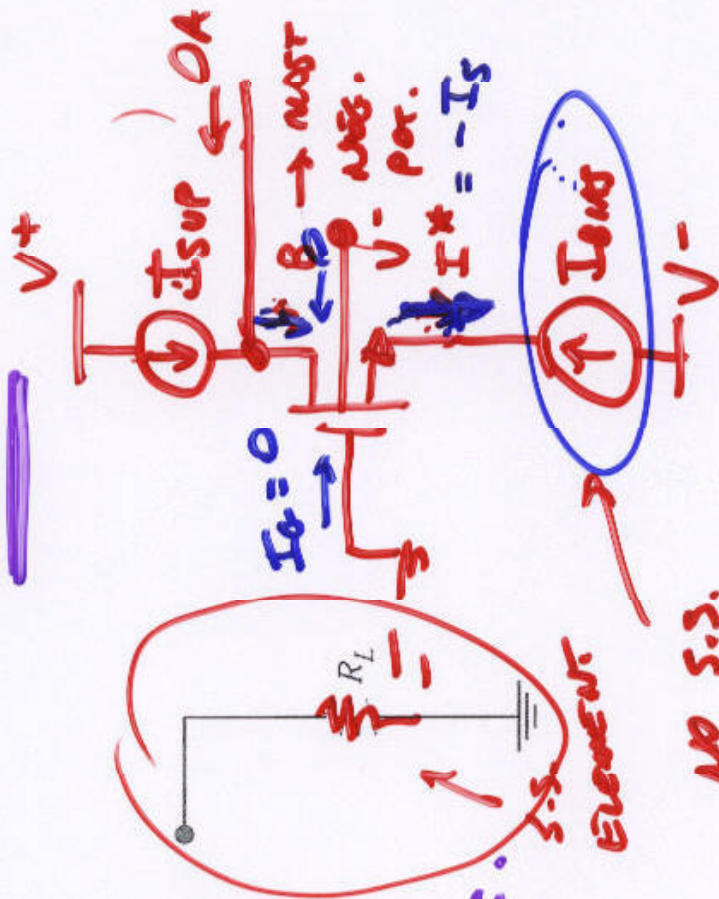
SAME THING!



Common Gate Amplifier



DC bias:



NO S.S.

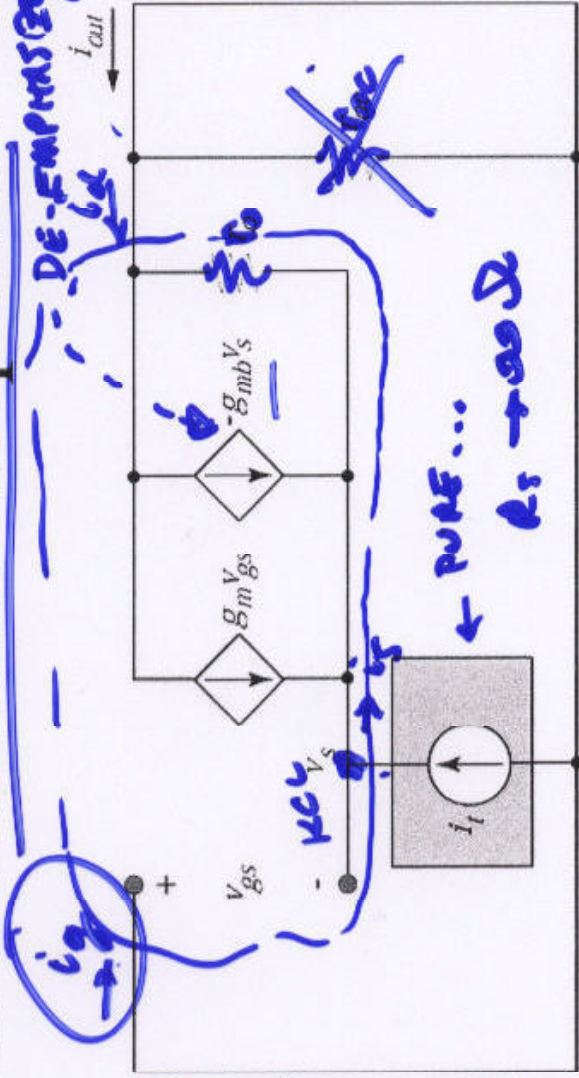
S.S. ELEMENTS.

$I_{SUP} = I_D$
 $I^* = I_D$
 $I_{OUT} = -I_D = -I_{SUP}$

BUFFER

CG as a Current Amplifier: Find A_i

DE-EMPHASIZED IN 10F ==



$g_m \approx 0.1$
 $v_{gs} = -v_s$

$i_{out} = i_d = -i_g - i_s = -i_t$

$A_i = \frac{i_{out}}{i_t} \Big|_{R_S = \infty, R_L = 0}$

$A_i = \frac{-i_t}{i_t} = -1$