

Lecture 16: High Gain Op Amps II

Announcements:

- ↪ HW#7 due Monday, Oct. 26, at 8 a.m.
- ↪ 240A students should be working on HW#1A, too, due Nov. 2, but let's change this, since Lab#2 is also due that day
- ↪ New HW#1A deadline: Friday, Nov. 6
- ↪ I will be traveling again this Friday, returning next Monday - should be back in time for office hours, depending on flight arrival time
- ↪ Midterm will be on the date specified in your syllabus: Thursday, Oct. 29, 6-8 p.m. in 141 McCone

Go through midterm information sheet

Lecture Topics:

- ↪ Telescopic Op Amp in Feedback
- ↪ Lab#2 Hints
- ↪ Compensation (a 1st pass)
- ↪ Slew Rate (a 1st pass)

Last Time:

- Telescopic op amps

over

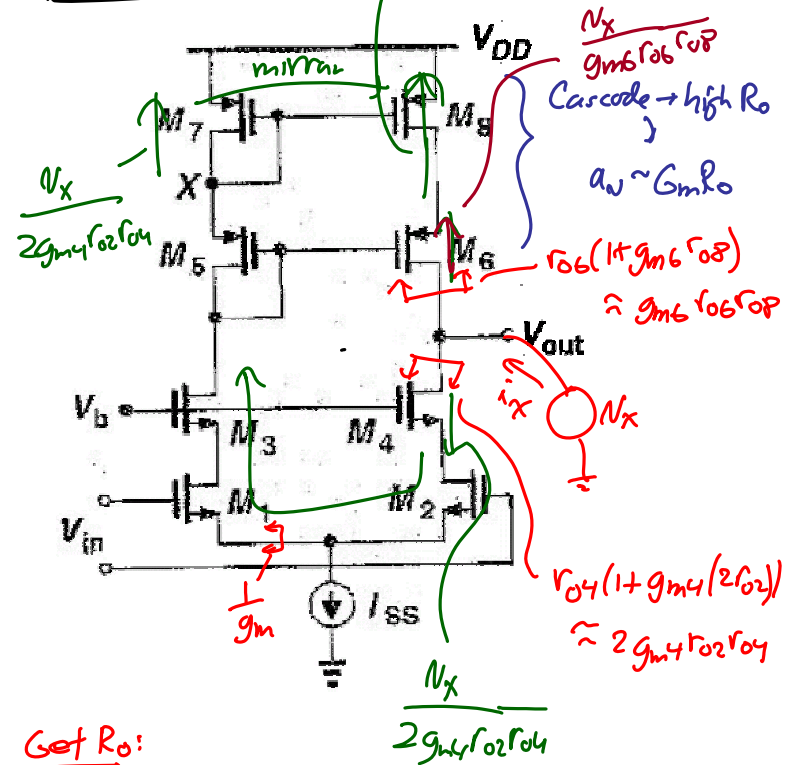
High Gain Op Amps

How can we increase gain?

- ① Cascode
- ② Cascode Amplifier

$$\frac{N_x}{2g_{m1}r_{o2}r_{o4}}$$

Telescopic Op Amp w/ Single-Ended Output



Get R_o :

$$i_{N_x} = \frac{N_x}{g_{m6}r_{o6}r_{o8}} + \frac{N_x}{2g_{m4}r_{o2}r_{o4}} + \frac{N_x}{2g_{m1}r_{o2}r_{o4}}$$

$$R_o = \frac{V_x}{i_x} = (g_{m6} r_{o6} r_{o8}) \parallel (g_{m4} r_{o2} r_{o4})$$

$$= (g_{mp} r_{op}^2) \parallel (g_{mn} r_{on}^2)$$

Get Gm!

$$i_o = g_m \frac{V_{in}}{2} + g_m \frac{V_{in}}{2} \Rightarrow G_m = \frac{i_o}{V_{in}} = g_m$$

$$\therefore \text{Gain} \cdot A_v = g_{mN} \left[(g_{mN} r_{oN}^2) \parallel (g_{mP} r_{oP}^2) \right]$$

$$\text{gain will be BIG!}$$

Freq. Response:

$$\omega_H = \frac{1}{R_o C_L}$$

Problem/Issue:

① Limited output swing:

$$V_{omax} = V_{DD} - |V_{t7}| - |V_{ov7}| - |V_{t5}| - |V_{ov5}| + |V_{t6}| + |V_{ov6}| - |V_{ov6}|$$

$$V_{omin} = V_{ovSS} + V_{ov2} + V_{ov4}$$

$$V_{swing} = V_{omax} - V_{omin}$$

Problem: Not so large!

Problem 2: Difficult to tie the Input to output!

unity gain buffer (very useful)

Common/ VBIAS Generated "DC" V_b

V_{ov4}

$V_x = V_b - V_{gs4}$

$V_{t2} + V_{ov2}$

Must keep M_2 & M_4 saturated!

M_4 : Need $V_{out} \geq V_b - V_{gs4} + V_{ov4} = V_b - V_{t4} - V_{ov4} + V_{ov4}$
 $\therefore V_{out} \geq V_b - V_{t4}$

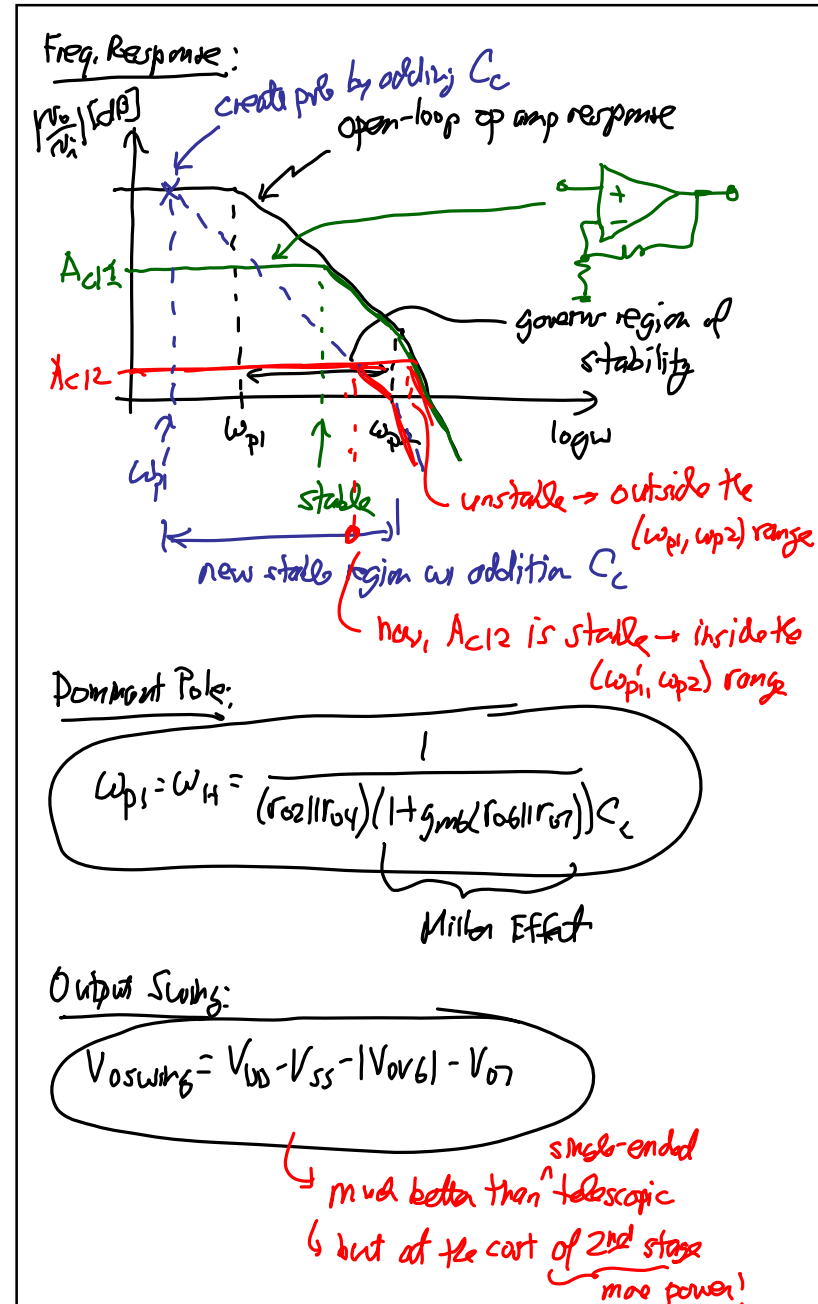
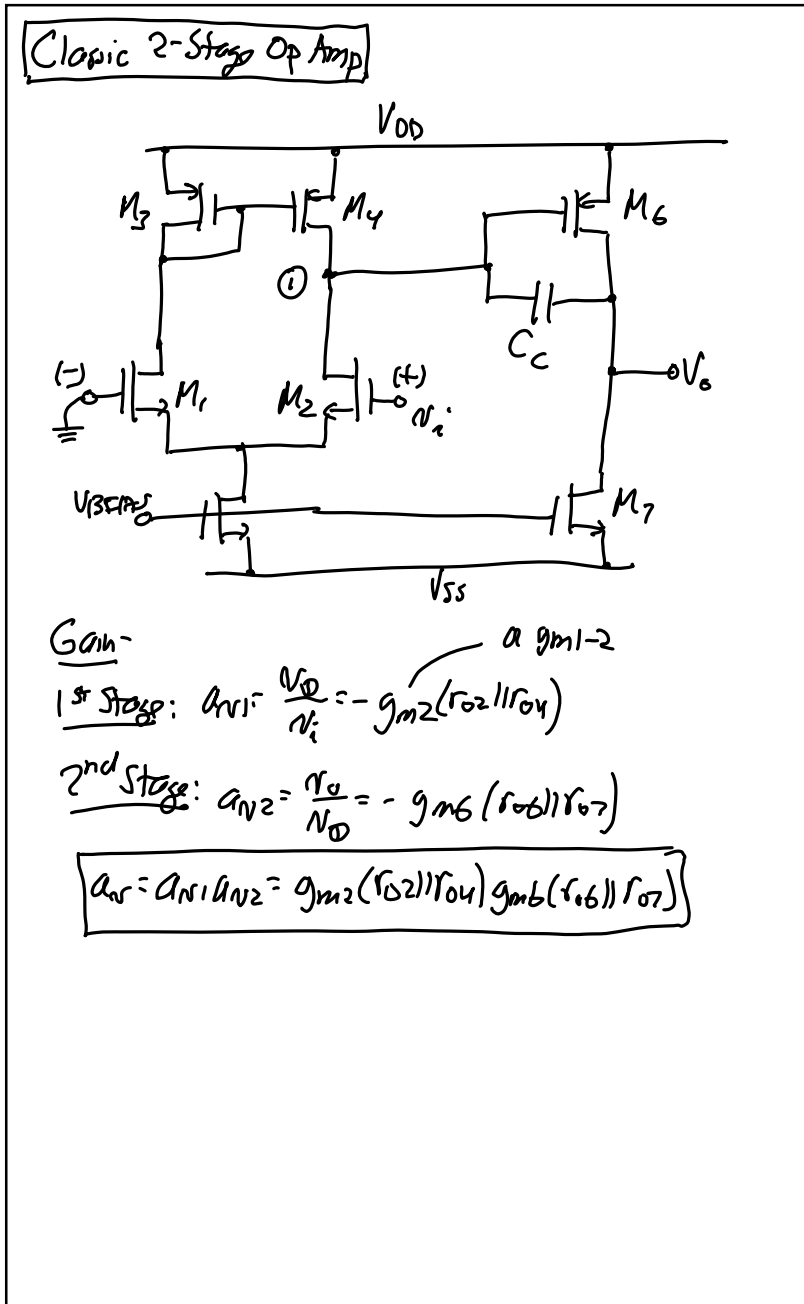
M_2 : Need $V_{out} \leq V_x - V_{ov2} + V_{t2} + V_{ov2}$
 $V_{out} \leq V_b - V_{t4} - V_{ov4} + V_{t2} \approx V_b - V_{ov4}$
 $V_{t2} = V_{t4}$

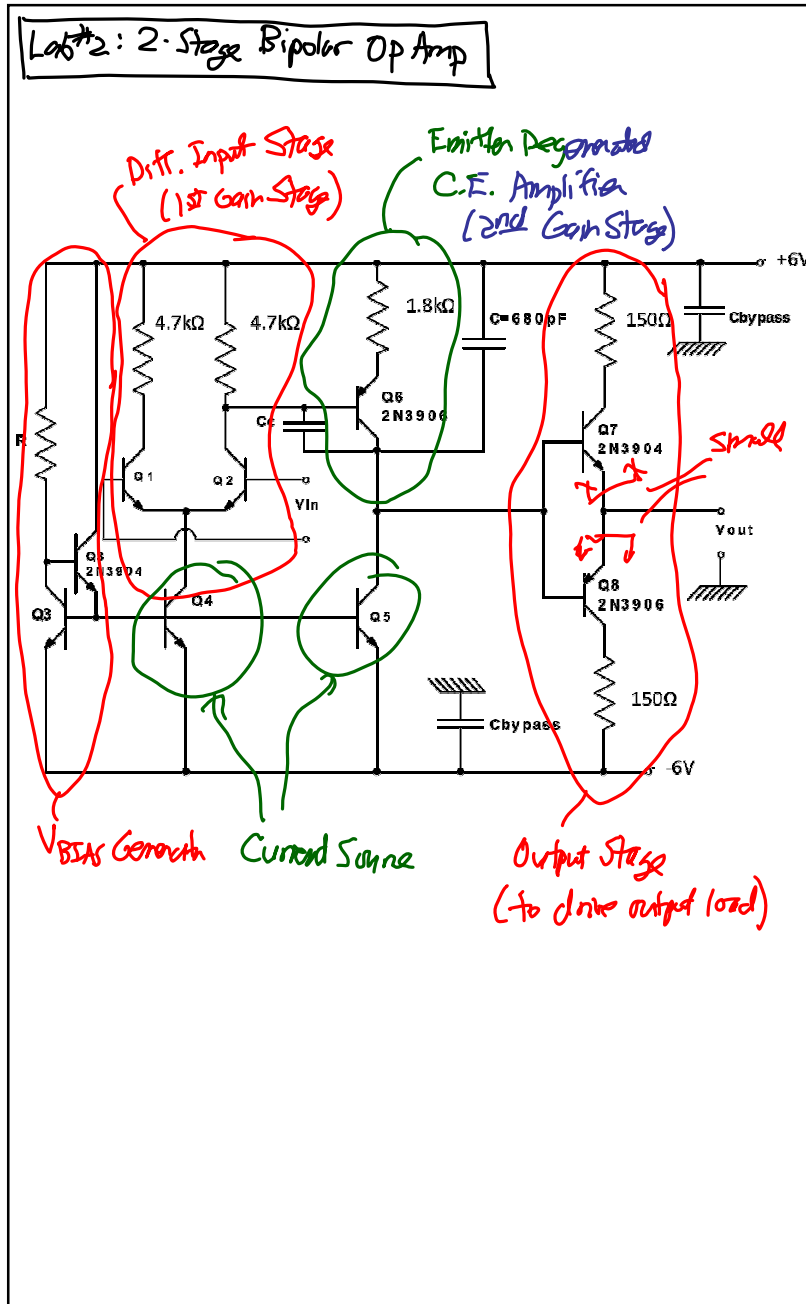
Allowable range of output voltage (not much! \rightarrow problem!)

Problem 3:
 Low freq, non-dominant pole associated w/ the "mirror" node \rightarrow will hurt stability in FB ccts!
 (we'll cover this later)

Soln: fully differential, fully balanced op amp

Another Soln: 2-stage op amp





Remarks.

- ① You analyze this in Lab#2.
- ② Usually, the resistively-loaded diff. pair is replaced w/ an active current mirror load for more gain.
- ③ R_{E6} raises the input R of Q_6 (of the 2nd gain stage), plus helps w/ biasing.
- ④ Same comment as ③ for the output stage.
- ⑤ Output stage needed when driving a resistive load

often the case for bipolar
 not often the case for MOS, where a capacitive load C_L is often more relevant → MOS op amps often don't need also for compensation output stages!

