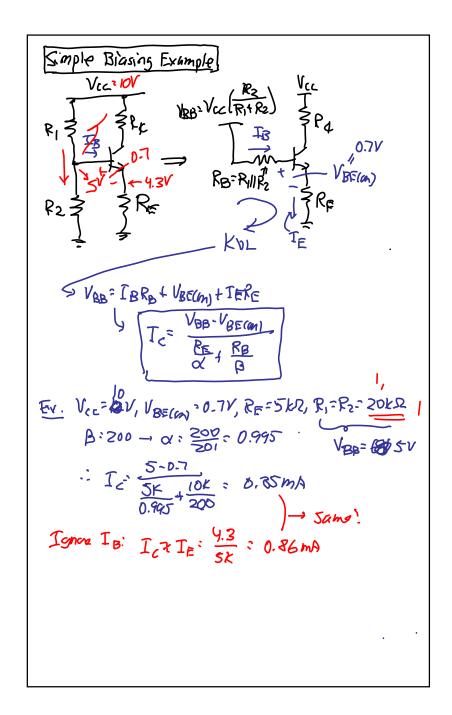
Lecture 2: Device Models I (Bipolar Review)

- · Announcements:
- The course website was up and running last week
- HW#1 online last week ... due next week
 Wednesday, at 8 a.m., in the 140/240A box on 1st floor (near the TI lab)
- · My Monday Office Hours updated to 2-3 p.m.
- •
- · Lecture Topics:
 - ♦ Review (fast)
 - \$ Bipolar Junction Transistor Modeling
 - -Basic Structure & Physics
 - -Large Signal Models
 - -DC Operating Point
 - -Small Signal Models
 - -Frequency Shaping Elements
 - -Layout
 - —Unity Gain Frequency
- Last Time: Reviewed op amps and started into BJT modeling using the module handout
- · Continue with the handout





Example. Find the Oc openating bit. > want the Ic's of all Ysistens - gm, co,... Smoot-signelemente $V_{CC} = 10 \text{V}$ 0.7V Same Tref VCC-VBE(M) = 10.0.7 : 0.93mA VCF = VBE(G) = 0.7V ナレŒ

