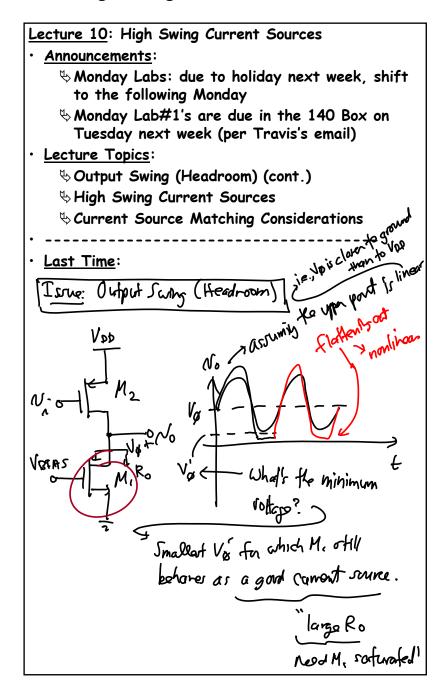
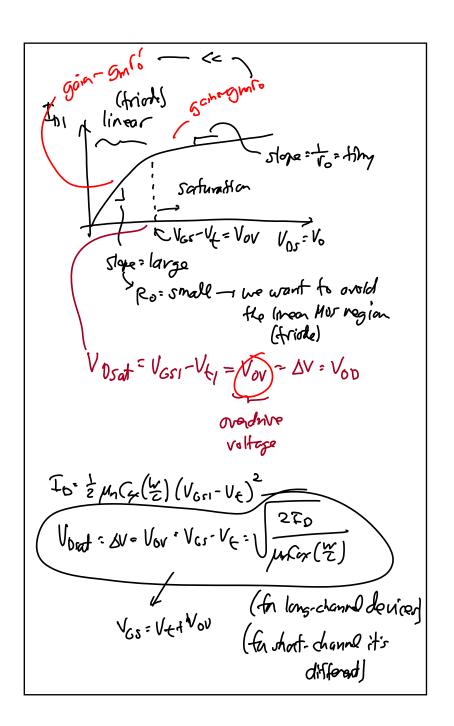
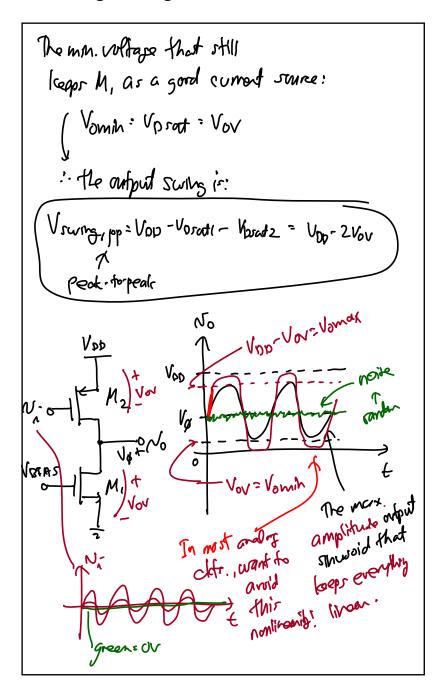
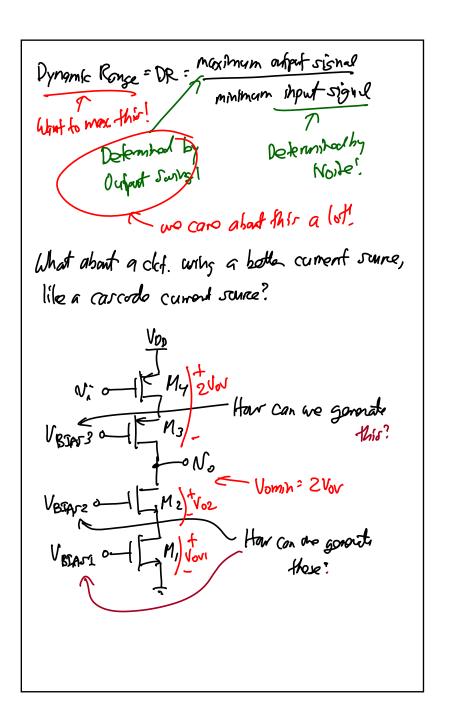
EE 140: Analog Integrated Circuits



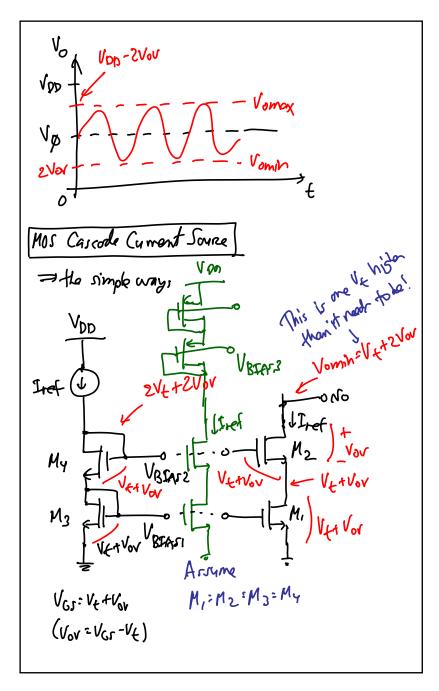


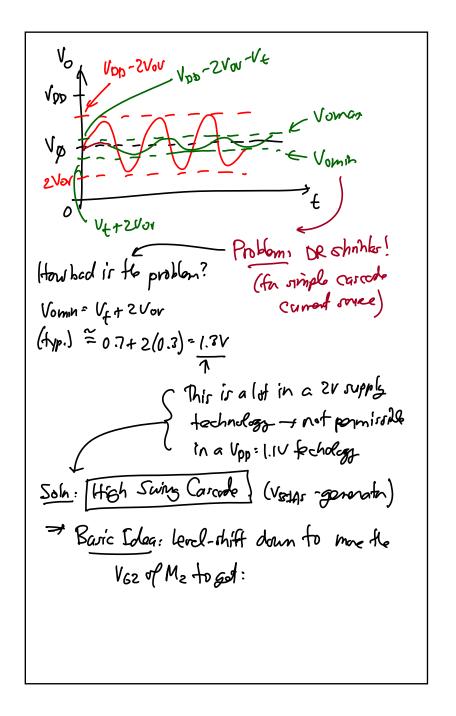
EE 140: Analog Integrated Circuits



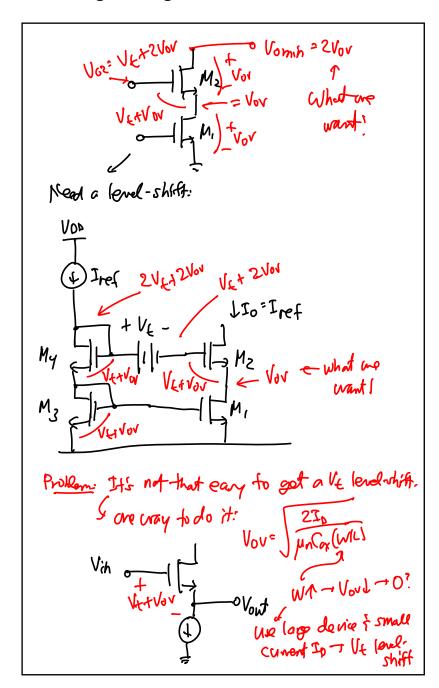


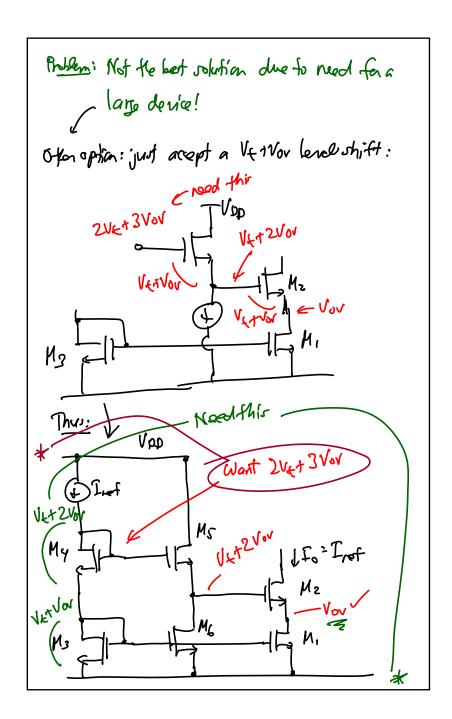
<u>EE 140</u>: Analog Integrated Circuits <u>Lecture 10w</u>: High Swing Current Sources



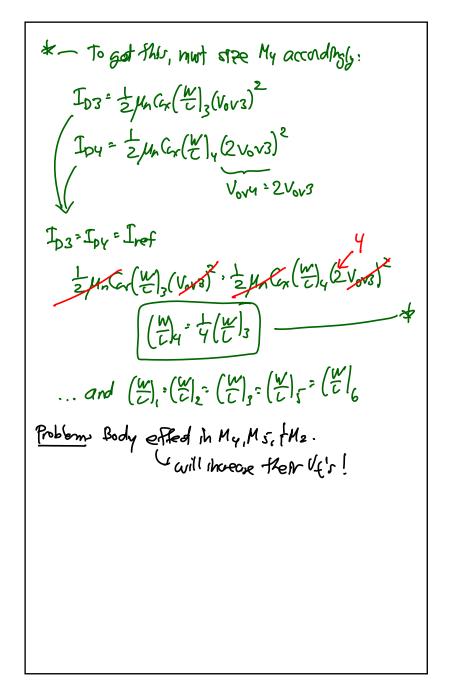


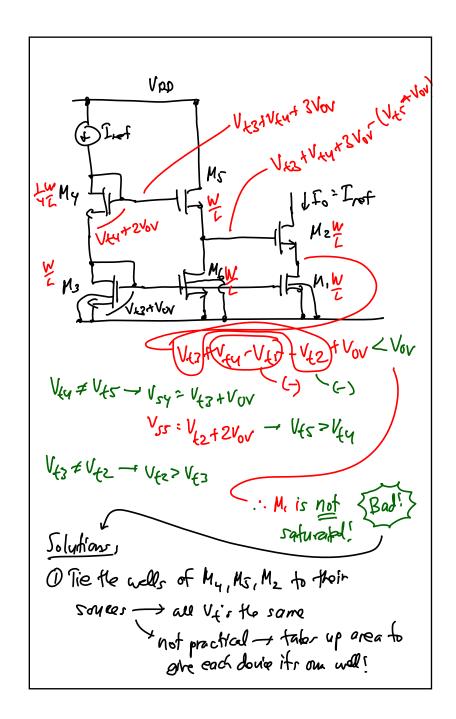
EE 140: Analog Integrated Circuits





EE 140: Analog Integrated Circuits





Bigs My so that
$$V_{OSY} > V_{\xi} + 2V_{OV}$$
,

 $(eg., V_{OSY} = V_{\xi} + 3V_{OV})$

make My smaller than $\frac{1}{4}(W_{\xi})_{\xi}$
 $(W_{\xi})_{\xi} > \frac{1}{4}(W_{\xi})_{\xi}$
 $(W_{\xi})_{\xi} > \frac{1}{4}(W_{\xi})_{\xi}$

Tref

Lof Inof

Solution: — rest time.