

EECS192 Lecture 1

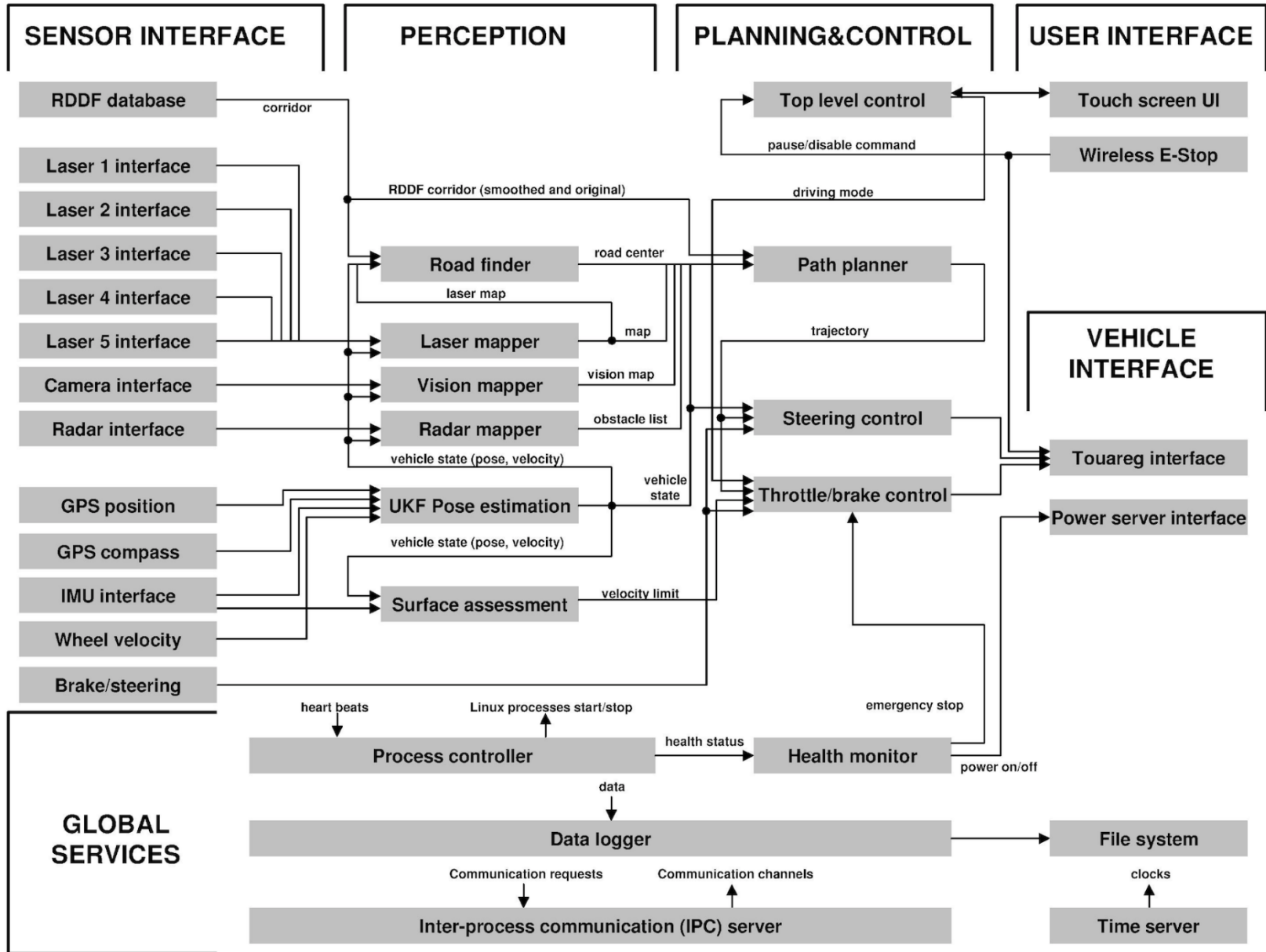
Jan. 17, 2017

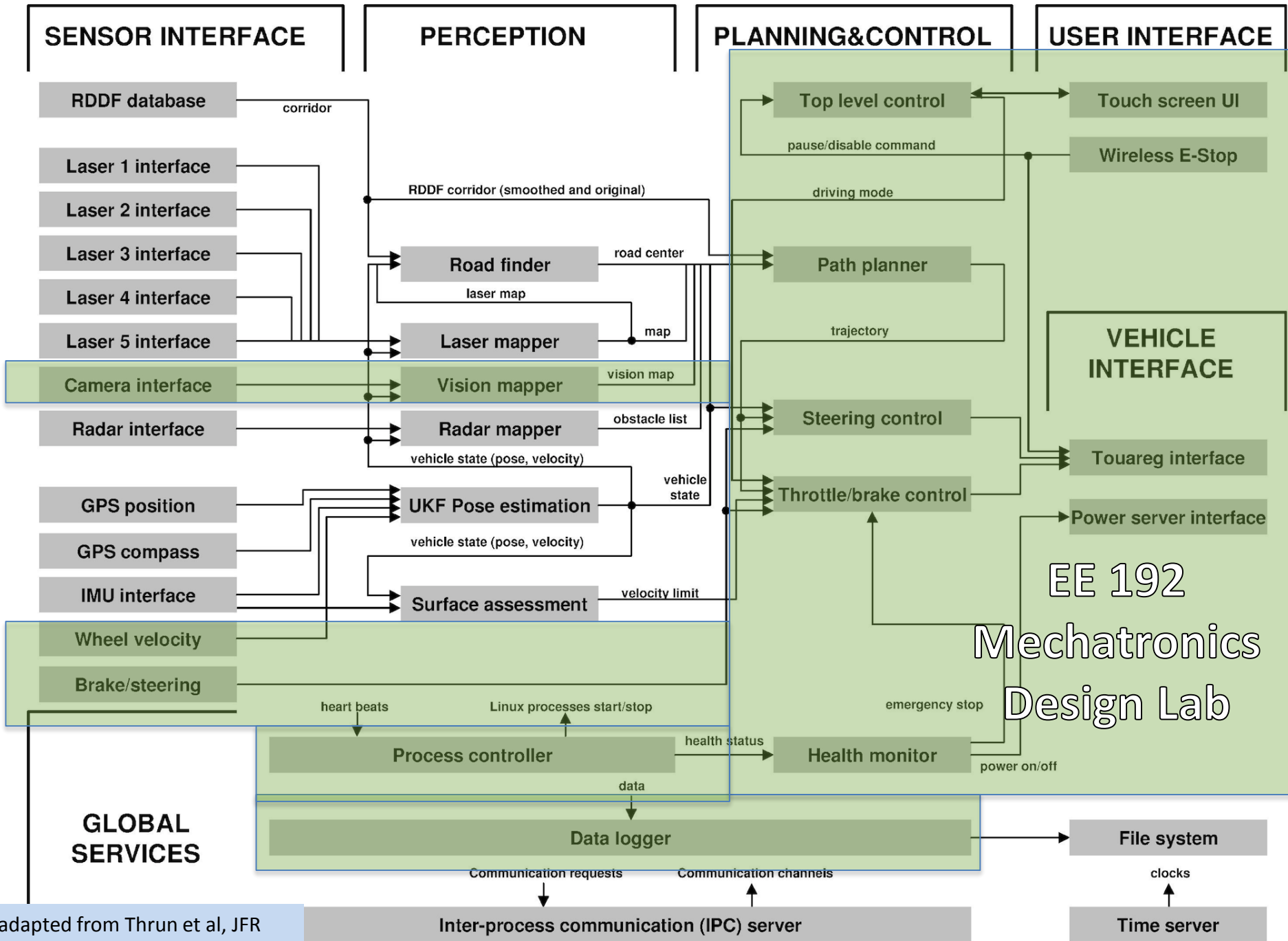
- Project Description
- Autonomous system example
- Course Organization
- ARM Cortex M0 overview
- Construction

Hardware

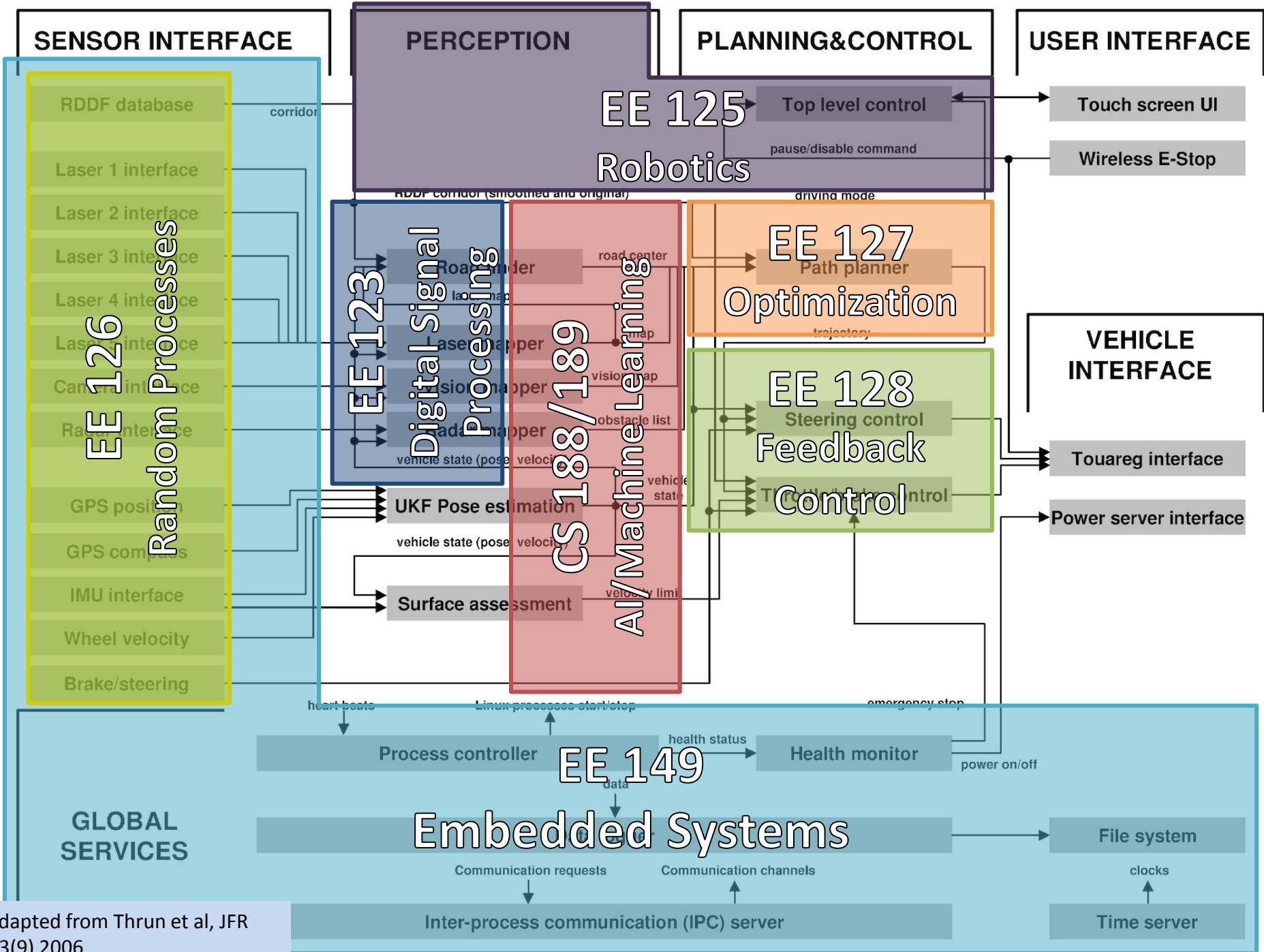


Thrun et al Stanley 2005





adapted from Thrun et al, JFR 23(9) 2006



SENSOR INTERFACE

PERCEPTION

PLANNING & CONTROL

USER INTERFACE

- RDDF database
- Laser 1 interface
- Laser 2 interface
- Laser 3 interface
- Laser 4 interface
- Laser 5 interface
- Camera interface
- Radar interface
- GPS position
- GPS compass
- IMU interface
- Wheel velocity
- Brake/steering

EE 126
 Random Processes

EE 123
 Digital Signal Processing

CS 188/189
 AI/Machine Learning

EE 125
 Robotics

EE 127
 Optimization

EE 128
 Feedback Control

EE 149

Embedded Systems

VEHICLE INTERFACE

ARM® Cortex™-M0+
Core

Debug
interfaces

Interrupt
controller

MTB

System

Internal
watchdog

DMA

BME

Memories and
Memory Interfaces

Program
flash

RAM

Clocks

Phase-
locked loop

Frequency-
locked loop

Low/high
frequency
oscillator

Internal
reference
clocks

128K Flash
16K RAM
32 bit ARM 7 core
48 MHz
A/D, D/A
2x SPI
Touch sense input
Timers

Security
and Integrity

Internal
watchdog

Analog

16-bit ADC
x1

Analog
comparator
x1

6-bit DAC

12-bit DAC

Timers

Timers
1x6ch+2x2ch

Low
power timer
x1

Periodic
interrupt
timers

RTC

Communication
Interfaces

I²C
x2

Low power
UART
x1

SPI
x2

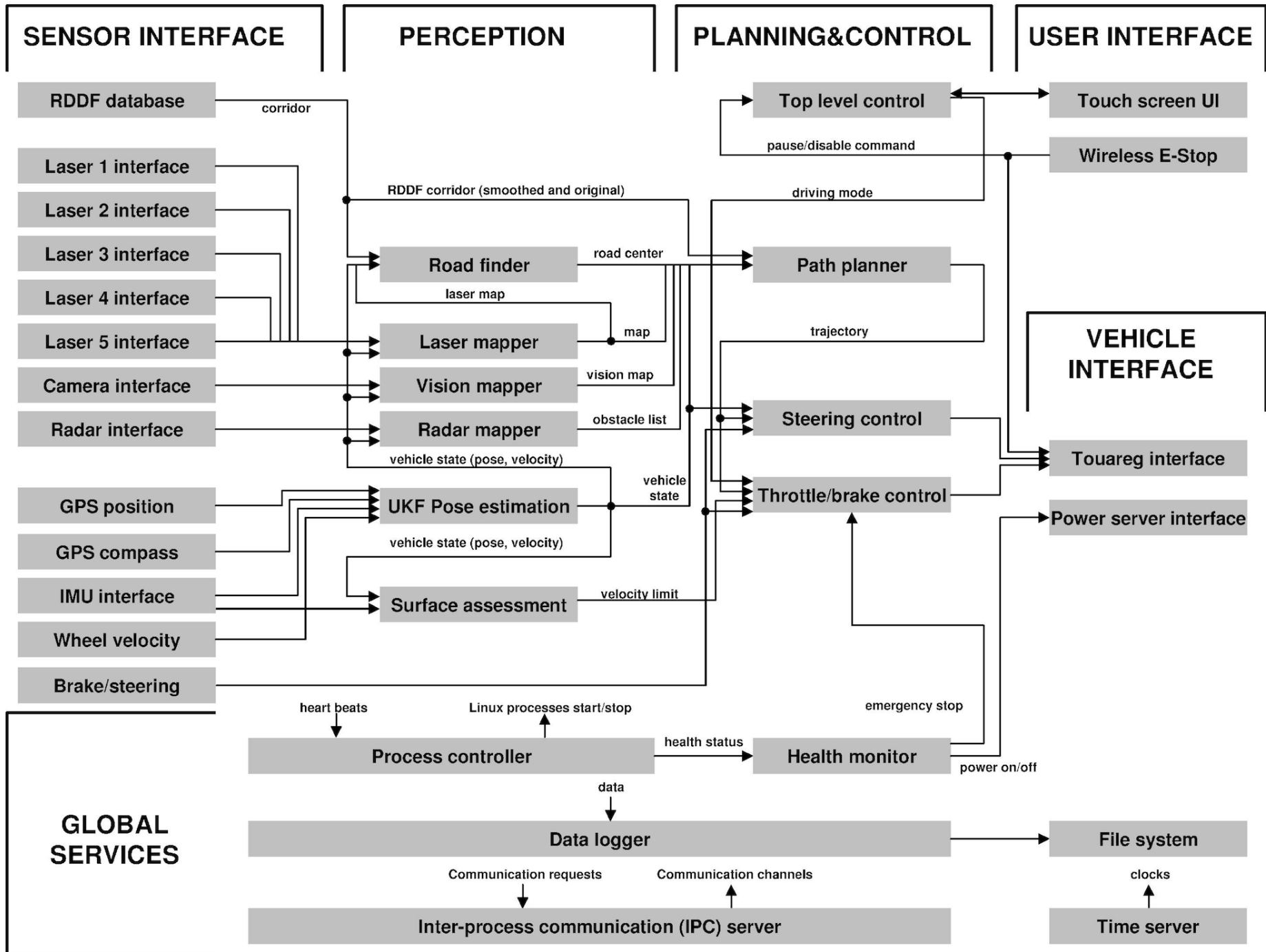
UART
x2

USB LS/FS
x1

Human-Machine
Interface (HMI)

GPIOs
with
interrupt

TSI



Challenge: Embedded real-time programming

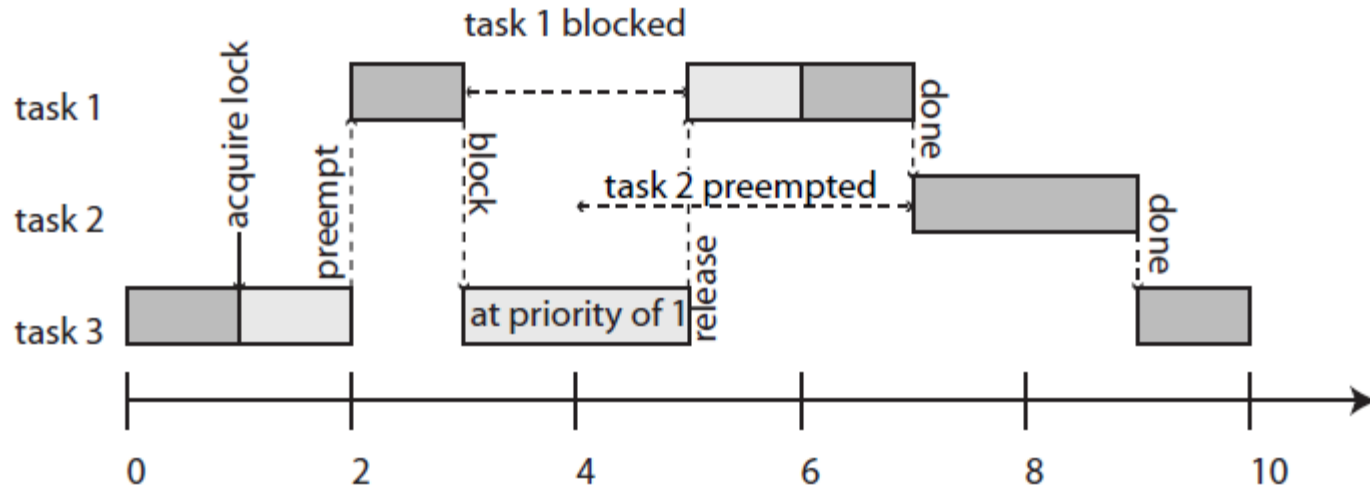
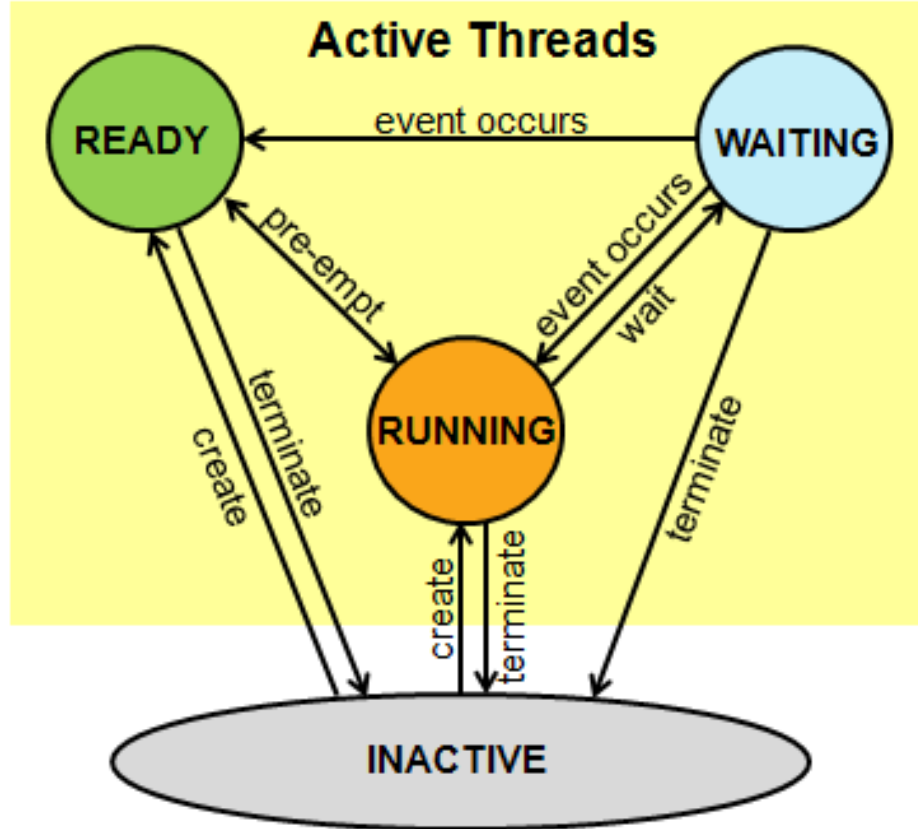
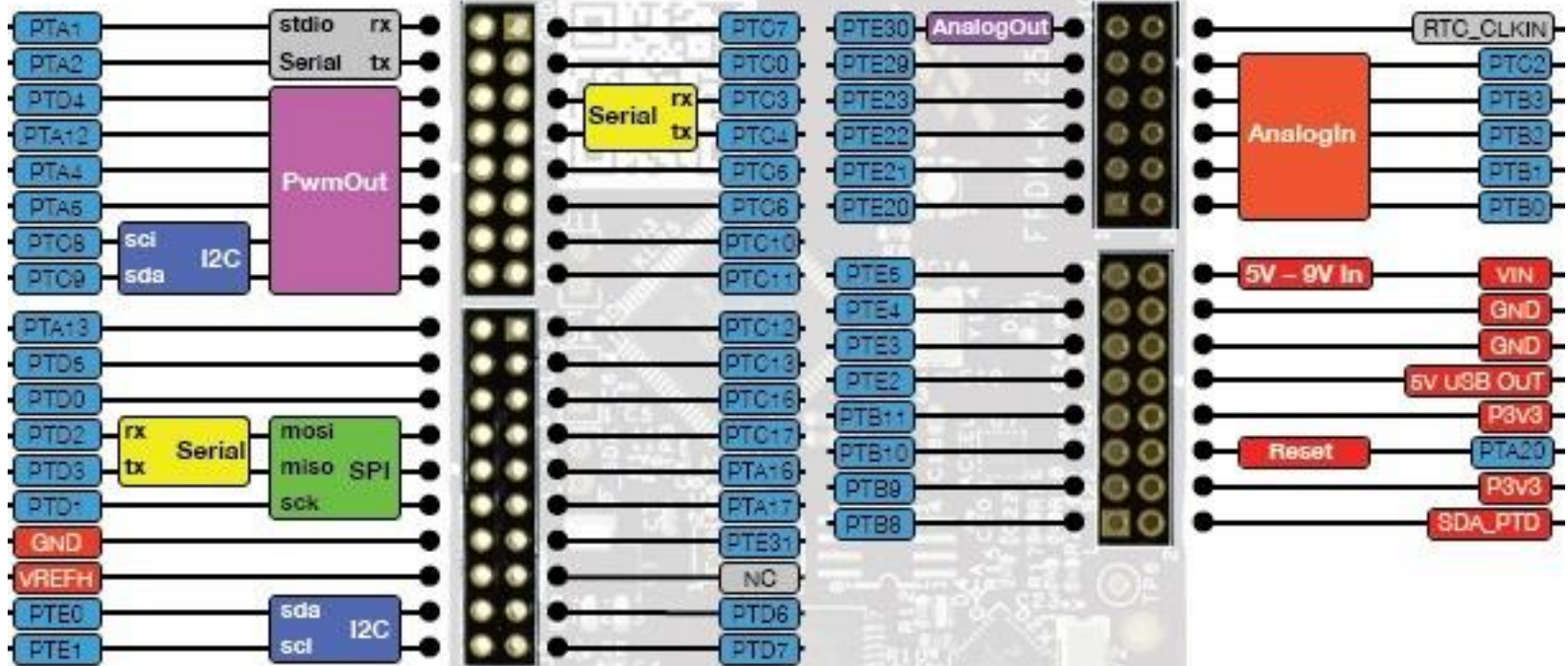


Figure 12.10: Illustration of the priority inheritance protocol. Task 1 has highest priority, task 3 lowest. Task 3 acquires a lock on a shared object, entering a critical section. It gets preempted by task 1, which then tries to acquire the lock and blocks. Task 3 inherits the priority of task 1, preventing preemption by task 2.

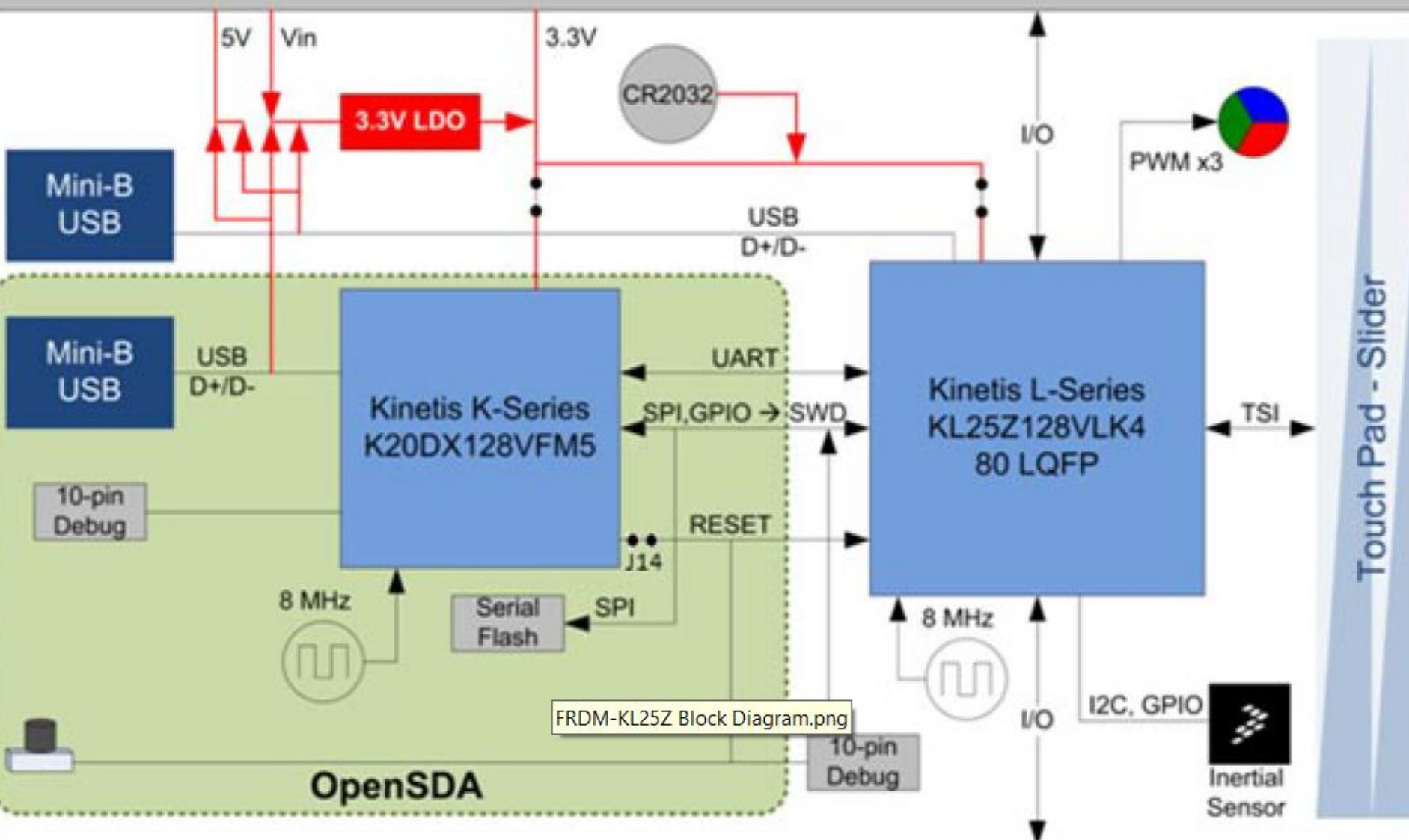


<https://developer.mbed.org/handbook/RTOS>

FRDM KL25Z



I/O Header

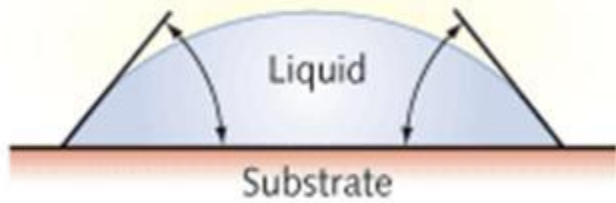


FRDM-KL25Z Block Diagram.png

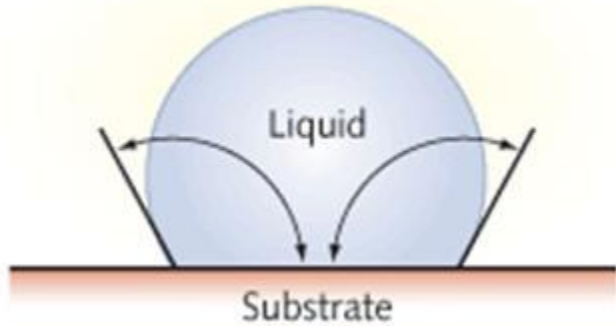
OpenSDA

I/O Header

Solder wetting



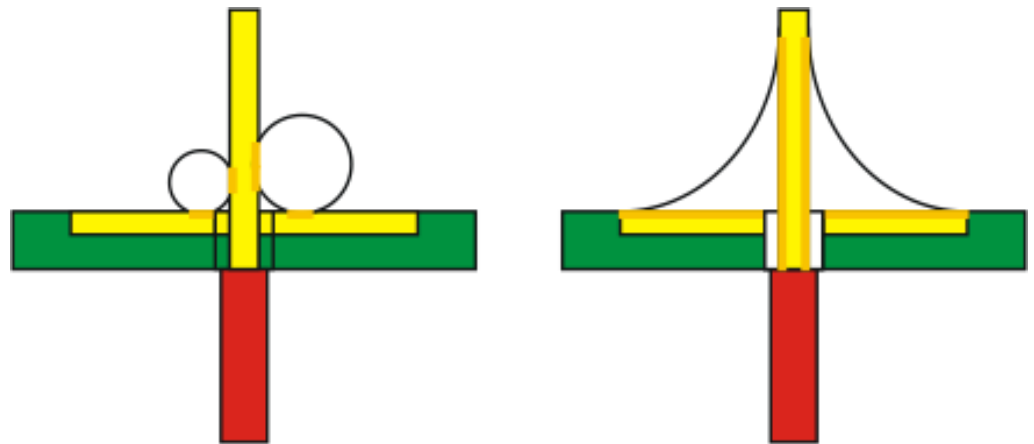
Solder nonwetting



EE192- Soldering Notes

- Oxide has lower energy than clean metal
- Higher energy surfaces attract molten solder
- Oxides have higher melting points than metals
- Oxides have lower thermal conductivity than metals
- Flux helps to prevent oxide formation, but is an insulator

From:
<http://solutions.palomartechnologies.com/Portals/60069/images/Wetting%20vs%20non-wetting%20conditions-resized-600.JPG>

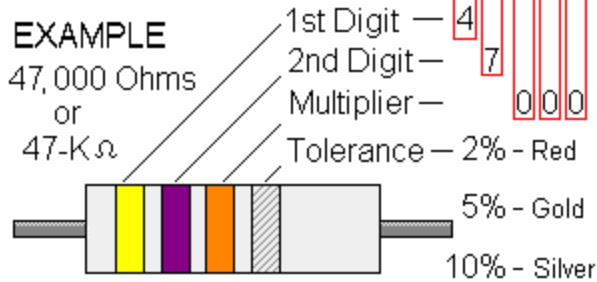


From:http://www.slagcoin.com/joystick/pcb_wiring/bond.png

Resistor Color Code

From:
<http://www.hep.fsu.edu/~wahl/phy3802/expinfo/electronics/res-color-code.gif>

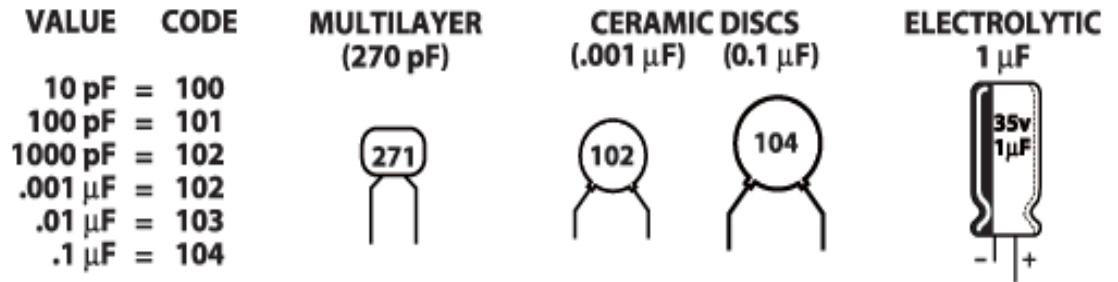
Color	Digit	Multiplier
BLACK	0	_____
BROWN	1	_____0
RED	2	_____00
ORANGE	3	_____000
YELLOW	4	___0,000
GREEN	5	__00,000
BLUE	6	_000,000
VIOLET	7	
GRAY	8	
WHITE	9	



Yellow | violet | orange | gold

Capacitor Codes

From: <http://www.applefritter.com/sites/default/meta/replicacreation/images/fige-10.png>



Better be right or your great big venture goes west...

Capacitor Types-ceramic



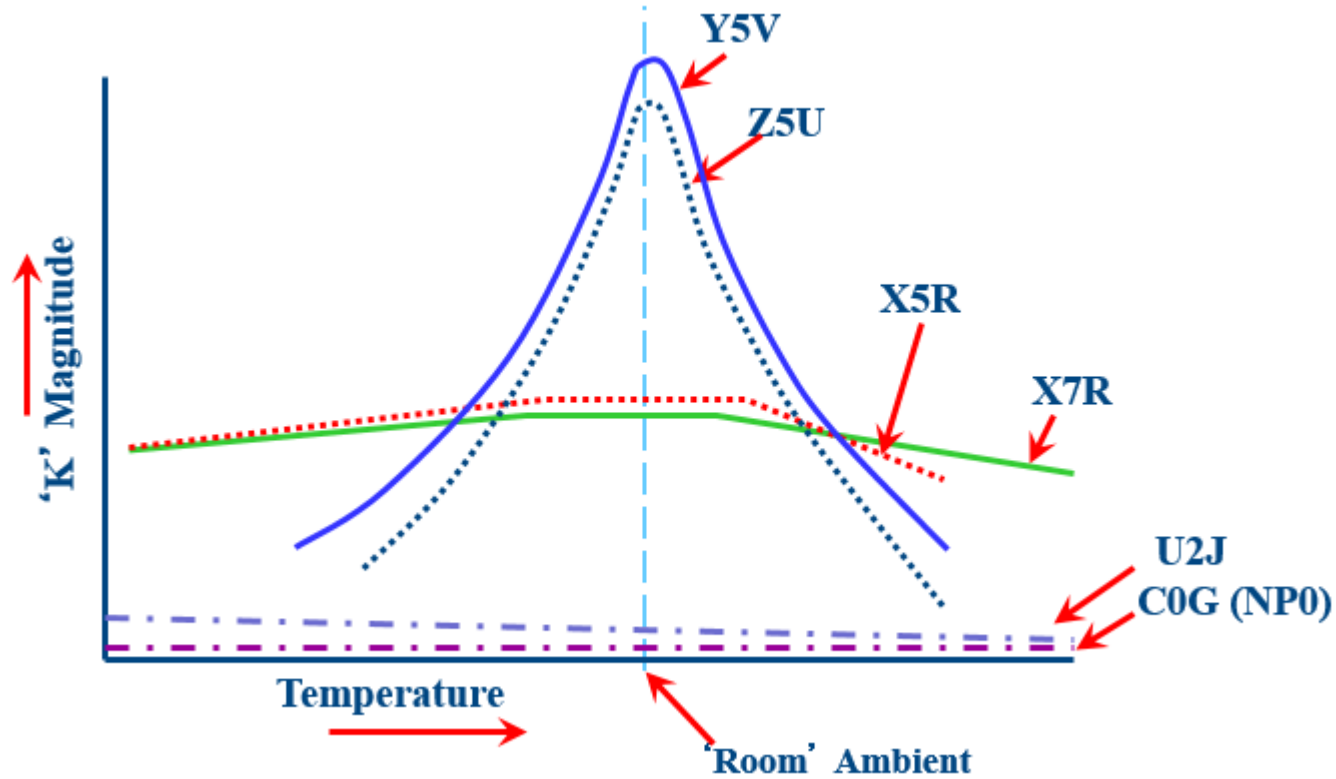
CAP CER 0.1UF 50V X7R RADIAL



0.1 μ F \pm 20% 50V Ceramic Capacitor Z5U Radial



CAP CER 0.1UF 630V X7R RADIAL



<https://ec.kemet.com/wp-content/uploads/2015/12/ceramic-dielectric-comparison-chart.png>



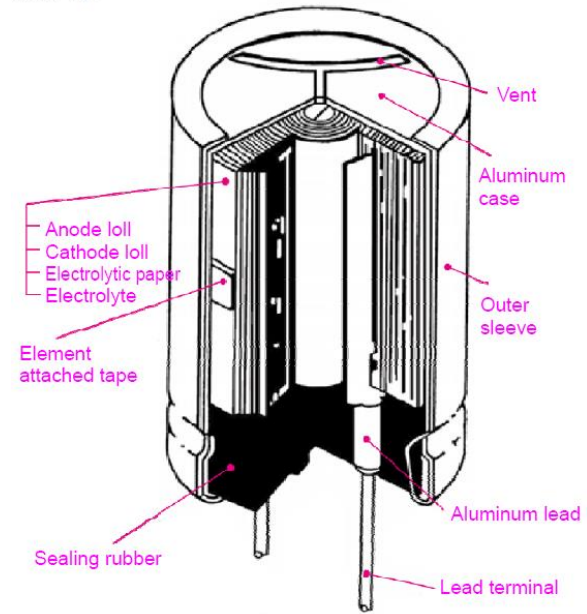
CAP CER 0.1UF 50V X7R 0805

Capacitor Types- 47 uF 50V

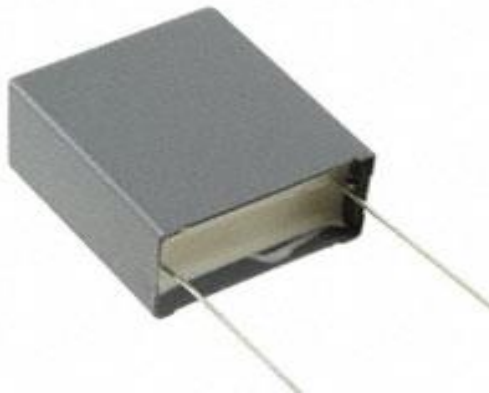


Ripple Current
600mA

Electrolytic Ripple Current
169mA @ 120Hz



<https://industrial.panasonic.com>



Metalized film



CAP TANT 22UF 50V
20% 2917

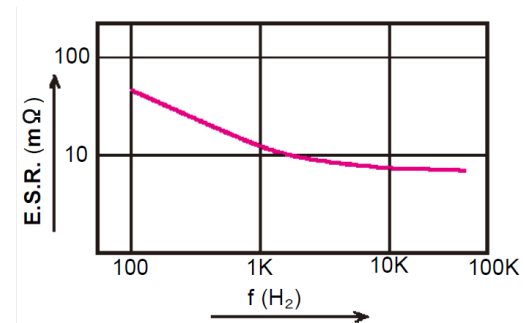


Fig.13 ESR vs frequency