The purpose of the progress report is to give you a chance to evaluate your interim progress, and to set (perhaps more realistic) final goals for the project. Also, it is a good time to document what you have built so far. (In addition, professional technical communication is an important skill to master.) The progress report is a non-trivial amount of work; we would like to see 6 hours of effort per team member. Here is an outline for you to follow (yes, we want you to address all of these points).

1. **Current State of Project (1 page) (20%)**
   What is your current hardware configuration? What types of sensors have you used? How far has your software been developed? Which software modules are currently working? Which still need further debugging? Provide overall block diagrams for the HW systems (ppt is fine for simple block diagrams).

2. **Hardware Documentation (n pages as needed) (45%)**
   This is the most important part of the progress report. Without documentation, power MOSFETS, DC-DC converters, and CPU boards will be fried as connections get inadvertently reversed late at night. Provide complete parts layouts (including component references) and schematics, (schematics should include pin numbers for all chips), of the following hardware modules:
   1. motor drive circuitry (20 pts)
   2. sensor electronics, e.g. speed and line sensors (5 pts)
   3. power supplies (how are things actually connected) (10 pts)
   4. labelled IO and other connections to Beagle Bone Blue Freedom board pinout, e.g. [http://inst.eecs.berkeley.edu/~ee192/sp19/files/BBBluePinout.jpg](http://inst.eecs.berkeley.edu/~ee192/sp19/files/BBBluePinout.jpg) (10 pts)
   
   Appropriate level of detail is Eagle schematic and parts layout. You are welcome to use a schematic entry tool if desired. Example parts layout style (no copper, use white background): [http://people.eecs.berkeley.edu/~ronf/ImageProc/ImageProc2_v24_dual-1.png](http://people.eecs.berkeley.edu/~ronf/ImageProc/ImageProc2_v24_dual-1.png)

3. **Refined Proposal for Software Architecture (2 pages including figures) (30%)**
   a) What do you plan to have implemented and debugged for the preliminary and final contest rounds? (8 pts)
   b) Please describe your proposed software system, including (20 pts):
      i) block diagrams in data flow style.
      ii) Make a timing diagram, e.g. as shown in Lee and Seshia Fig. 12.10 for module execution.
   c) What is your time estimate for development and debugging of the blocks you have not yet implemented? (2 pts)

4. **Additional resources required (0.5 page) (5%)**
   What additional resources will you need to complete your vehicle? Are there additional resources the lab still needs?