EE247
Administrative

- Due to office hour conflict with EE142 class:
  - New office hours:
    - Tues: 4 to 5pm (same as before)
    - Wed.: 10:30 to 11:30am (new)
    - Thurs.: no office hours
  - Office hours held @ 567 Cory Hall

EE247
Course Reading Material

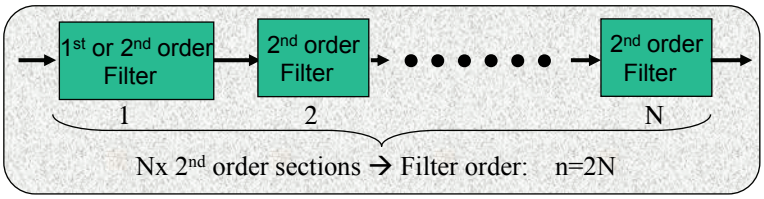
- Note that the class website includes a section named: <u>Reading Material</u> including:
  - List of books (on reserve in the library)
  - List of articles (as pdf files):
    - Articles for Filters
    - Articles for Nyquist Rate Data Converters
    - Articles for Oversampled Data Converters
    - You will be asked to read some of the articles and answer questions
    - If you plan to embark on a career in Mixed Signal Circuit Design consider reading all the publications listed

EE247
Lecture 3

- Active Filters
  - Active biquads
    - How to build higher order filters?
    - Integrator-based filters
      - Signal flowgraph concept
      - First order integrator-based filter
      - Second order integrator-based filter & biquads
  - High order & high Q filters
    - Cascaded biquads & first order filters
      - Cascaded biquad sensitivity to component mismatch
    - Ladder type filters

Higher-Order Filters in the Integrated Form

- One way of building higher-order filters (n>2) is via cascade of 2nd order biquads & 1st order , e.g. Sallen-Key,or Tow-Thomas, & RC



Cascade of 1st and 2nd order filters:

☺ Easy to implement

☹ Highly sensitive to component mismatch -good for low Q filters only

→ For high Q applications good alternative: Integrator-based ladder filters

Integrator Based Filters

- Main building block for this category of filters
  → Integrator
- By using signal flowgraph techniques
  → Conventional RLC filter topologies can be converted to integrator based type filters
- How to design integrator based filters?
  - Introduction to signal flowgraph techniques
  - 1st order integrator based filter
  - 2nd order integrator based filter
  - High order and high Q filters

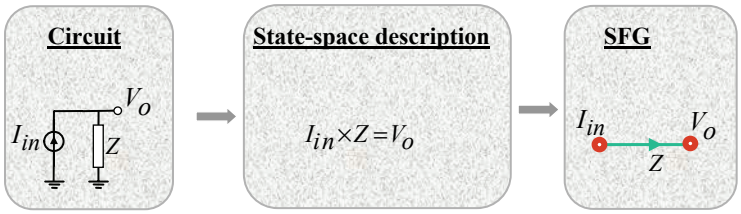
What is a Signal Flowgraph (SFG)?

- SFG → Topological network representation consisting of nodes & branches- used to convert one form of network to a more suitable form (e.g. passive RLC filters to integrator based filters)
- Any network described by a set of linear differential equations can be expressed in SFG form
- For a given network, many different SFGs exists
- Choice of a particular SFG is based on practical considerations such as type of available components

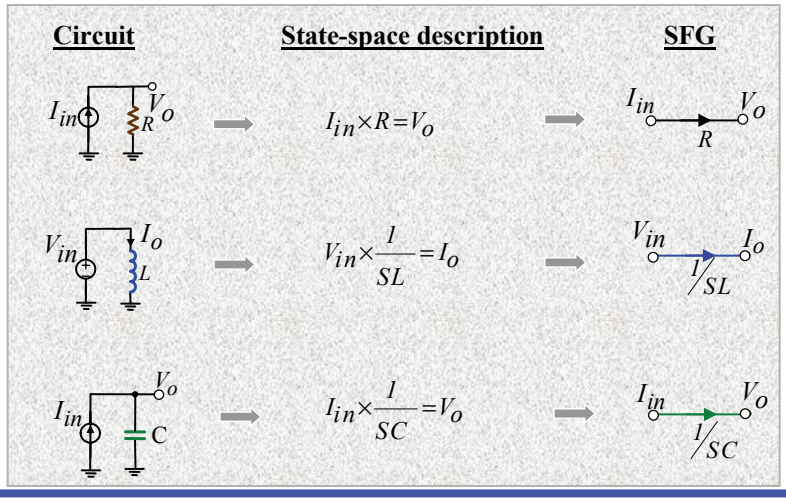
*Ref: W.Heinlein & W. Holmes, "Active Filters for Integrated Circuits", Prentice Hall, Chap. 8, 1974.

What is a Signal Flowgraph (SFG)?

- Signal flowgraph technique consist of **nodes & branches**:
  - **Nodes** represent variables (*V* & *I* in our case)
  - **Branches** represent transfer functions (we will call the transfer function *branch multiplication factor* or *BMF*)
- To convert a network to its SFG form, *KCL* & *KVL* is used to derive state space description
- Simple example:

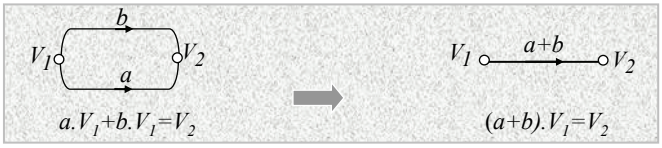


Signal Flowgraph (SFG) Examples

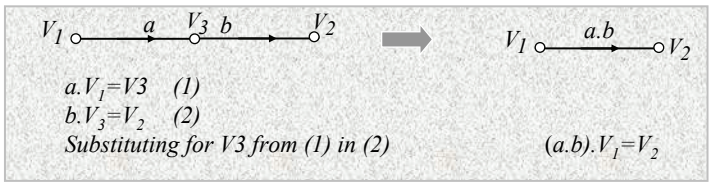


Useful Signal Flowgraph (SFG) Rules

- Two parallel branches can be replaced by a single branch with overall *BMF* equal to **sum** of two *BMF*s

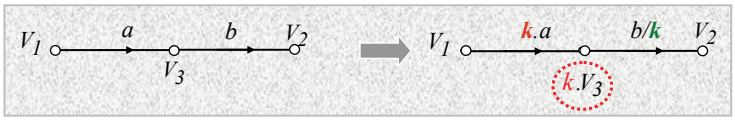


- A node with only one incoming branch & one outgoing branch can be eliminated & replaced by a single branch with *BMF* equal to the **product** of the two *BMF*s



Useful Signal Flowgraph (SFG) Rules

- An intermediate node can be multiplied by a factor (*k*). *BMF*s for incoming branches have to be multiplied by *k* and outgoing branches divided by *k*



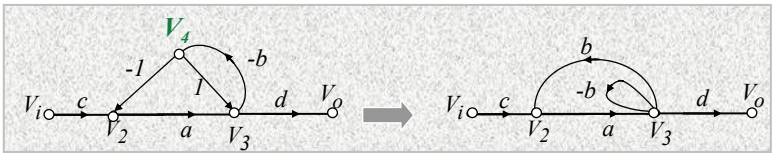
$a.V_1=V_3$ (1)
$b.V_3=V_2$ (2)

Multiply both sides of *(1)* by ***k***
$(a.k)\ .\ V_1=k.V_3$ (1)

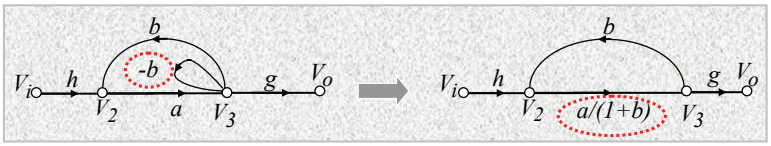
Divide & multiply left side of *(2)* by *k*
$(b/k)\ .\ k.V_3 = V_2$ (2)

Useful Signal Flowgraph (SFG) Rules

- Simplifications can often be achieved by shifting or eliminating nodes
- Example: eliminating node V_4

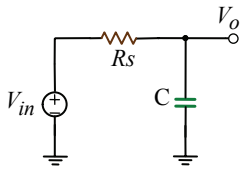


- A self-loop branch with *BMF* ***y*** can be eliminated by multiplying the *BMF* of <u>incoming</u> branches by ***1/(1-y)***



Integrator Based Filters
1st Order LPF

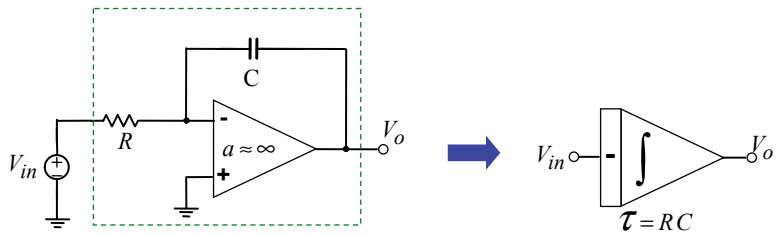
- Conversion of simple lowpass RC filter to integrator-based type by using signal flowgraph techniques



$$\frac{V_o}{V_{in}} = \frac{1}{1+sRC}$$

What is an Integrator?

Example: Single-Ended Opamp-RC Integrator



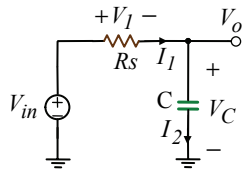
$$\frac{V_{in}}{R} = -V_o s C = \quad , \quad V_o = -V_{in} \times \frac{1}{sRC} \quad , \quad V_o = -\frac{1}{RC}\int V_{in}\, dt$$

Note: Practical integrator in CMOS technology has input & output both in the form of voltage and not current → Consideration for SFG derivation

Integrator Based Filters

1st Order LPF

1. Start from circuit prototype-
   Name voltages & currents for all components



2. Use KCL & KVL to derive state space description in such a way to have BMFs in the integrator form:
   → Capacitor voltage expressed as function of its current $V_{Cap.}=f(I_{Cap.})$
   → Inductor current as a function of its voltage $I_{Ind.}=f(V_{Ind.})$

3. Use state space description to draw signal flowgraph (SFG) (see next page)

Integrator Based Filters

First Order LPF

$$V_1 = V_{in} - V_C$$

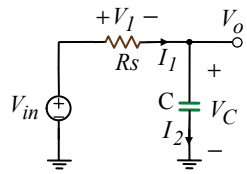
$$V_C = I_2 \times \frac{1}{sC}$$

Integrator form

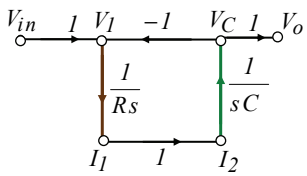
$$V_o = V_C$$

$$I_1 = V_1 \times \frac{1}{Rs}$$

$$I_2 = I_1$$



SFG



- All voltages & currents → nodes of SFG
- Voltage nodes on top, corresponding current nodes below each voltage node

Normalize

- Since integrators are the main building blocks→ require in & out signals in the form of voltage (not current)
  - → Convert all currents to voltages by multiplying current nodes by a scaling resistance R^*
  - → Corresponding *BMF*s should then be scaled accordingly

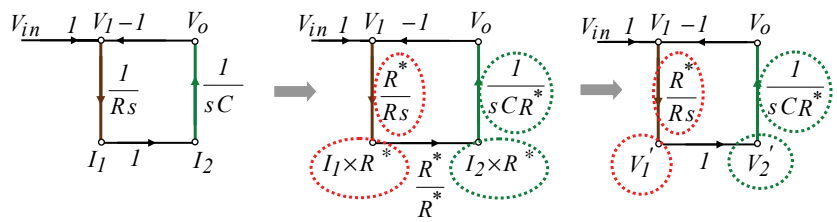
$$V_1 = V_{in} - V_o \qquad I_1 = \frac{V_1}{Rs} \qquad V_o = \frac{I_2}{sC} \qquad I_2 = I_1$$

$$V_1 = V_{in} - V_o \qquad I_1 R^* = \frac{R^*}{Rs} V_1 \qquad V_o = \frac{I_2 R^*}{sC\,R^*} \qquad I_2 R^* = I_1 R^*$$

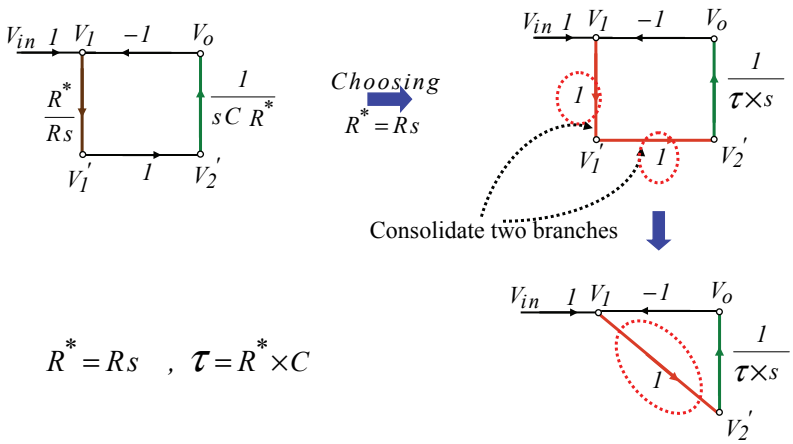
$$\boxed{I_x R^* = V_x'}$$

$$V_1 = V_{in} - V_o \qquad V_1' = \frac{R^*}{Rs} V_1 \qquad V_o = \frac{V_2'}{sC\,R^*} \qquad V_2' = V_1'$$

1st Order Lowpass Filter SGF Normalize

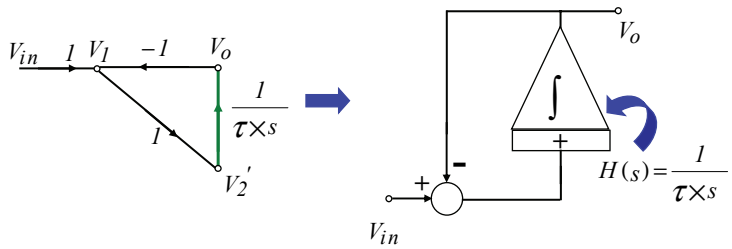


1st Order Lowpass Filter SGF Synthesis



$$R^* = Rs \quad , \quad \tau = R^* \times C$$

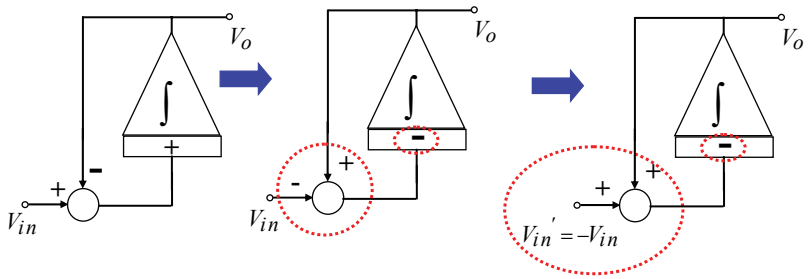
First Order Integrator Based Filter



1st Order Filter Built with Opamp-RC Integrator

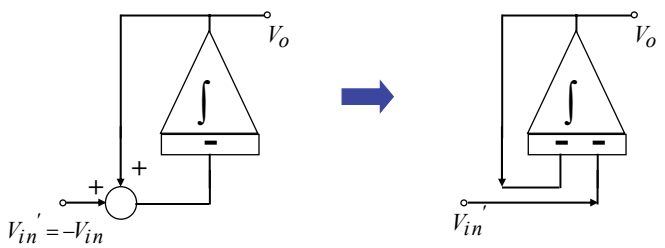
- Single-ended Opamp-RC integrator has a sign inversion from input to output

  → Convert SFG accordingly by modifying BMF

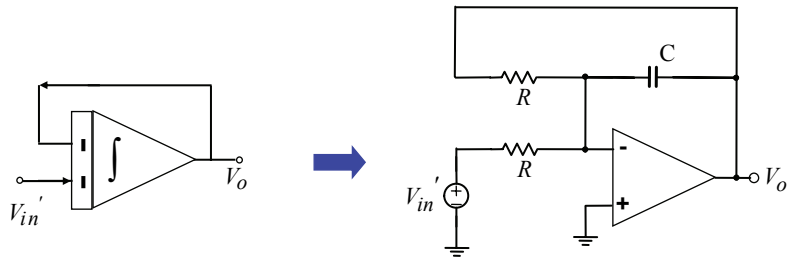


1st Order Filter
Built with Opamp-RC Integrator

- To avoid requiring an additional opamp to perform summation at the input node:



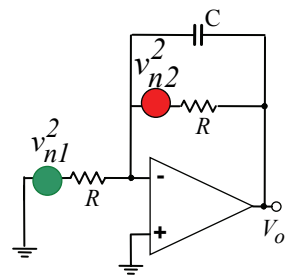
1st Order Filter
Built with Opamp-RC Integrator (continued)



$$\frac{V_o}{V_{in}'} = -\frac{1}{1+sRC}$$

Opamp-RC 1st Order Filter Noise

Identify noise sources (here it is resistors & opamp)
Find transfer function from each noise source to the output (opamp noise next page)



$$\overline{v_o^2} = \sum_{m=1}^{k} \int_0^\infty |H_m(f)|^2 S_m(f)\, df$$

$$S_i(f) \rightarrow Noise\ spectral\ density\ of\ i^{th}\ noise\ source$$

$$|H_1(f)|^2 = |H_2(f)|^2 = \frac{1}{1+(2\pi f R C)^2}$$

$$v_{n1}^2 = v_{n2}^2 = 4KTR\Delta f$$

$$\sqrt{\overline{v_o^2}} = \sqrt{2\frac{kT}{C}}$$

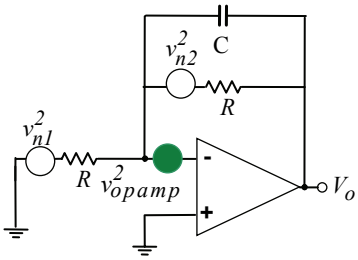
$\alpha = 2$

α

Typically, α increases as filter order increases

Opamp-RC Filter Noise Opamp Contribution

- So far only the fundamental noise sources are considered
- In reality, noise associated with the opamp increases the overall noise
- For a well-designed filter opamp is designed such that noise contribution of opamp is negligible compared to other noise sources
- The bandwidth of the opamp affects the opamp noise contribution to the total noise



Integrator Based Filter
2nd Order RLC Filter

•State space description:

$$V_R = V_L = V_C = V_o$$

$$V_C = \frac{I_C}{sC}$$

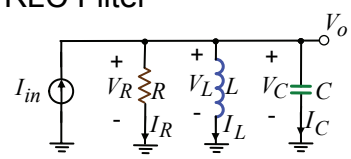
$$I_R = \frac{V_R}{R}$$

$$I_L = \frac{V_L}{sL}$$

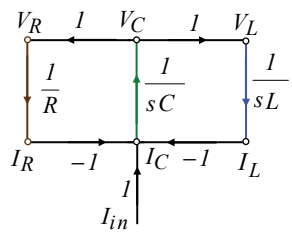
$$I_C = I_{in} - I_R - I_L$$

Integrator form

• Draw signal flowgraph (SFG)

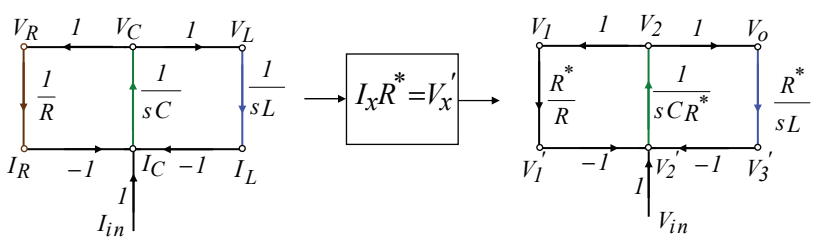


SFG

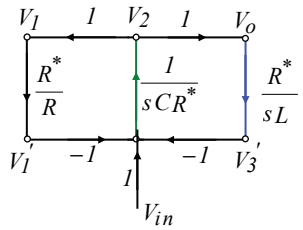


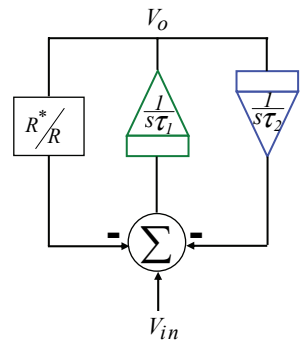
2nd Order RLC Filter SGF
Normalize

- Convert currents to voltages by multiplying all current nodes by the scaling resistance R^*



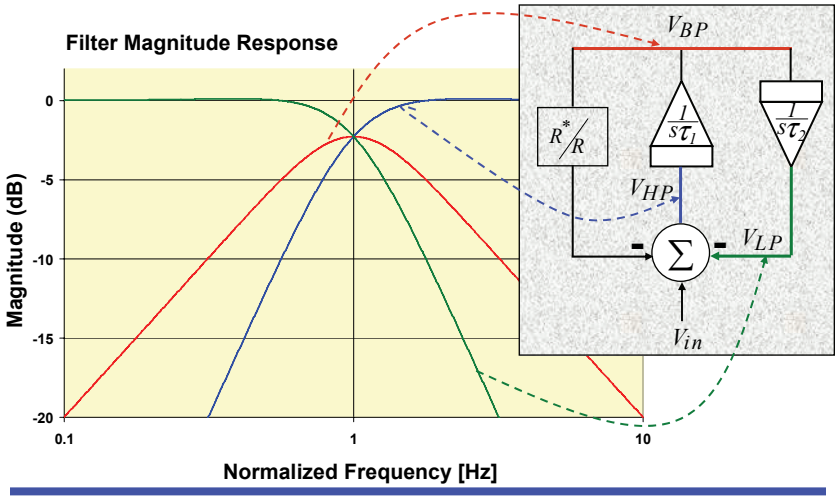
2nd Order RLC Filter SGF Synthesis





$\tau_1 = R^* \times C \qquad \tau_2 = L / R^*$

Second Order Integrator Based Filter



Second Order Integrator Based Filter

$$\frac{V_{BP}}{V_{in}} = \frac{\tau_2 s}{\tau_1\tau_2 s^2 + \beta\tau_2 s + 1}$$

$$\frac{V_{LP}}{V_{in}} = \frac{1}{\tau_1\tau_2 s^2 + \beta\tau_2 s + 1}$$

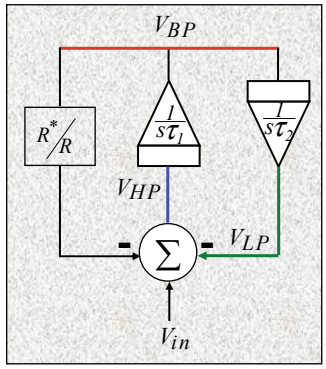
$$\frac{V_{HP}}{V_{in}} = \frac{\tau_1\tau_2 s^2}{\tau_1\tau_2 s^2 + \beta\tau_2 s + 1}$$

$$\tau_1 = R^* \times C \qquad \tau_2 = L / R^*$$

$$\beta = R^* / R$$

$$\omega_0 = 1 / \sqrt{\tau_1\tau_2} = 1 / \sqrt{L\,C}$$

$$Q = 1/\beta \times \sqrt{\tau_1 / \tau_2}$$



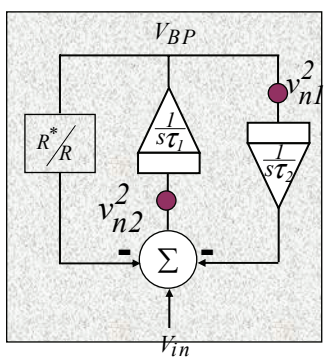
From matching point of view desirable:

$$\tau_1 = \tau_2 \rightarrow Q = R / R^*$$

Second Order Bandpass Filter Noise

$$\overline{v_o^2} = \sum_{m=1}^{k} \int_0^\infty |H_m(f)|^2 S_m(f)\, df$$

- Find transfer function of each noise source to the output
- Integrate contribution of all noise sources
- Here it is assumed that opamps are noise free (not usually the case!)



$$v_{n1}^2 = v_{n2}^2 = 4KTRdf$$

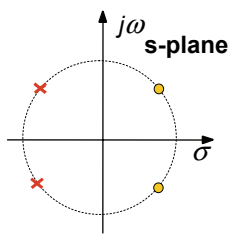
$$\boxed{\sqrt{\overline{v_o^2}} = \sqrt{\underbrace{2\,Q}_{\alpha}\,\frac{k\,T}{C}}}$$

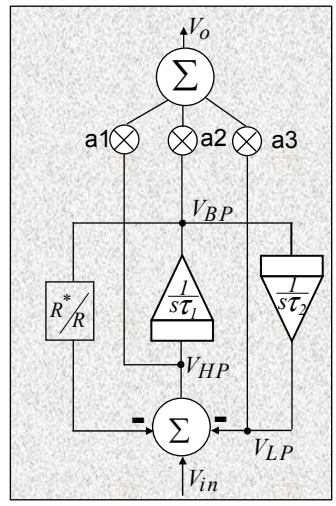
Typically, α increases as filter order increases
Note the noise power is directly proportion to Q

Second Order Integrator Based Filter Biquad

- By combining outputs can generate general biquad function:

$$\frac{V_0}{V_{in}} = \frac{a_1 \tau_1 \tau_2 s^2 + a_2 \tau_2 s + a_3}{\tau_1 \tau_2 s^2 + \beta \tau_2 s + 1}$$





Summary
Integrator Based Monolithic Filters

- Signal flowgraph techniques utilized to convert RLC networks to integrator based active filters
- Each reactive element (L& C) replaced by an integrator
- Fundamental noise limitation determined by integrating capacitor value:
  - For lowpass filter: $\sqrt{\overline{v_o^2}} = \sqrt{\alpha \frac{k\,T}{C}}$
  - Bandpass filter: $\sqrt{\overline{v_o^2}} = \sqrt{\alpha\, Q\, \frac{k\,T}{C}}$

where α is a function of filter order and topology

Higher Order Filters

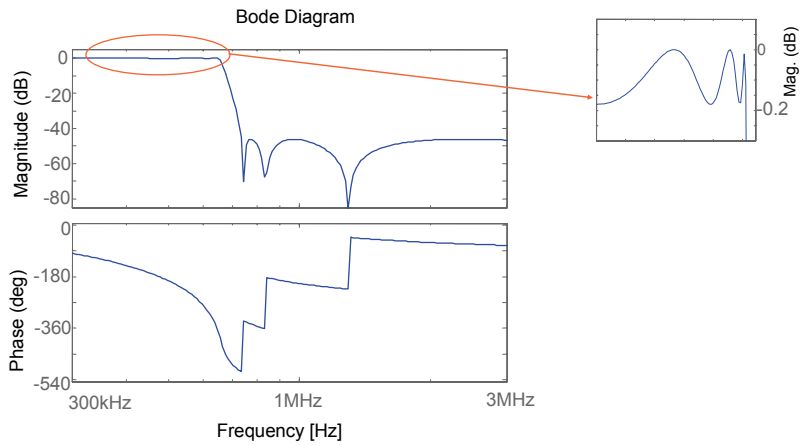
- How do we build higher order filters?
  - Cascade of biquads and 1st order sections
    - Each complex conjugate pole built with a biquad and real pole with 1st order section
    - Easy to implement
    - In the case of high order high Q filters → highly sensitive to component mismatch
  - Direct conversion of high order ladder type RLC filters
    - SFG techniques used to perform exact conversion of ladder type filters to integrator based filters
    - More complicated conversion process
    - Much less sensitive to component mismatch compared to cascade of biquads

Higher Order Filters
Cascade of Biquads

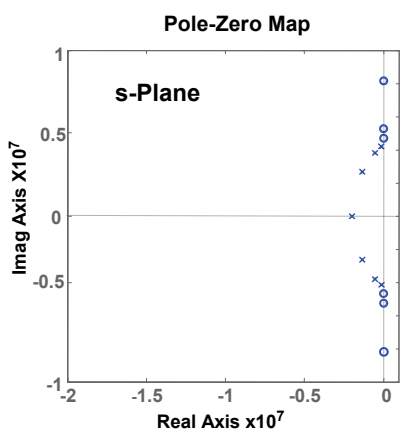
Example: LPF filter for CDMA cell phone baseband receiver

- LPF with
  - fpass = 650 kHz Rpass = 0.2 dB
  - fstop = 750 kHz Rstop = 45 dB
  - Assumption: Can compensate for phase distortion in the digital domain
- Matlab used to find minimum order required → 7th order Elliptic Filter
- Implementation with cascaded Biquads
  Goal: Maximize dynamic range
  - Pair poles and zeros
  - In the cascade chain place lowest Q poles first and progress to higher Q poles moving towards the output node

Overall Filter Frequency Response



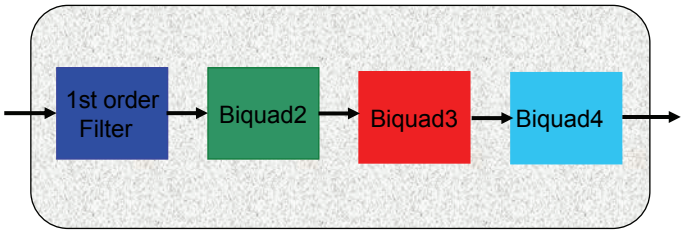
Pole-Zero Map (pzmap in Matlab)



Q_{pole}	f_{pole} [kHz]
16.7902	659.496
3.6590	611.744
1.1026	473.643
	319.568

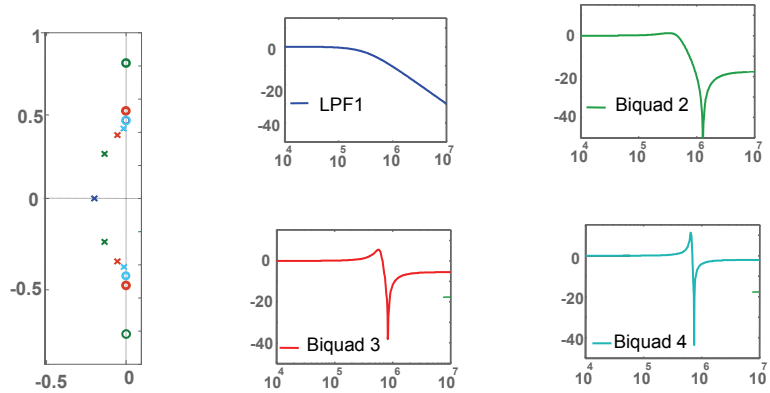
f_{zero} [kHz]
1297.5
836.6
744.0

CDMA Filter
Built with Cascade of 1st and 2nd Order Sections

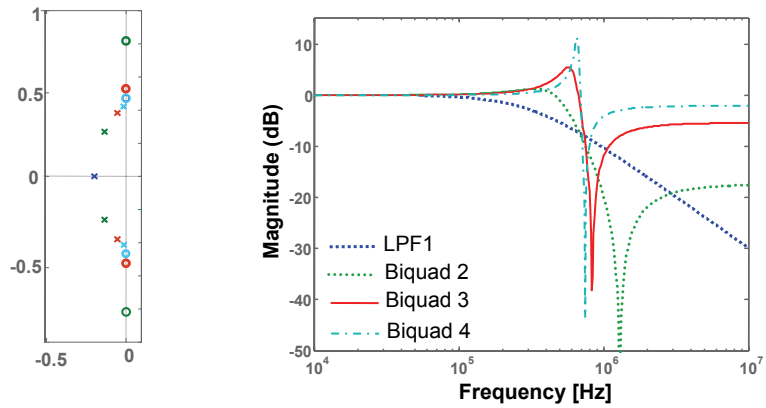


- 1st order filter implements the single real pole
- Each biquad implements a pair of complex conjugate poles and a pair of imaginary axis zeros

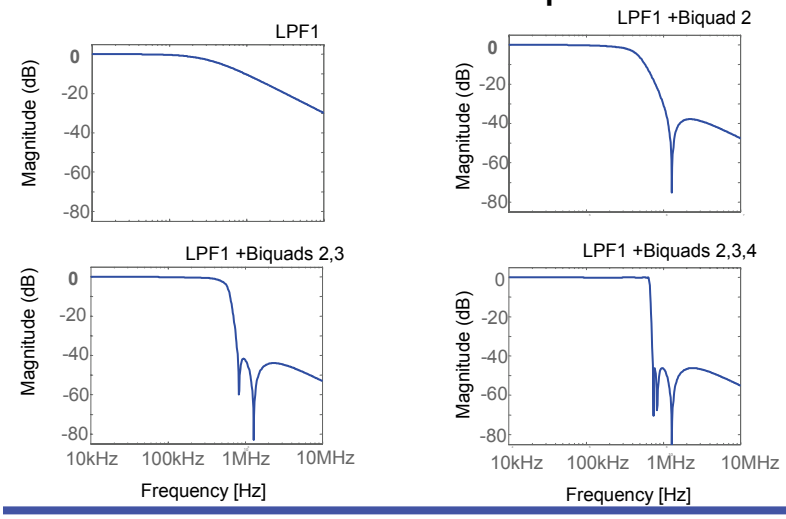
Biquad Response



Individual Stage Magnitude Response



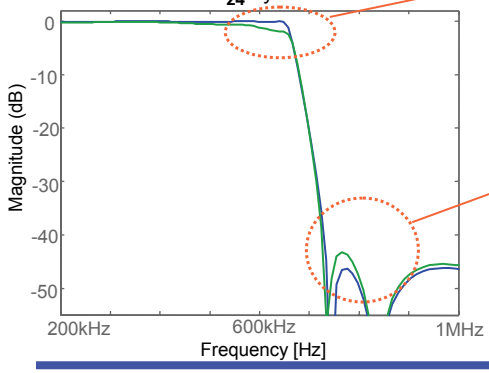
Intermediate Outputs

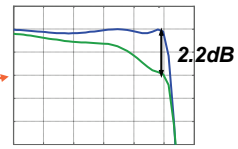


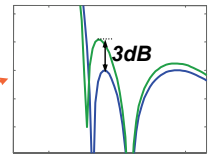
Sensitivity to Relative Component Mismatch

Component variation in Biquad 4 relative to the rest (highest Q poles):

- Increase ω_{p4} by 1%
- Decrease ω_{z4} by 1%







High Q poles → High sensitivity in Biquad realizations

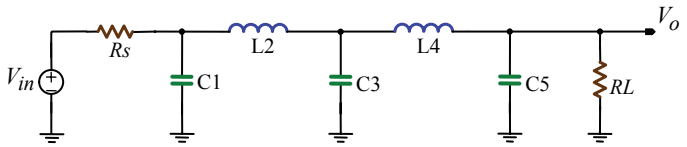
High Q & High Order Filters

- Cascade of biquads
  - Highly sensitive to component mismatch → not suitable for implementation of high Q & high order filters
  - Cascade of biquads only used in cases where required Q for all biquads <4 (e.g. filters for disk drives)
- Ladder type filters more appropriate for high Q & high order filters (next topic)
  - Will show later →Less sensitive to component mismatch

Ladder Type Filters

- Active ladder type filters
  - For simplicity, will start with all pole ladder type filters
    - Convert to integrator based form- example shown
  - Then will attend to high order ladder type filters incorporating zeros
    - Implement the same 7th order elliptic filter in the form of ladder RLC with zeros
      - Find level of sensitivity to component mismatch
      - Compare with cascade of biquads
    - Convert to integrator based form utilizing SFG techniques
  - Effect of integrator non-Idealities on filter frequency characteristics

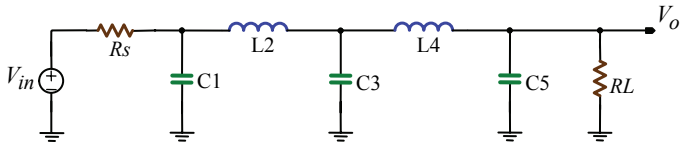
RLC Ladder Filters
Example: 5th Order Lowpass Filter



- Made of resistors, inductors, and capacitors
- Doubly terminated or singly terminated (with or w/o R_L)

Doubly terminated LC ladder filters → Lowest sensitivity to component mismatch

LC Ladder Filters



- First step in the design process is to find values for Ls and Cs based on specifications:
  - Filter graphs & tables found in:
    - A. Zverev, *Handbook of filter synthesis*, Wiley, 1967.
    - A. B. Williams and F. J. Taylor, *Electronic filter design*, 3rd edition, McGraw-Hill, 1995.
  - CAD tools
    - Matlab
    - Spice

LC Ladder Filter Design Example

Design a LPF with maximally flat passband:

f-3dB = 10MHz, fstop = 20MHz

Rs >27dB @ fstop

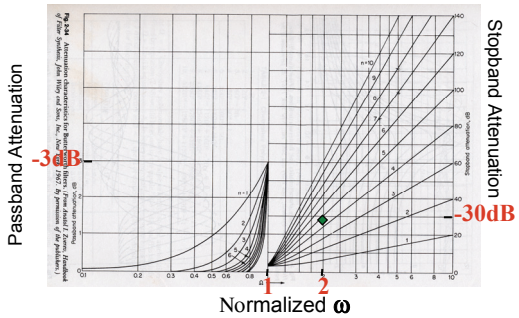
- Maximally flat passband → Butterworth
  - Find minimum filter order :
  - Here standard graphs from filter books are used

fstop / f-3dB = 2

Rs >27dB

Minimum Filter Order

⇨5th order Butterworth



From: Williams and Taylor, p. 2-37

LC Ladder Filter Design Example

Find values for L & C from Table:

Note L &C values normalized to

$\omega_{-3dB} = 1$

Denormalization:

Multiply all L_{Norm}, C_{Norm} by:

$L_r = R/\omega_{-3dB}$

$C_r = 1/(R \times \omega_{-3dB})$

R is the value of the source and termination resistor (choose both 1Ω for now)

Then: $L = L_r \times L_{Norm}$

$C = C_r \times C_{Norm}$

NORMALIZED FILTER DESIGN TABLES 11.3

TABLE 11-2 Butterworth *LC* Element Values (*Continued*)

n	R_s	C_1	L_2	C_3	L_4	C_5	L_6	C_7
5	1.0000	0.6180	1.6180	2.0000	1.6180	0.6180		
	0.9000	0.4416	1.0265	1.9095	1.7562	1.3887		
	0.8000	0.4698	0.8660	2.0605	1.5443	1.7380		
	0.7000	0.5173	0.7313	2.2849	1.3326	2.1083		
	0.6000	0.5860	0.6094	2.5998	1.1255	2.5524		
	0.5000	0.6857	0.4955	3.0510	0.9237	3.1331		
	0.4000	0.8378	0.3877	3.7357	0.7274	3.9648		
	0.3000	1.0937	0.2848	4.8835	0.5367	5.3073		
	0.2000	1.6077	0.1861	7.1849	0.3518	7.9345		
	0.1000	3.1522	0.0912	14.0945	0.1727	15.7103		
	Inf.	1.5451	1.6944	1.3820	0.8944	0.3090		
6	1.0000	0.5176	1.4142	1.9319	1.9319	1.4142	0.5176	
	1.1111	0.2890	1.0403	1.3217	2.0539	1.7443	1.3347	
	1.2500	0.2445	1.1163	1.1257	2.2389	1.5498	1.6881	
	1.4286	0.2072	1.2363	0.9567	2.4991	1.3464	2.0618	
	1.6667	0.1732	1.4071	0.8011	2.8580	1.1431	2.5092	
	2.0000	0.1412	1.6531	0.6542	3.3687	0.9423	3.0938	
	2.5000	0.1108	2.0275	0.5139	4.1408	0.7450	3.9305	
	3.3333	0.0816	2.6559	0.3788	5.4325	0.5517	5.2804	
	5.0000	0.0535	3.9170	0.2484	8.0201	0.3628	7.9216	
	10.0000	0.0263	7.7053	0.1222	15.7855	0.1788	15.7375	
	Inf.	1.5529	1.7593	1.5529	1.2016	0.7579	0.2588	
7	1.0000	0.4450	1.2470	1.8019	2.0000	1.8019	1.2470	0.4450
	0.9000	0.2985	0.7111	1.4043	1.4891	2.1249	1.7268	1.2961
	0.8000	0.3215	0.6057	1.5174	1.2777	2.3338	1.5461	1.6520
	0.7000	0.3571	0.5154	1.6883	1.0910	2.6177	1.3498	2.0277
	0.6000	0.4075	0.4322	1.9284	0.9170	3.0050	1.1503	2.4771
	0.5000	0.4799	0.3536	2.2726	0.7512	3.5532	0.9513	3.0640
	0.4000	0.5899	0.2782	2.7950	0.5917	4.3799	0.7542	3.9037
	0.3000	0.7745	0.2055	3.6706	0.4373	5.7612	0.5600	5.2583
	0.2000	1.1448	0.1350	5.4267	0.2874	8.5263	0.3692	7.9079
	0.1000	2.2571	0.0665	10.7004	0.1417	16.8222	0.1823	15.7480
	Inf.	1.5576	1.7988	1.6588	1.3972	1.0550	0.6560	0.2225
n	$1/R_s$	L_1	C_2	L_3	C_4	L_5	C_6	L_7

From: Williams and Taylor, p. 11.3

LC Ladder Filter Design Example

Find values for L & C from Table:

Normalized values:

$C1_{Norm} = C5_{Norm} = 0.618$

$C3_{Norm} = 2.0$

$L2_{Norm} = L4_{Norm} = 1.618$

Denormalization:

Since $\omega_{-3dB} = 2\pi \times 10MHz$

$L_r = R/\omega_{-3dB} = 15.9$ nH

$C_r = 1/(R \times \omega_{-3dB}) = 15.9$ nF

R =1

⇨C1=C5=9.836nF, C3=31.83nF

⇨L2=L4=25.75nH

NORMALIZED FILTER DESIGN TABLES 11.3

TABLE 11-2 Butterworth *LC* Element Values (*Continued*)

n	R_s	C_1	L_2	C_3	L_4	C_5	L_6	C_7
5	1.0000	0.6180	1.6180	2.0000	1.6180	0.6180		
	0.9000	0.4416	1.0265	1.9095	1.7562	1.3887		
	0.8000	0.4698	0.8660	2.0605	1.5443	1.7380		
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	0.6000	0.5860	0.6094	2.5998	1.1255	2.5524		
	0.5000	0.6857	0.4955	3.0510	0.9237	3.1331		
	0.4000	0.8378	0.3877	3.7357	0.7274	3.9648		
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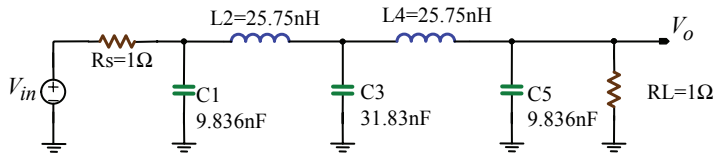
	0.1000	3.1522	0.0912	14.0945	0.1727	15.7103		
	Inf.	1.5451	1.6944	1.3820	0.8944	0.3090		
6	1.0000	0.5176	1.4142	1.9319	1.9319	1.4142	0.5176	
	1.1111	0.2890	1.0403	1.3217	2.0539	1.7443	1.3347	
	1.2500	0.2445	1.1163	1.1257	2.2389	1.5498	1.6881	
	1.4286	0.2072	1.2363	0.9567	2.4991	1.3464	2.0618	
	1.6667	0.1732	1.4071	0.8011	2.8580	1.1431	2.5092	
	2.0000	0.1412	1.6531	0.6542	3.3687	0.9423	3.0938	
	2.5000	0.1108	2.0275	0.5139	4.1408	0.7450	3.9305	
	3.3333	0.0816	2.6559	0.3788	5.4325	0.5517	5.2804	
	5.0000	0.0535	3.9170	0.2484	8.0201	0.3628	7.9216	
	10.0000	0.0263	7.7053	0.1222	15.7855	0.1788	15.7375	
	Inf.	1.5529	1.7593	1.5529	1.2016	0.7579	0.2588	
7	1.0000	0.4450	1.2470	1.8019	2.0000	1.8019	1.2470	0.4450
	0.9000	0.2985	0.7111	1.4043	1.4891	2.1249	1.7268	1.2961
	0.8000	0.3215	0.6057	1.5174	1.2777	2.3338	1.5461	1.6520
	0.7000	0.3571	0.5154	1.6883	1.0910	2.6177	1.3498	2.0277
	0.6000	0.4075	0.4322	1.9284	0.9170	3.0050	1.1503	2.4771
	0.5000	0.4799	0.3536	2.2726	0.7512	3.5532	0.9513	3.0640
	0.4000	0.5899	0.2782	2.7950	0.5917	4.3799	0.7542	3.9037
	0.3000	0.7745	0.2055	3.6706	0.4373	5.7612	0.5600	5.2583
	0.2000	1.1448	0.1350	5.4267	0.2874	8.5263	0.3692	7.9079
	0.1000	2.2571	0.0665	10.7004	0.1417	16.8222	0.1823	15.7480
	Inf.	1.5576	1.7988	1.6588	1.3972	1.0550	0.6560	0.2225
n	$1/R_s$	L_1	C_2	L_3	C_4	L_5	C_6	L_7

From: Williams and Taylor, p. 11.3

Last Lecture:
Example: 5th Order Butterworth Filter



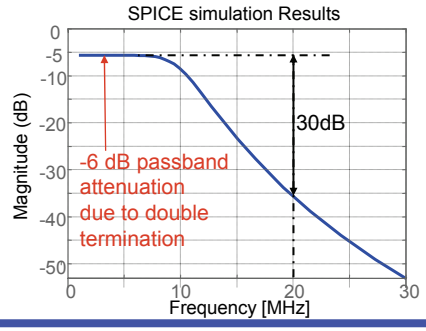
Specifications:

f-3dB = 10MHz,

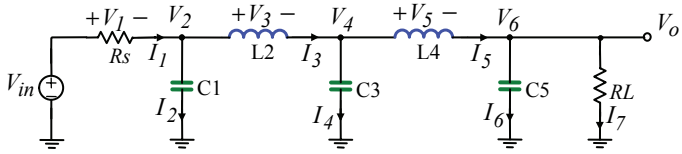
fstop = 20MHz

Rs >27dB

Used filter tables to obtain Ls & Cs

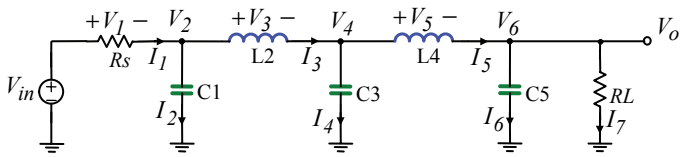


Low-Pass RLC Ladder Filter
Conversion to Integrator Based Active Filter



- To convert RLC ladder prototype to integrator based filer:
  - ❑Use Signal Flowgraph technique
    - ✓Name currents and voltages for all components
    - ✓Use KCL & KVL to derive equations
    - ✓Make sure reactive elements expressed as 1/s terms
    - → *V(C) =f(I) & I(L)=f(V)*
    - ✓Use state-space description to derive the SFG
    - ✓Modify & simply the SFG for implementation with integrators e.g. convert all current nodes to voltage

Low-Pass RLC Ladder Filter
Conversion to Integrator Based Active Filter



- Use KCL & KVL to derive equations:

$$V_1 = V_{in} - V_2 \;,\; V_2 = \frac{I_2}{sC_1} \;,\; V_3 = V_2 - V_4$$

$$V_4 = \frac{I_4}{sC_3} \;,\; V_5 = V_4 - V_6 \;,\; V_6 = \frac{I_6}{sC_5} \quad V_o = V_6$$

$$I_1 = \frac{V_1}{Rs} \;,\; I_2 = I_1 - I_3 \;,\; I_3 = \frac{V_3}{sL_2}$$

$$I_4 = I_3 - I_5 \;,\; I_5 = \frac{V_5}{sL_4} \;,\; I_6 = I_5 - I_7 \;,\; I_7 = \frac{V_6}{RL}$$

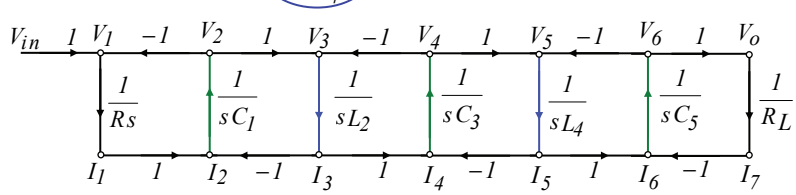
Low-Pass RLC Ladder Filter
Signal Flowgraph

$$V_1 = V_{in} - V_2 \;,\; V_2 = \frac{I_2}{sC_1} \;,\; V_3 = V_2 - V_4$$

$$V_4 = \frac{I_4}{sC_3} \;,\; V_5 = V_4 - V_6 \;,\; V_6 = \frac{I_6}{sC_5} \quad V_o = V_6$$

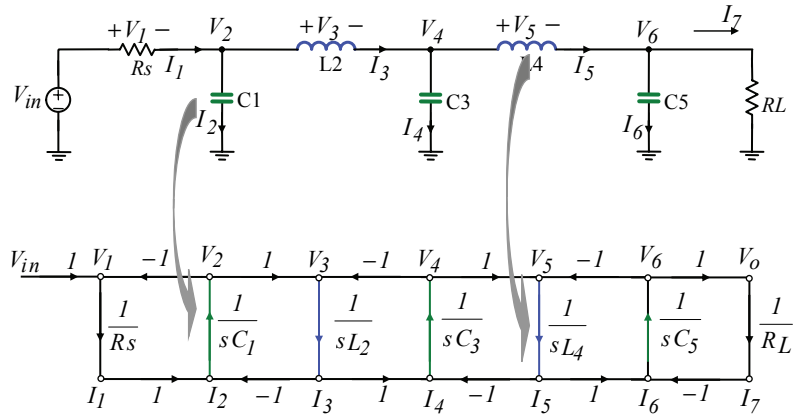
$$I_1 = \frac{V_1}{Rs} \;,\; I_2 = I_1 - I_3 \;,\; I_3 = \frac{V_3}{sL_2}$$

$$I_4 = I_3 - I_5 \;,\; I_5 = \frac{V_5}{sL_4} \;,\; I_6 = I_5 - I_7 \;,\; I_7 = \frac{V_6}{RL}$$



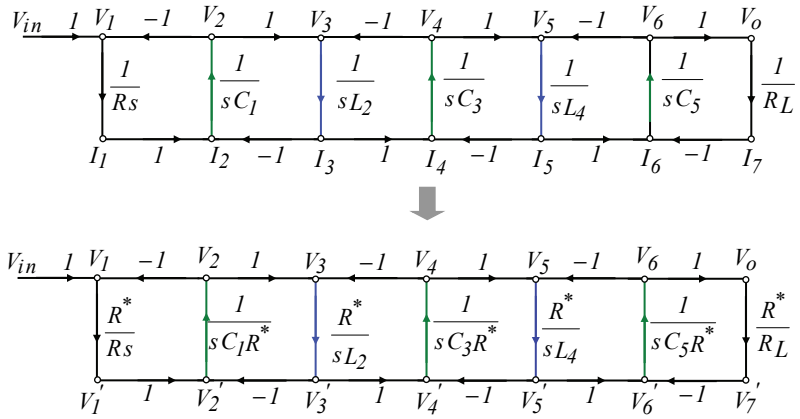
SFG

Low-Pass RLC Ladder Filter
Signal Flowgraph

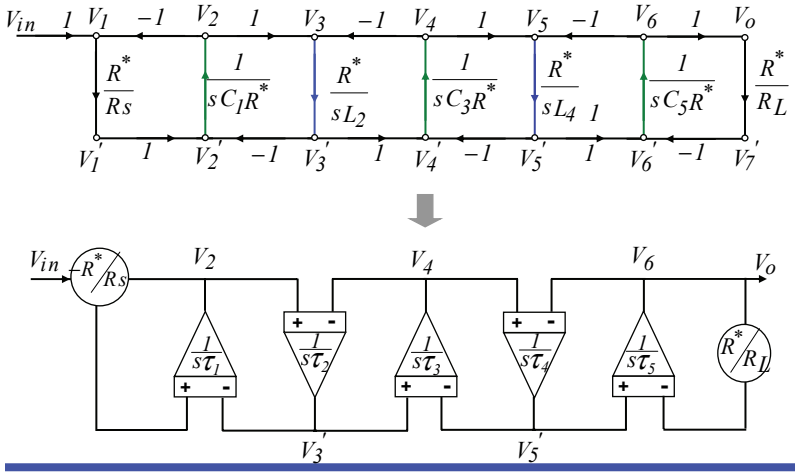


SFG

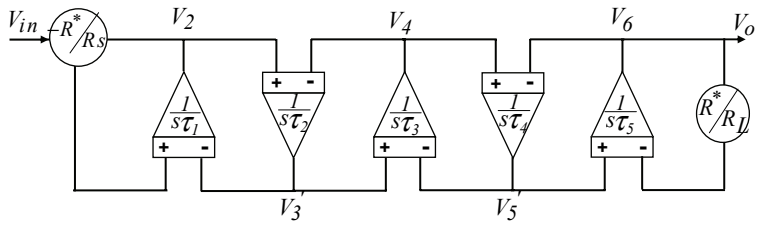
Low-Pass RLC Ladder Filter
Normalize



Low-Pass RLC Ladder Filter Synthesize



Low-Pass RLC Ladder Filter Integrator Based Implementation

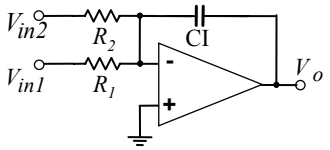


$$\tau_1 = C_1.R^* \quad , \quad \tau_2 = \frac{L_2}{R^*} = C_2.R^* \quad , \quad \tau_3 = C_3.R^* \quad , \quad \tau_4 = \frac{L4}{R^*} = C_4.R^* \quad , \quad \tau_5 = C_5.R^*$$

Main building block: Integrator
Let us start to build the filter with RC& Opamp type integrator

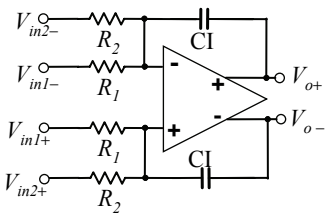
Opamp-RC Integrator

Single-Ended



$$V_o = -V_{in1} \times \frac{1}{sR_1CI} - V_{in2} \times \frac{1}{sR_2CI}$$

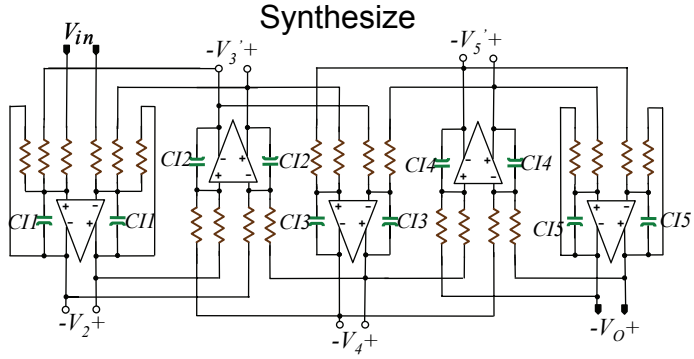
Differential



$$V_{o+} - V_{o-} = (V_{in1+} - V_{in1-}) \times \frac{1}{sR_1CI} + (V_{in2+} - V_{in2-}) \times \frac{1}{sR_2CI}$$

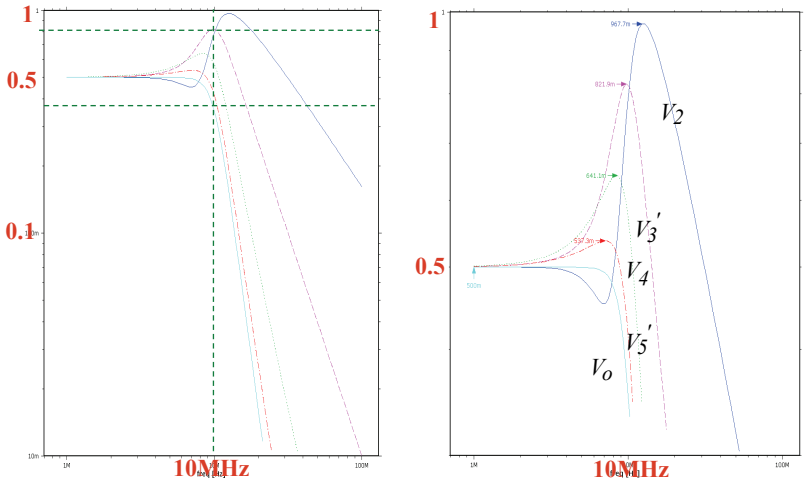
Note: Implementation with single-ended integrator requires extra circuitry for sign inversion whereas in differential case both signal polarities are available

Differential Integrator Based LP Ladder Filter Synthesize



- First iteration:
  - All resistors are chosen=1Ω
  - Values for $\tau_x = R_x CI_x$ found from RLC analysis
  - Capacitors: *CI1=CI5=9.836nF, CI2=CI4=25.45nF, CI3=31.83nF*

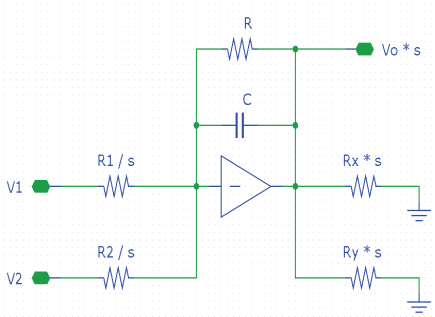
Simulated Magnitude Response



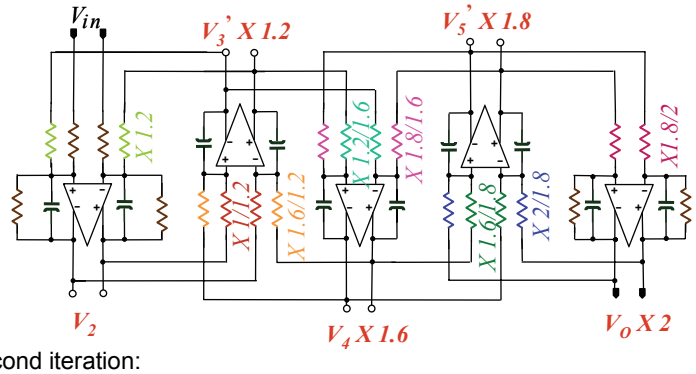
Scale Node Voltages

To maximize dynamic range
→ scale node voltages

Scale V_o by factor "s"



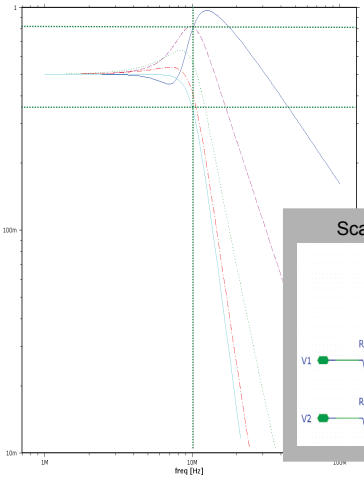
Differential Integrator Based LP Ladder Filter Node Scaling



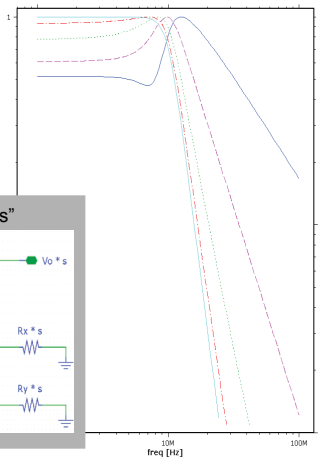
- Second iteration:
  - ❑Nodes scaled, note output node x2
  - ❑Resistor values scaled according to scaling of nodes
  - ❑Capacitors the same : *C1=C5=9.836nF, C2=C4=25.45nF, C3=31.83nF*

Maximizing Signal Handling by Node Voltage Scaling

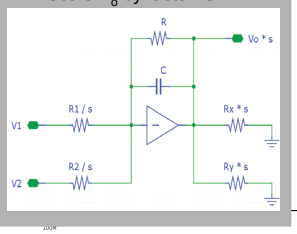
Before Node Scaling



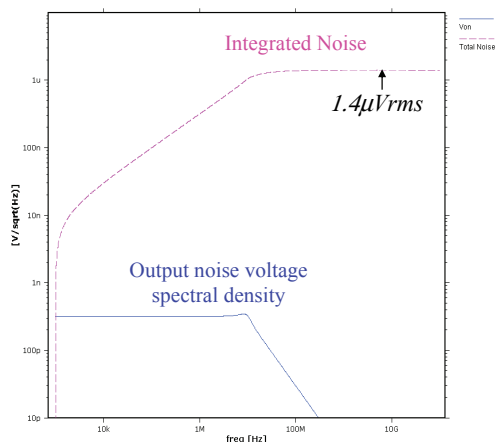
After Node Scaling



Scale V_o by factor "s"



Filter Noise



Total noise @ the output:
1.4 μV rms
(noiseless opamps)

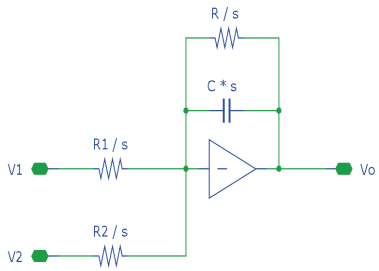
That's excellent, but:

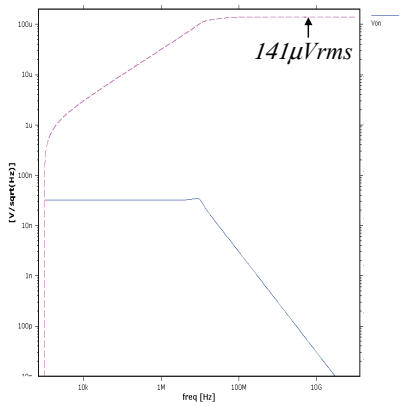
- Capacitors too large for integration → unrealistic Si area
- Resistors too small → high power dissipation

Typical applications allow higher noise, assuming tolerable noise in the order of 140 μV rms …

Scale to Meet Noise Target

Scale capacitors and resistors to meet noise objective

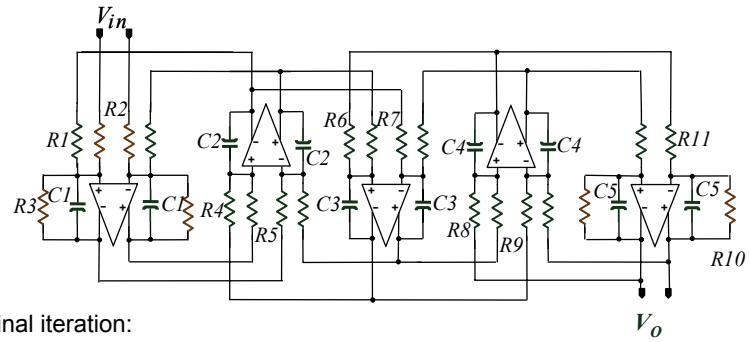




$s = 10^{-4} \rightarrow (V_{n1}/V_{n2})^2$

Noise after scaling: 141 μV rms (noiseless opamps)

Differential Integrator Based LP Ladder Filter Final Design



- Final iteration:
  - ❑Based on scaled nodes and noise considerations
  - ❑Capacitors: *C1=C5=0.9836pF, C2=C4=2.545pF, C3=3.183pF*
  - ❑*Resistors: R1=11.77K, R2=9.677K, R3=10K, R4=12.82K, R5=8.493K, R6=11.93K, R7=7.8K, R8=10.75K, R9=8.381K, R11=10K, R11=9.306K*