EE C247B – ME C218
Introduction to MEMS Design
Spring 2015

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Lecture Module 4: Lithography, Etching, & Doping

Lecture Outline

- Reading: Senturia, Chpt. 3; Jaeger, Chpt. 2, 4, 5
  - Lithography
  - Etching
    - Wet etching
    - Dry etching
  - Semiconductor Doping
    - Ion implantation
    - Diffusion

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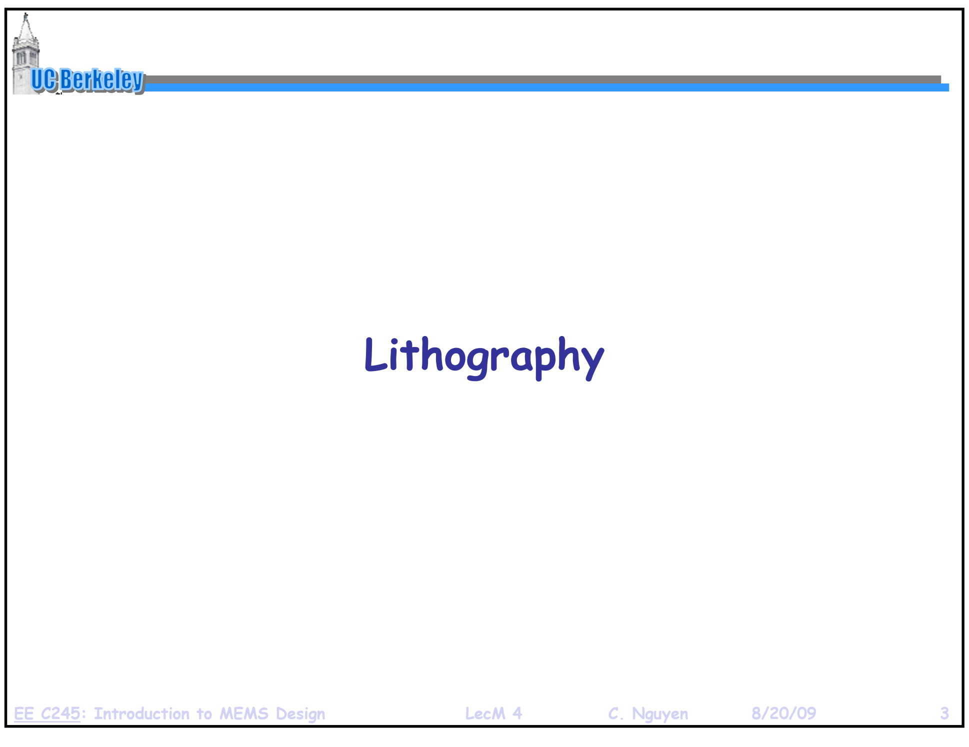

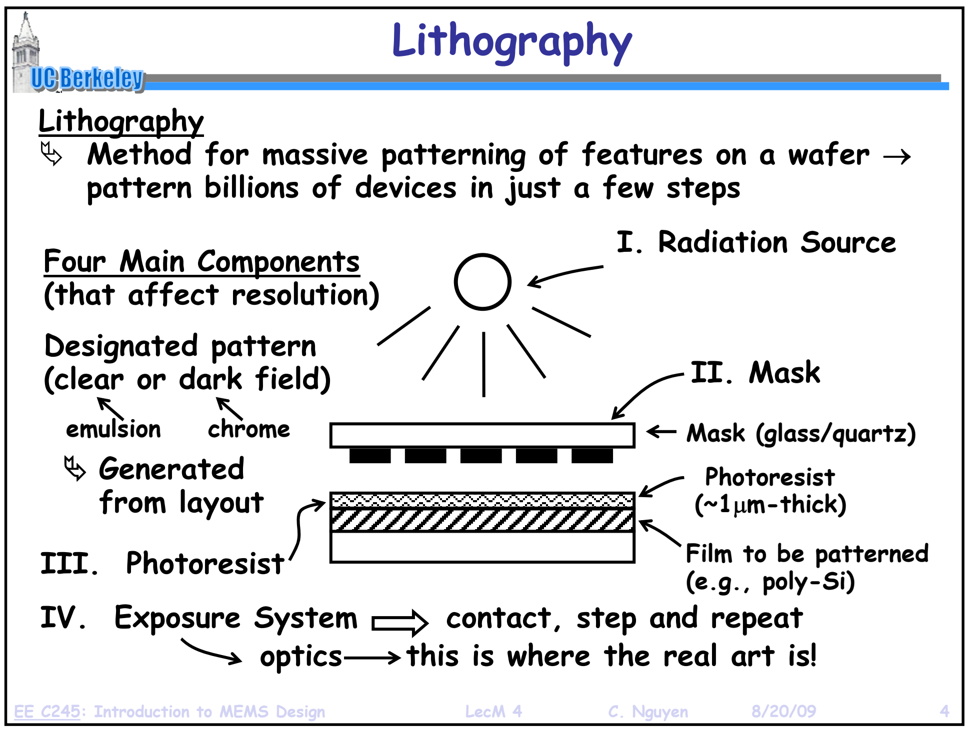

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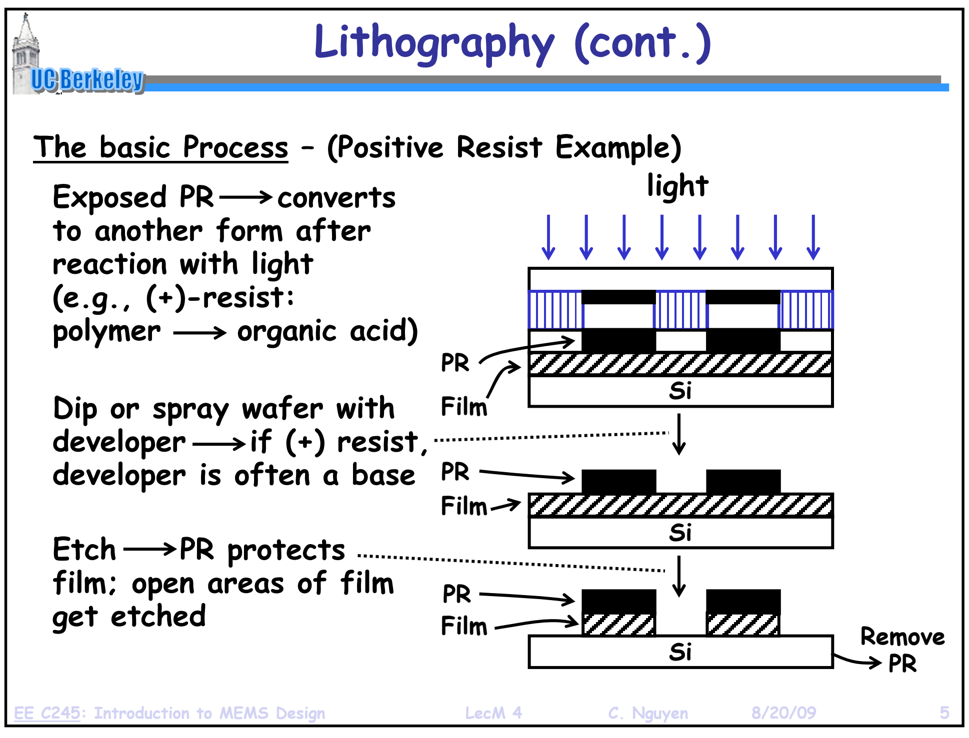

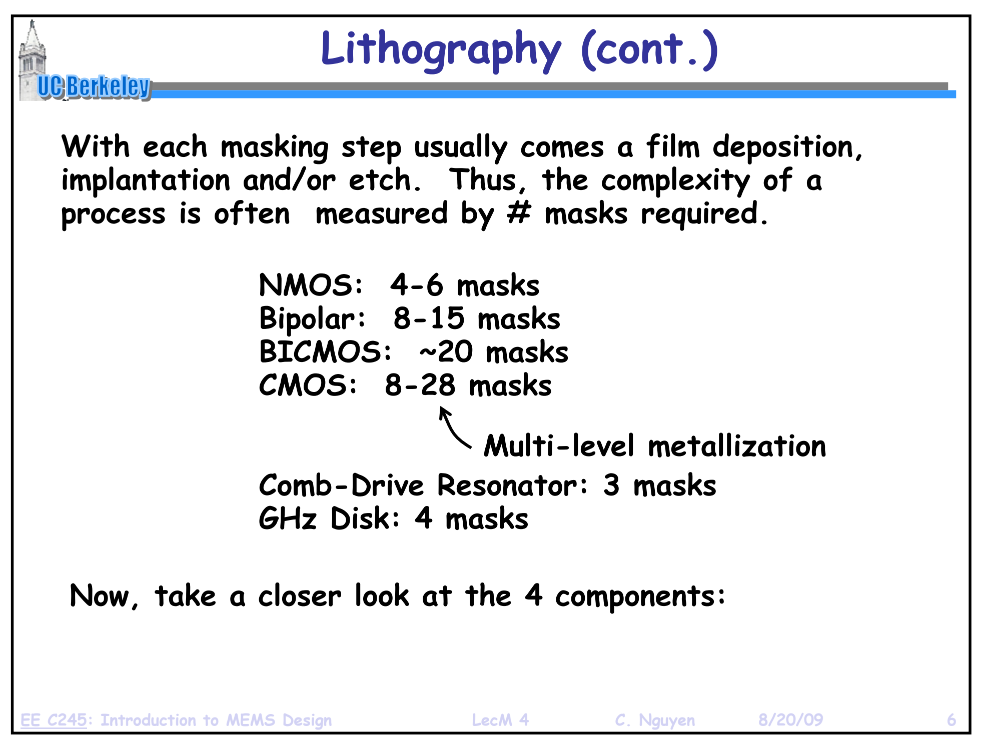

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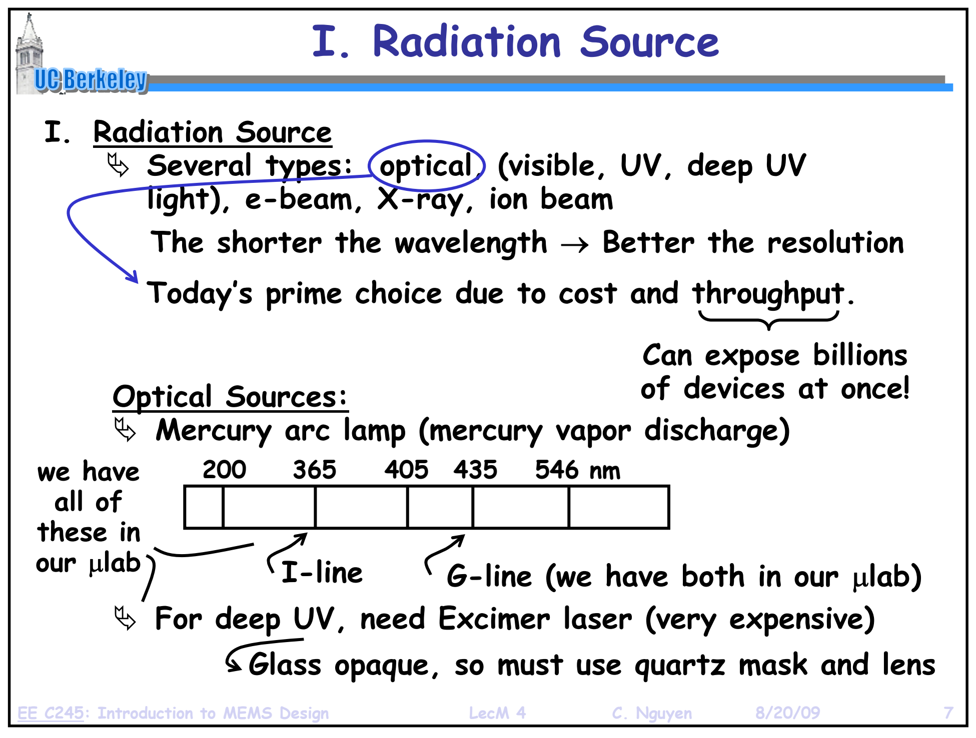

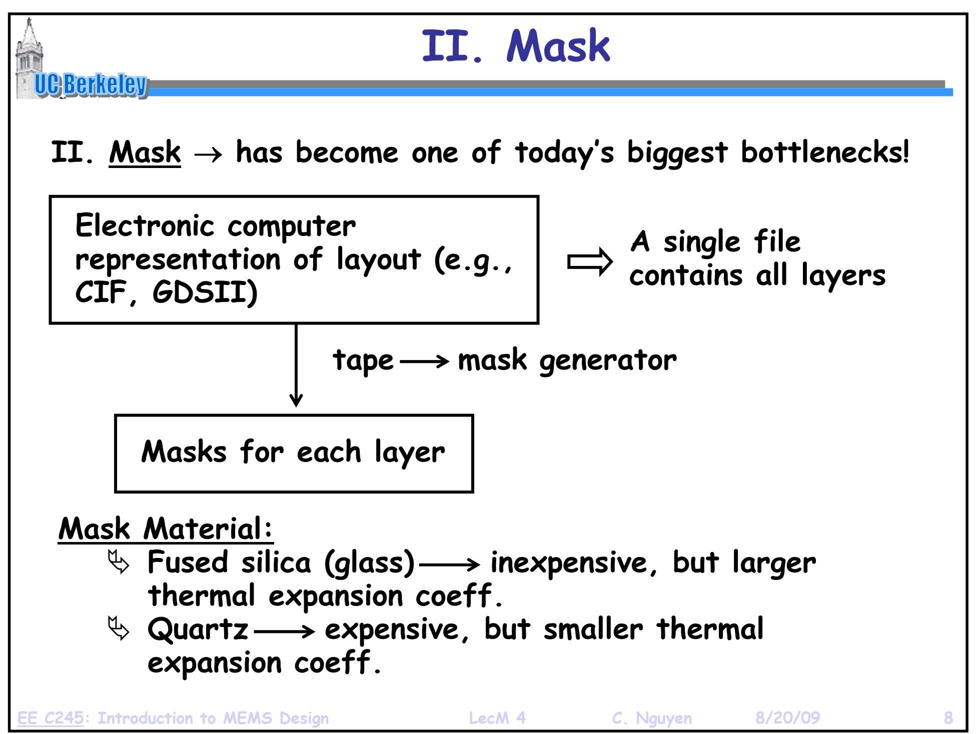

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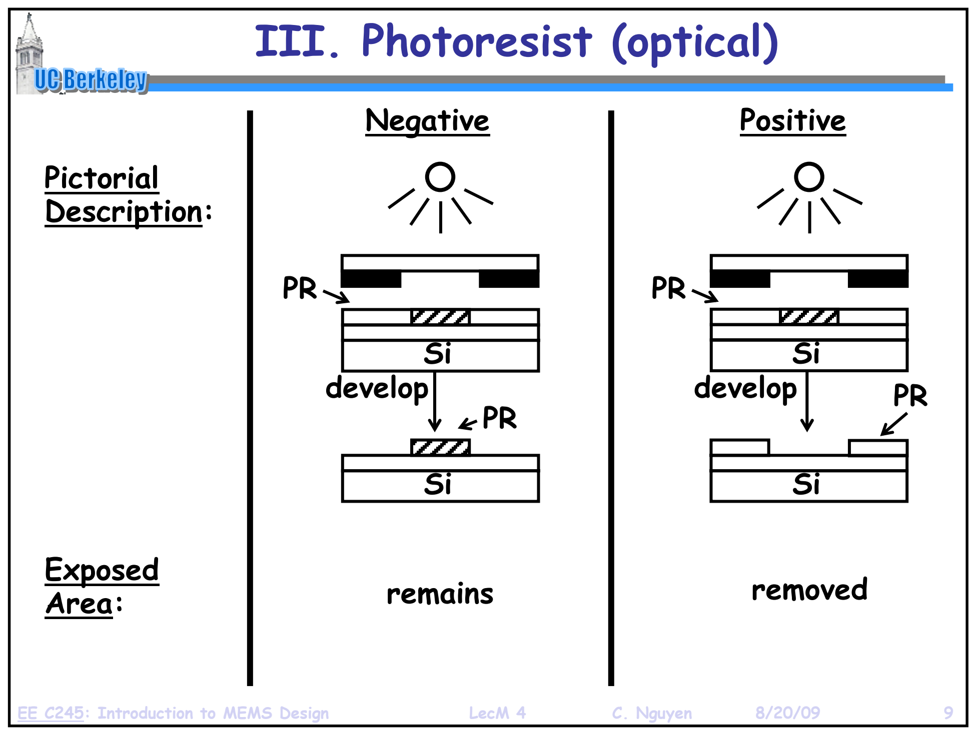

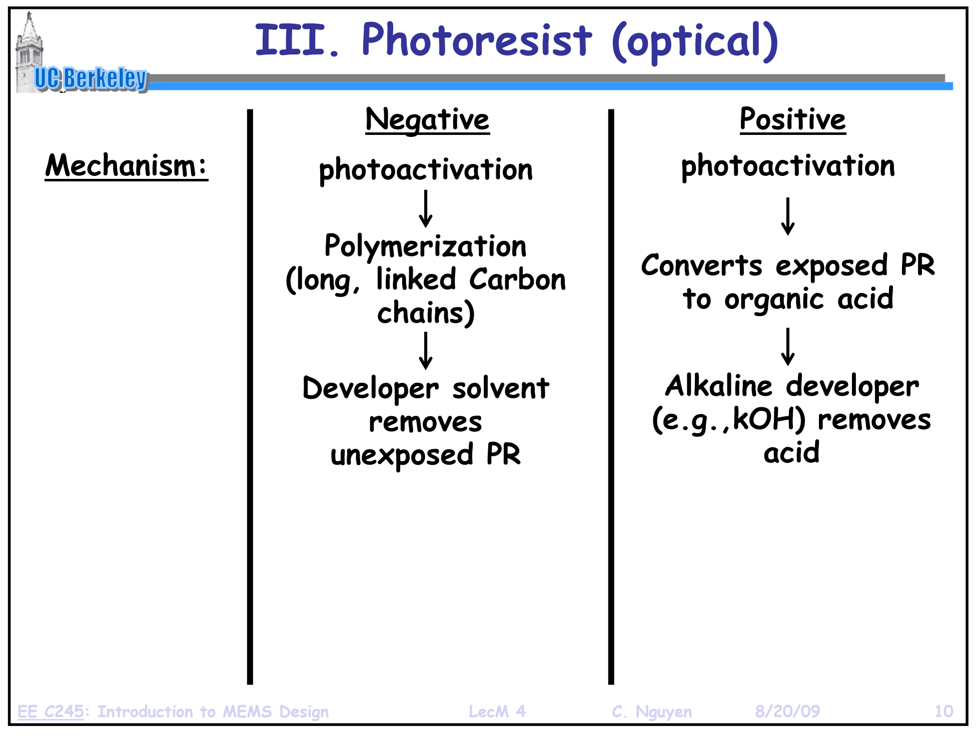

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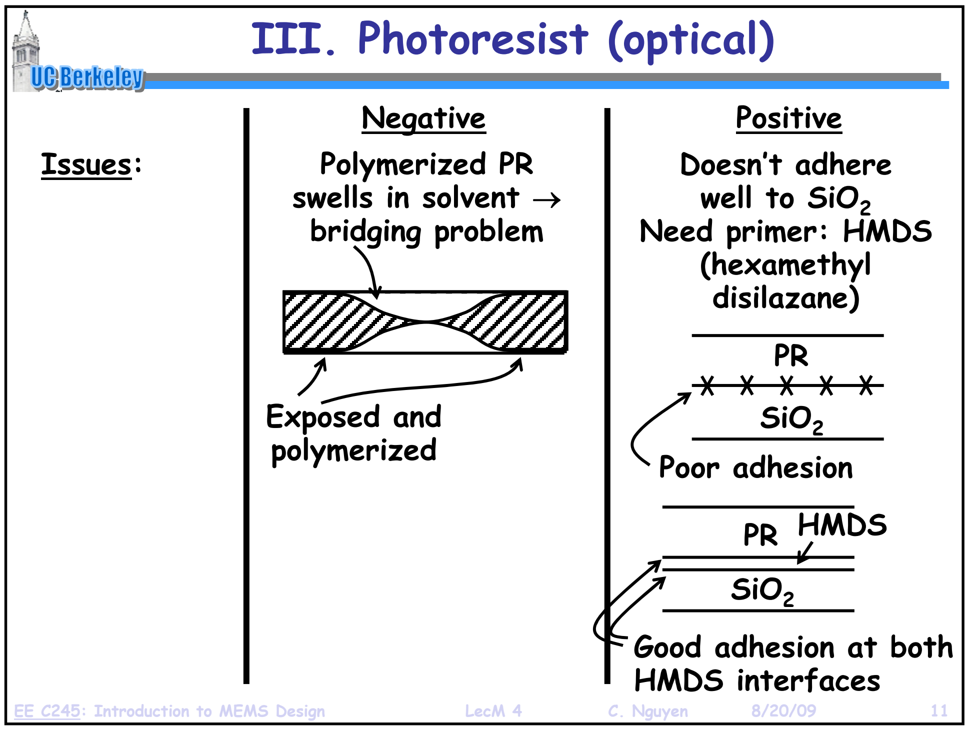

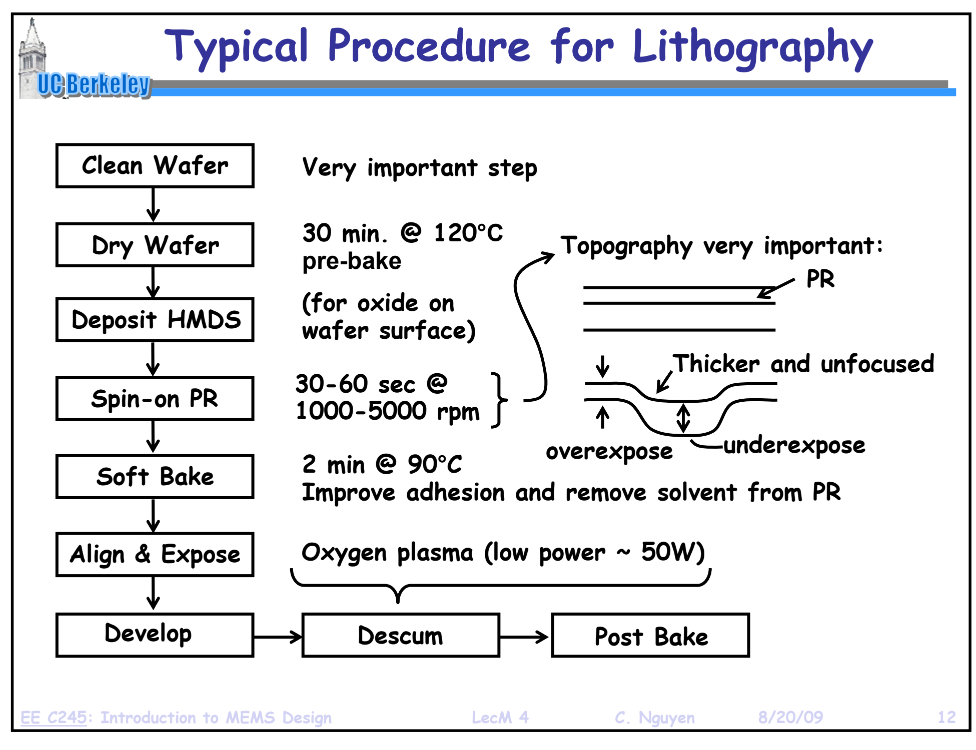

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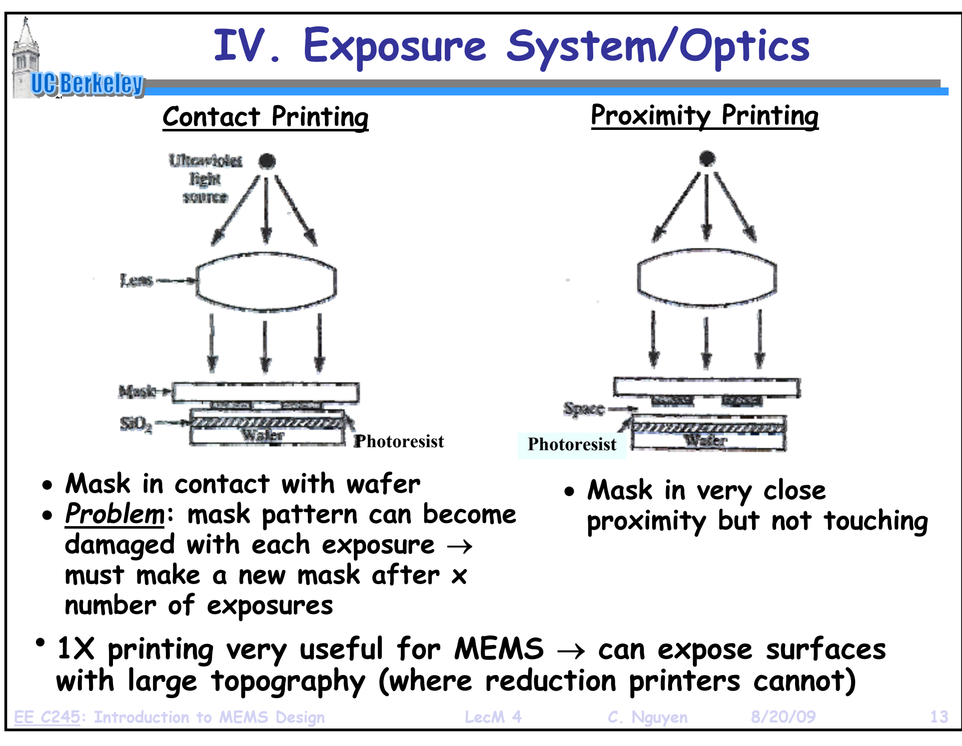

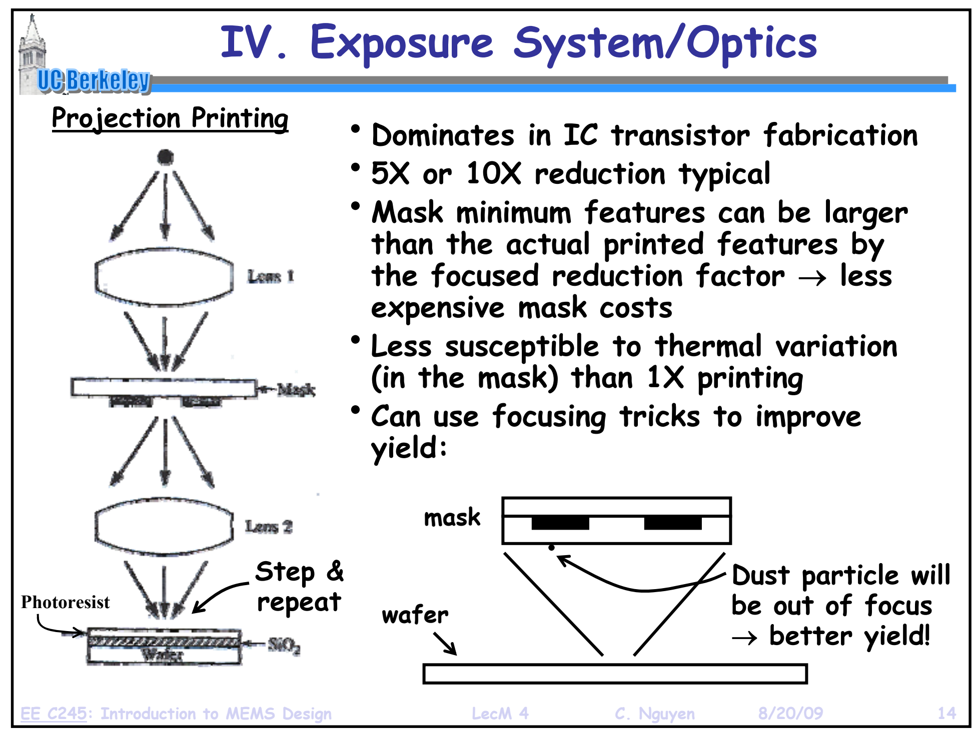

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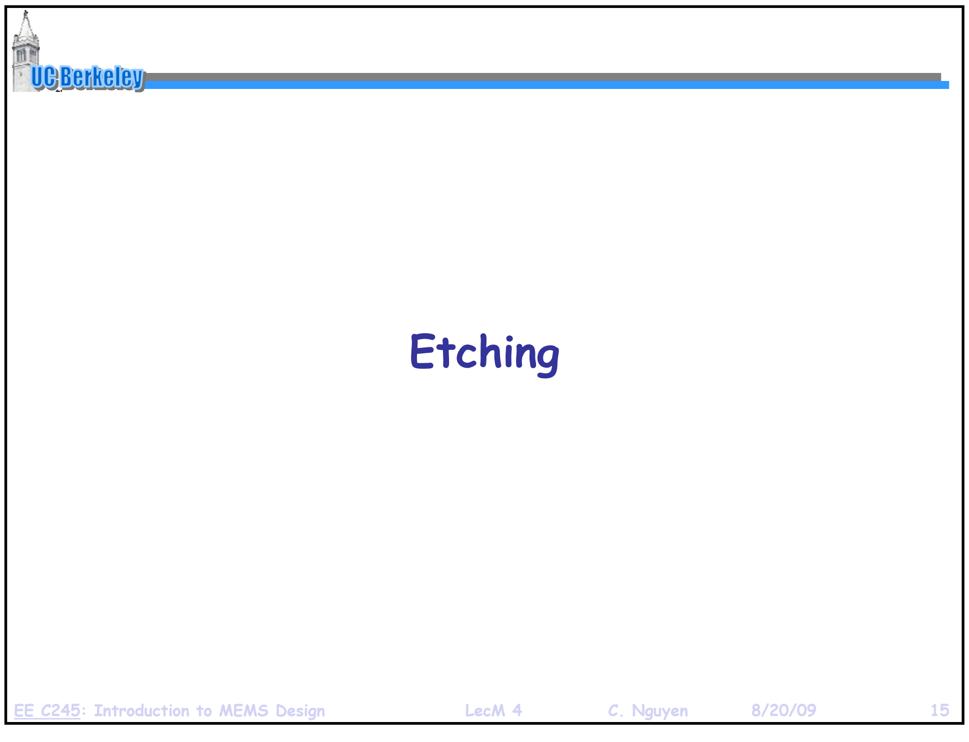

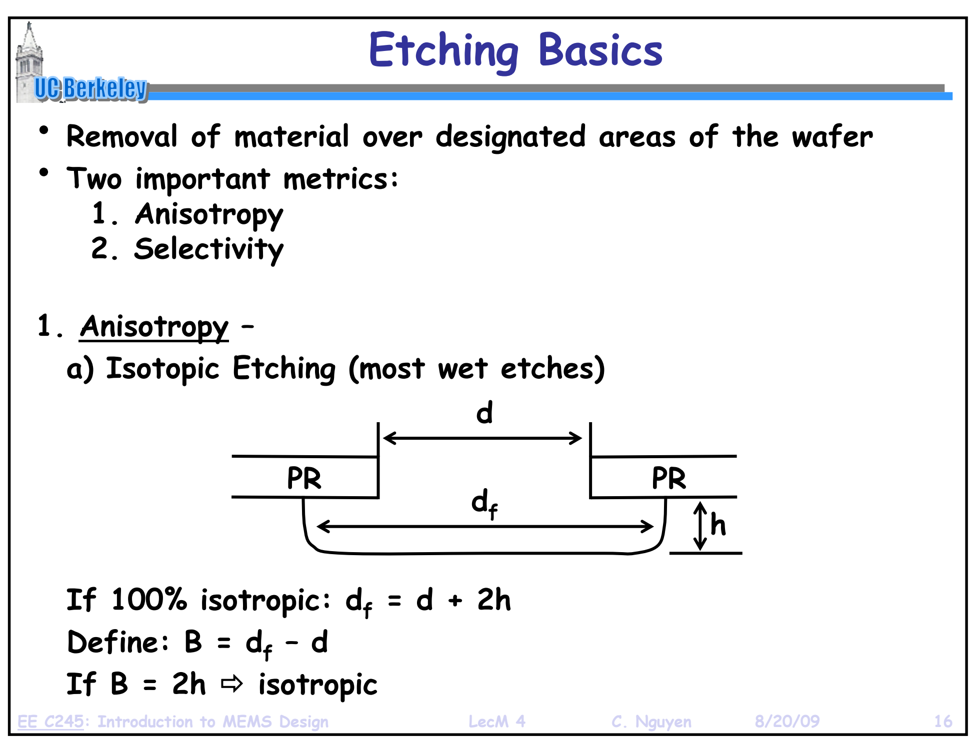

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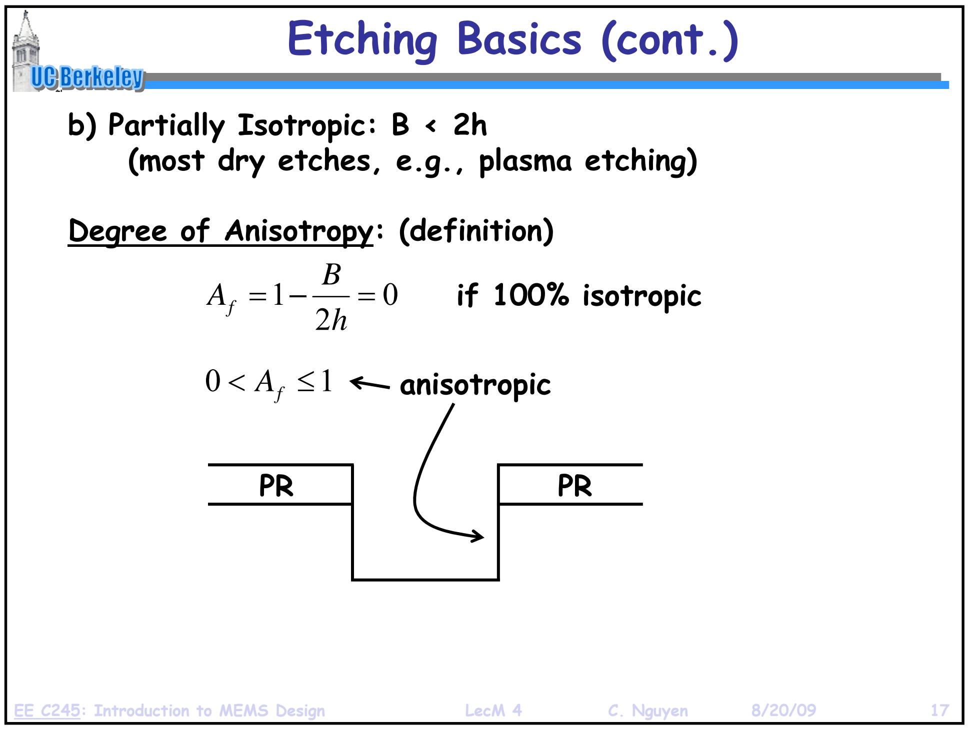

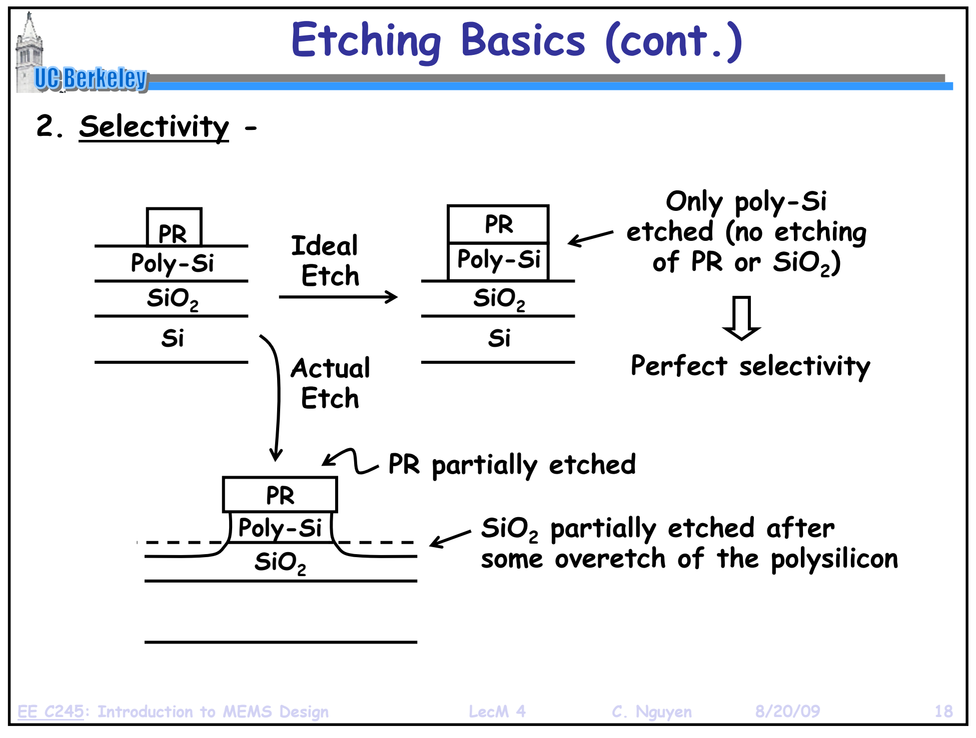

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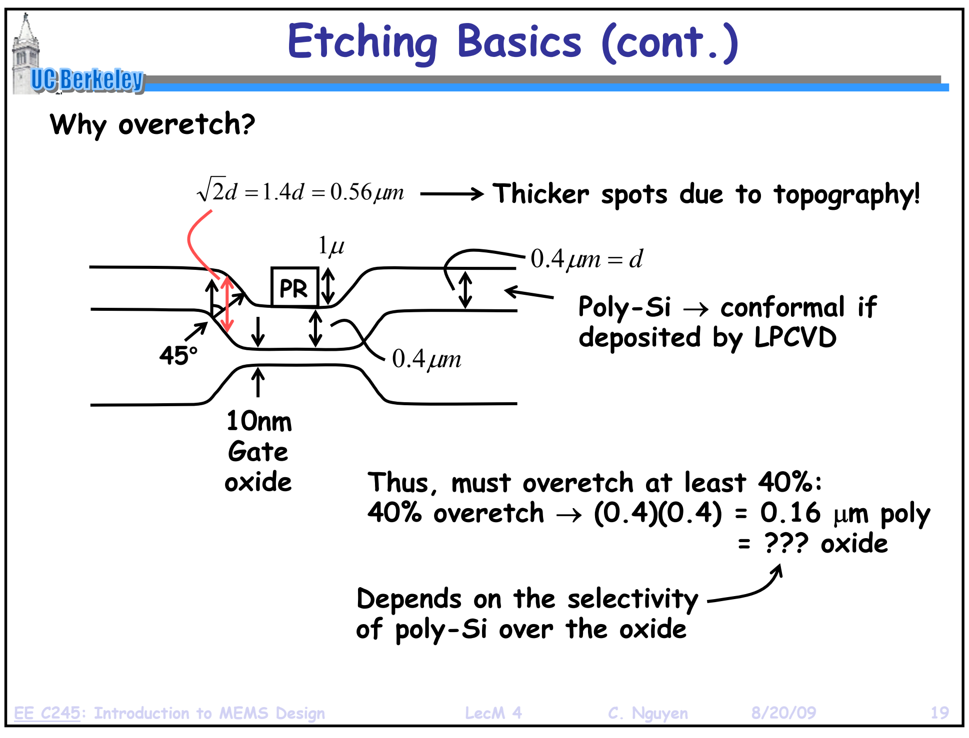

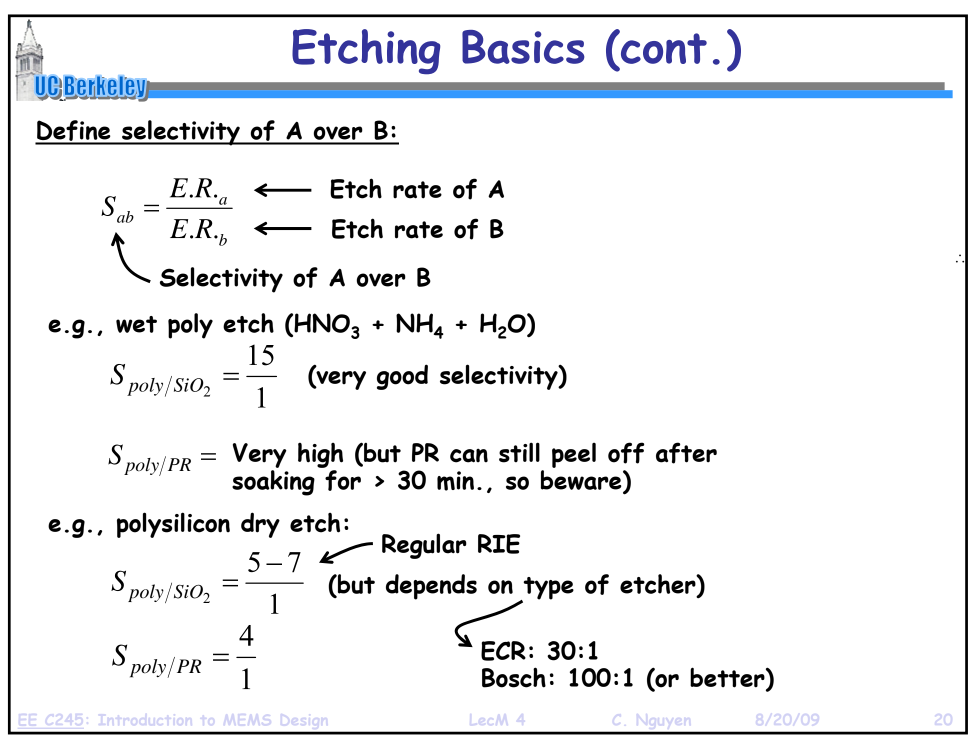

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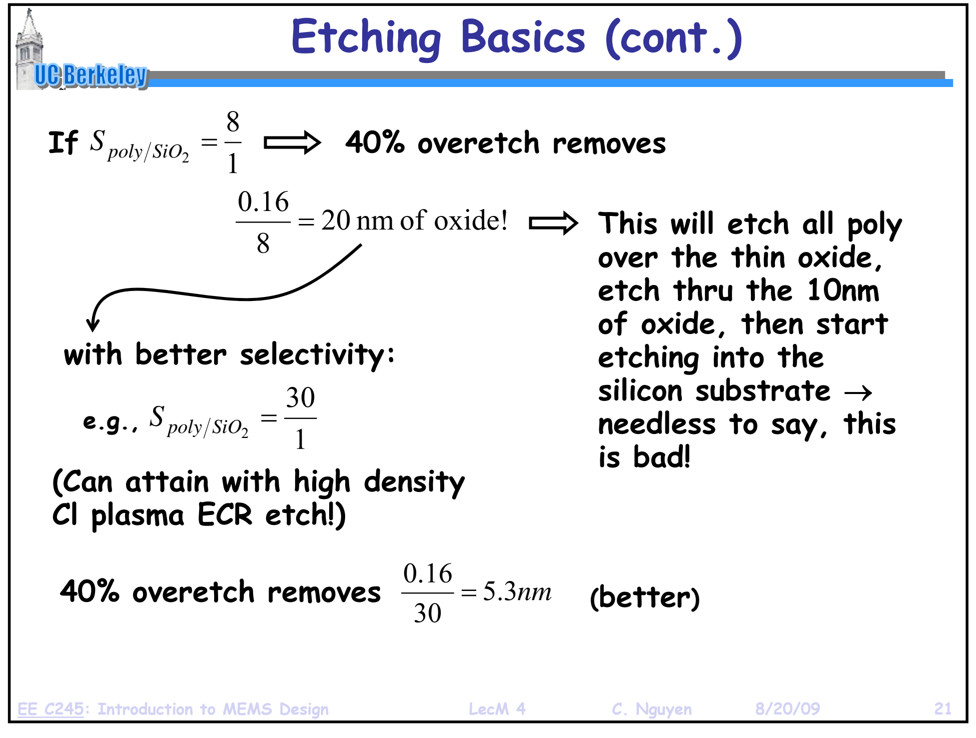

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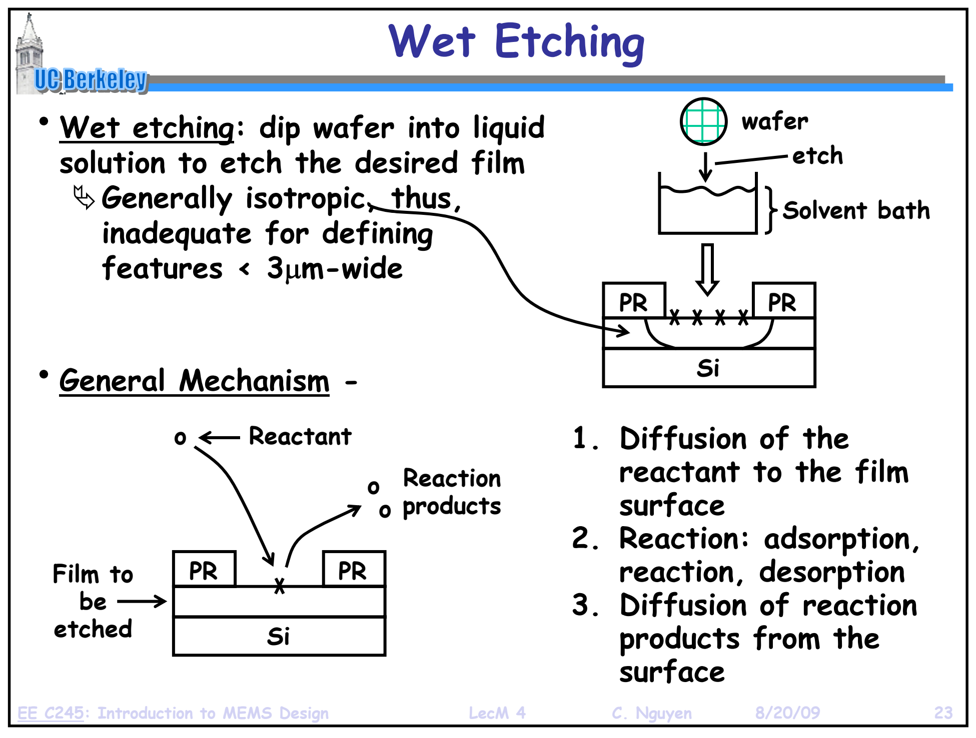

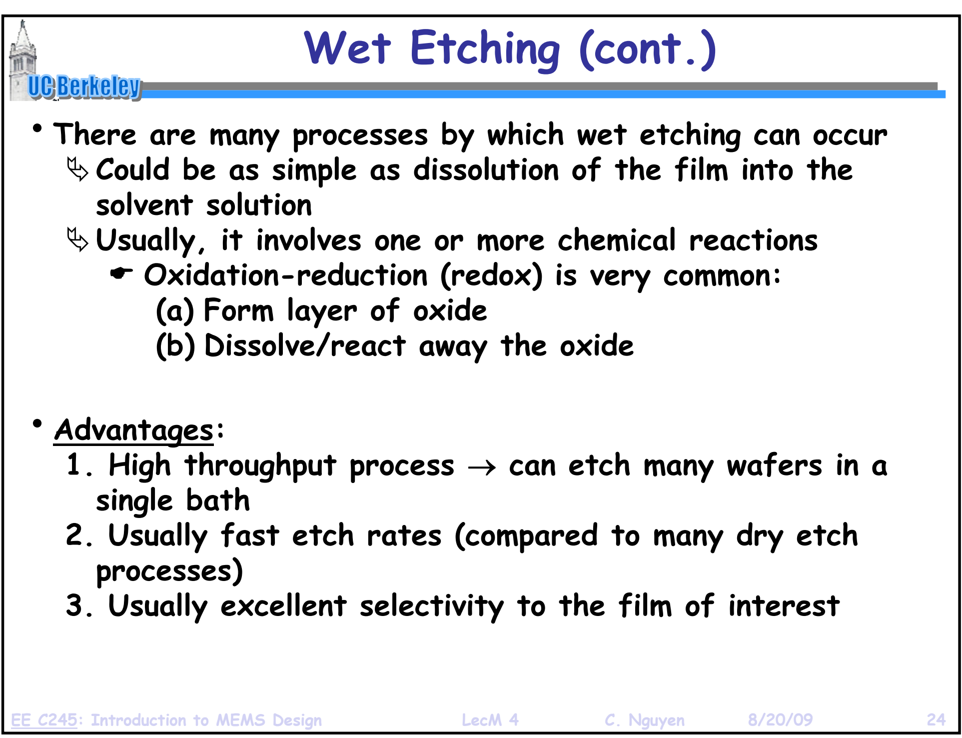

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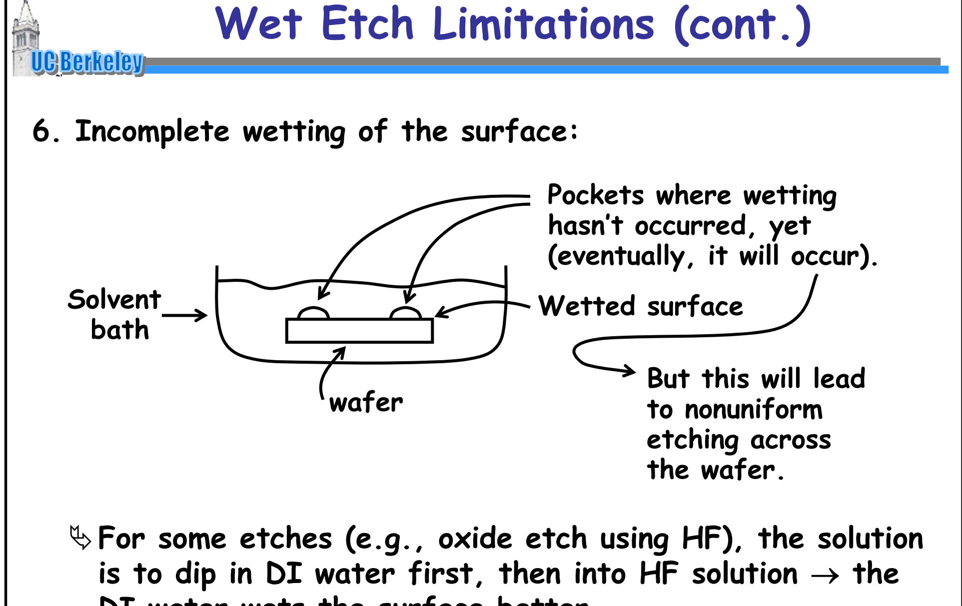
Wet Etching Limitations

1. Isotropic
   - Limited to <3μm features
   - But this is also an advantage of wet etching, e.g., if used for undercutting for MEMS
2. Higher cost of etchants & DI water compared w/ dry etch gas expenses (in general, but not true vs. deep etchers)
3. Safety
   - Chemical handling is a hazard
4. Exhaust fumes and potential for explosion
   - Need to perform wet etches under hood
5. Resist adhesion problems
   - Need HMDS (but this isn't so bad)

Wet Etch Limitations (cont.)

6. Incomplete wetting of the surface:



- For some etches (e.g., oxide etch using HF), the solution is to dip in DI water first, then into HF solution → the DI water wets the surface better

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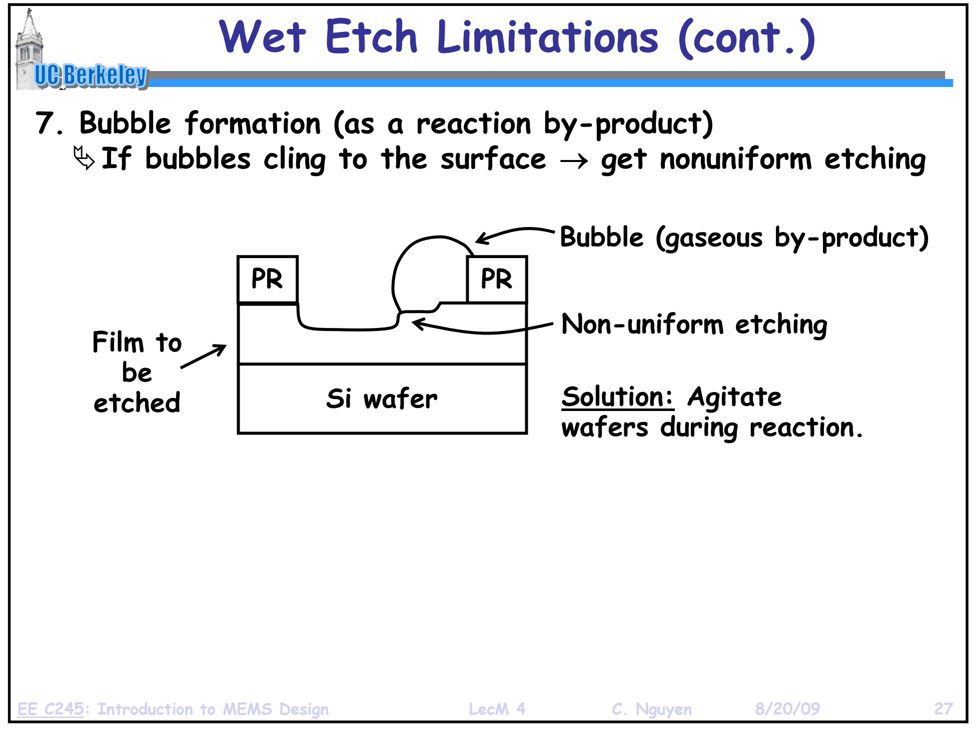

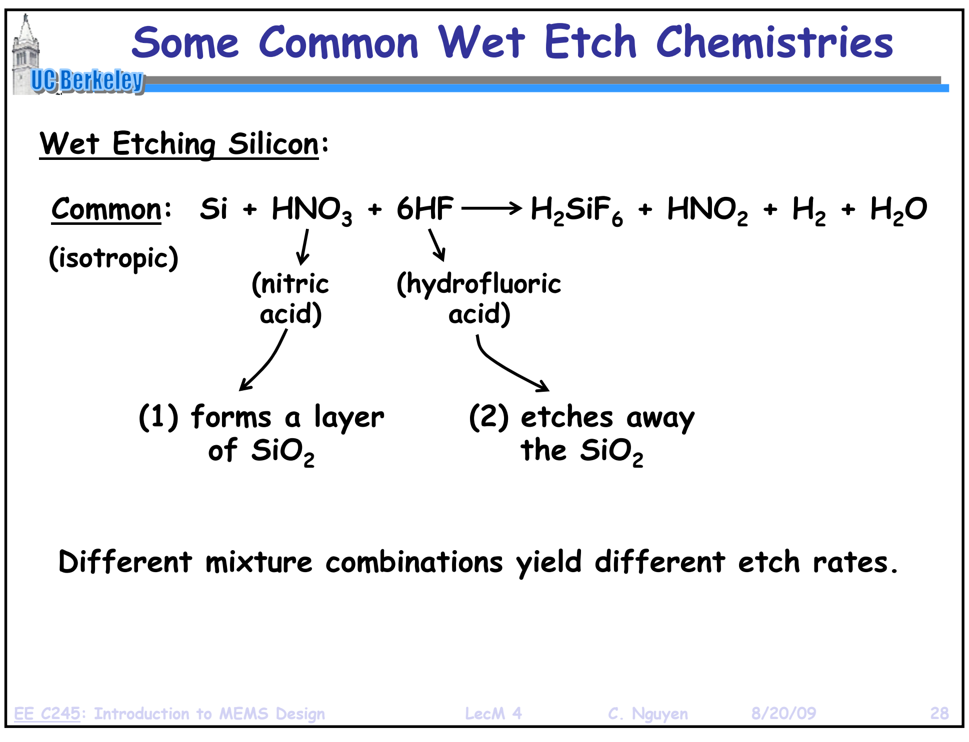

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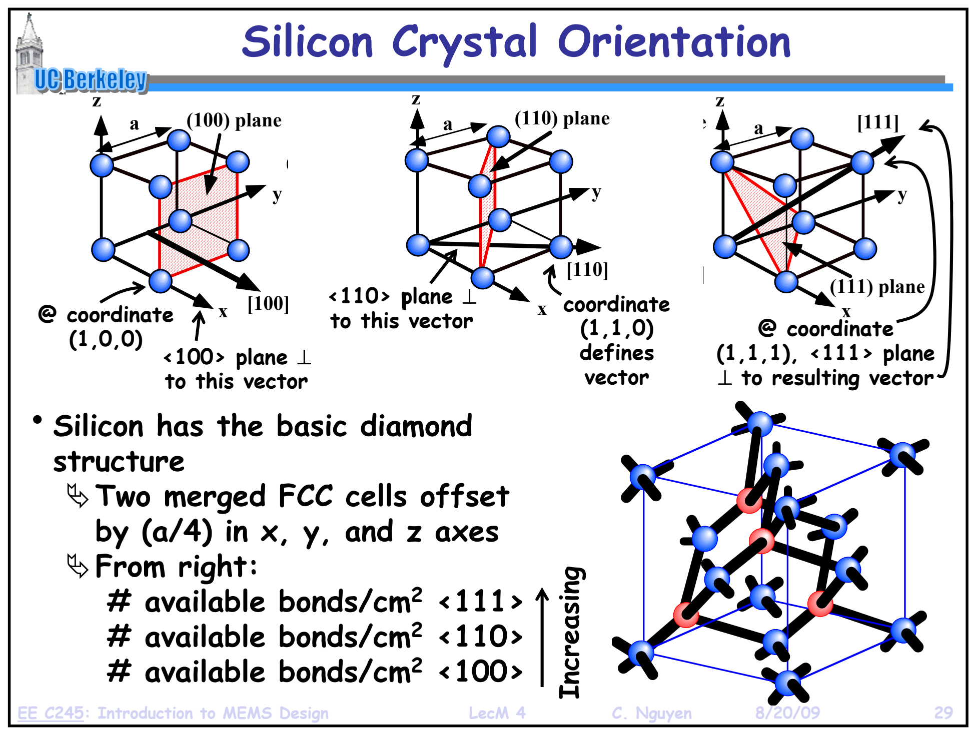

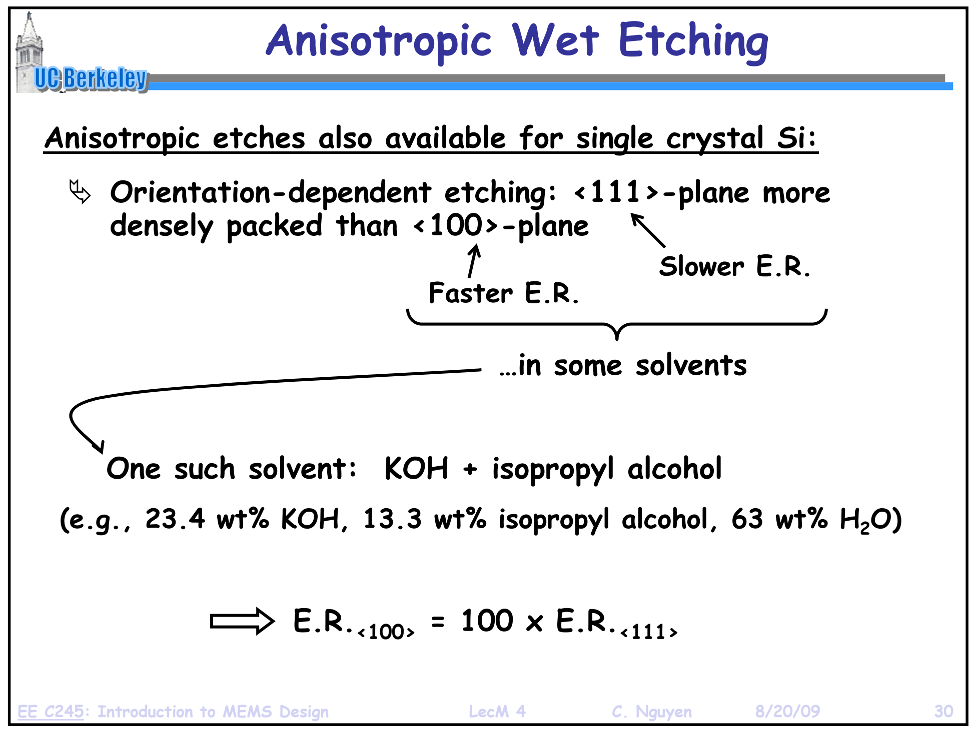

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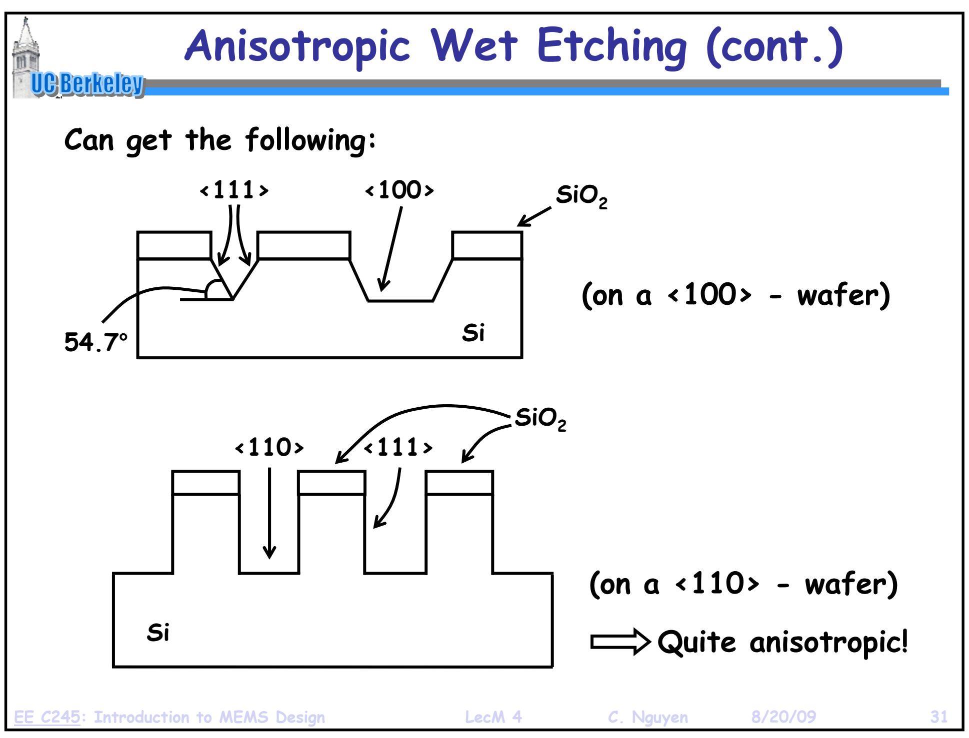

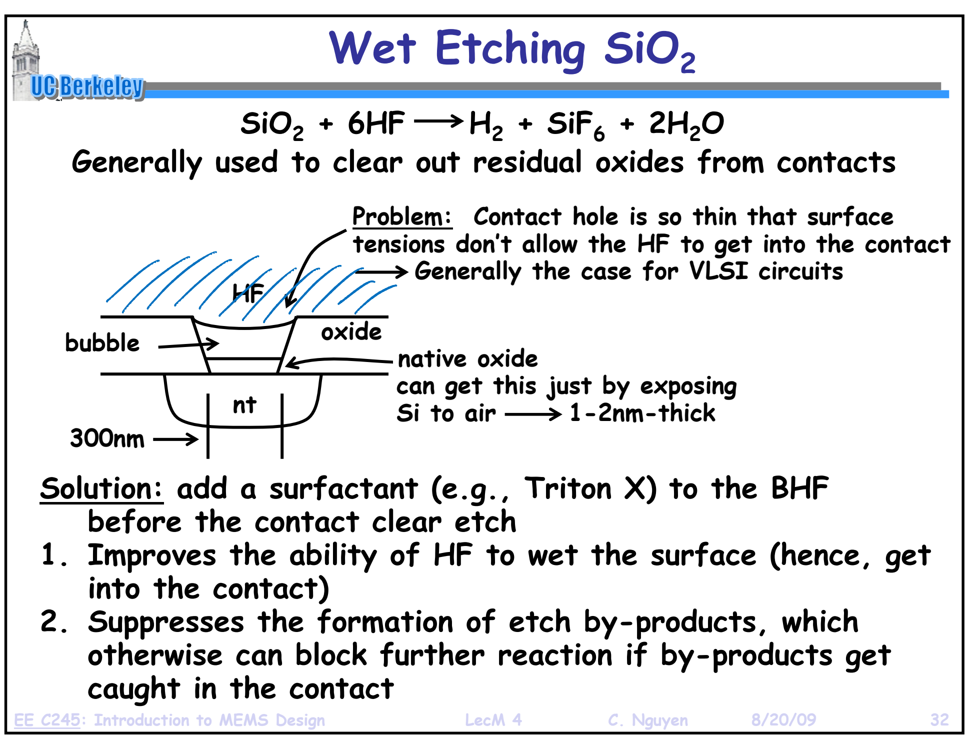

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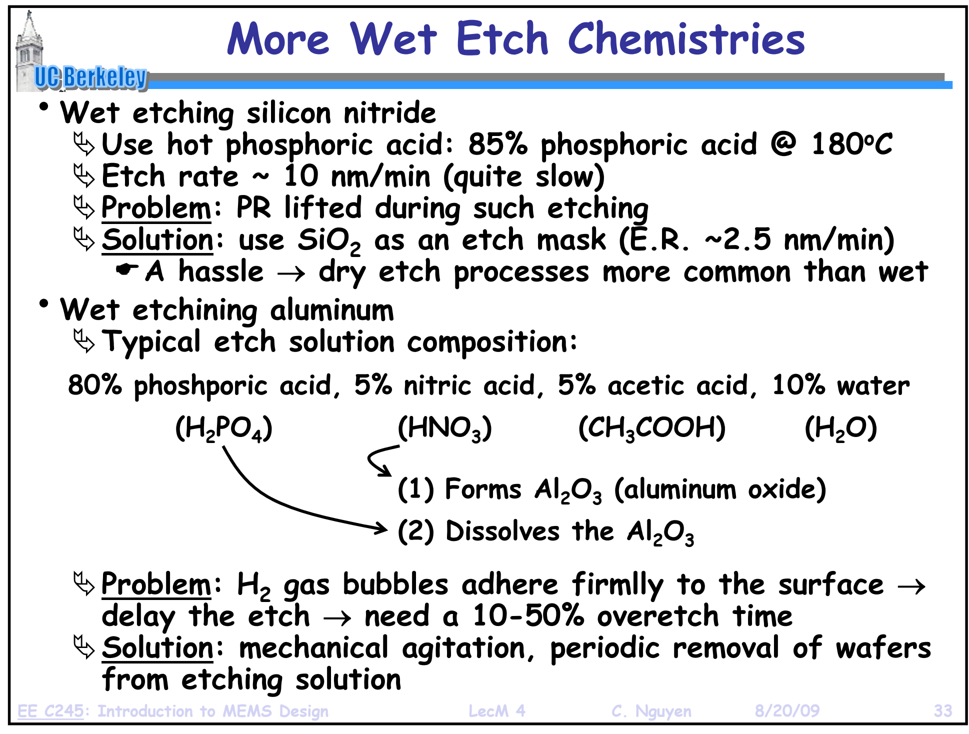

Wet Etch Rates (f/ K. Williams)

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Wet-Etch Rates for Micromachining and IC Processing (Å/min)

The top etch rate was measured by the authors with fresh solutions, etc. The center and bottom values are the low and high etch rates observed by the authors and others in our lab under less carefully controlled conditions.

ETCHANT EQUIPMENT CONDITIONS	TARGET MATERIAL	SC Si <100>	Poly n+	Poly undop	Wet Ox	Dry Ox	LTO undop	PSG unanl	PSG annld	Stoic Nitrid	Low-σ Nitrid	Al/2% Si	Sput Tung	Sput Ti	Sput Ti/W	OCG 820PR	Olin HntPR
Concentrated HF (49%) Wet Sink Room Temperature	Silicon oxides	-	0	-	23k 18k 23k	F	>14k	F	36k	140	52 30 52	42 0 42	<50	F	-	P 0	P 0
10:1 HF Wet Sink Room Temperature	Silicon oxides	-	7	0	230	230	340	15k	4700	11	3	2500 2500 12k	0	11k	<70	0	0
25:1 HF Wet Sink Room Temperature	Silicon oxides	-	0	0	97	95	150	W	1500	6	1	W	0	-	-	0	0
5:1 BHF Wet Sink Room Temperature	Silicon oxides	-	9	2	1000 900 1080	1000	1200	6800	4400 3500 4400	9	4 3 4	1400	<20 0.25 20	F	1000	0	0
Phosphoric Acid (85%) Heated Bath with Reflux 160°C	Silicon nitrides	-	7	-	0.7	0.8	<1	37	24 9 24	28 28 42	19 19 42	9800	-	-	-	550	390
Silicon Etchant (126 HNO_3 : 60 H_2O : 5 NH_4F) Wet Sink Room Temperature	Silicon	1500	3100 1200 6000	1000	87	W	110	4000	1700	2	3	4000	130	3000	-	0	0
KOH (1 KOH : 2 H_2O by weight) Heated Stirred Bath 80°C	<100> Silicon	14k	>10k	F	77 41 77	-	94	W	380	0	0	F	0	-	-	F	F
Aluminum Etchant Type A (16 H_3PO_4 : 1 HNO_3 : 1 HAc : 2 H_2O) Heated Bath 50°C	Aluminum	-	<10	<9	0	0	0	-	<10	0	2	6600 2600 6600	-	0	-	0	0
Titanium Etchant (20 H_2O : 1 H_2O_2 : 1 HF) Wet Sink Room Temperature	Titanium	-	12	-	120	W	W	W	2100	8	4	W	0 0 <10	8800	-	0	0
H_2O_2 (30%) Wet Sink Room Temperature	Tungsten	-	0	0	0	0	0	0	0	0	0	<20	190 190 1000	0	60 60 150	<2	0
Piranha (~50 H_2SO_4 : 1 H_2O_2) Heated Bath 120°C	Cleaning off metals and organics	-	0	0	0	0	0	-	0	0	0	1800	-	2400	-	F	F
Acetone Wet Sink Room Temperature	Photoresist	-	0	0	0	0	0	-	0	0	0	0	-	0	-	>44k	>39k

Notation: - =test not performed; W=not performed, but known to Work (≥ 100 Å/min); F=not performed, but known to be *Fast* (≥ 10 kÅ/min); P=some of film *Peeled* during etch or when rinsed; A=film was visibly *Attacked* and roughened.
Etch areas are all of a 4-inch wafer for the transparent films and half of the wafer for single-crystal silicon and the metals.
Etch rates will vary with temperature and prior use of solution, area of exposure of film, other materials present (e.g., photoresist), film impurities and microstructure, etc. *Some variation should be expected.*

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Film Etch Chemistries

- For some popular films:

Material	Wet etchant	Etch rate [nm/min]	Dry etchant	Etch rate [nm/min]
Polysilicon	HNO_3:H_2O:NH_4F	120-600	SF_6 + He	170-920
Silicon nitride	H_3PO_4	5	SF_6	150-250
Silicon dioxide	HF	20-2000	CHF_3 + O_2	50-150
Aluminum	H_3PO_4:HNO_3:CH_3COOH	660	Cl_2 + $SiCl_4$	100-150
Photoresist	Acetone	>4000	O_2	35-3500
Gold	KI	40	n/a	n/a

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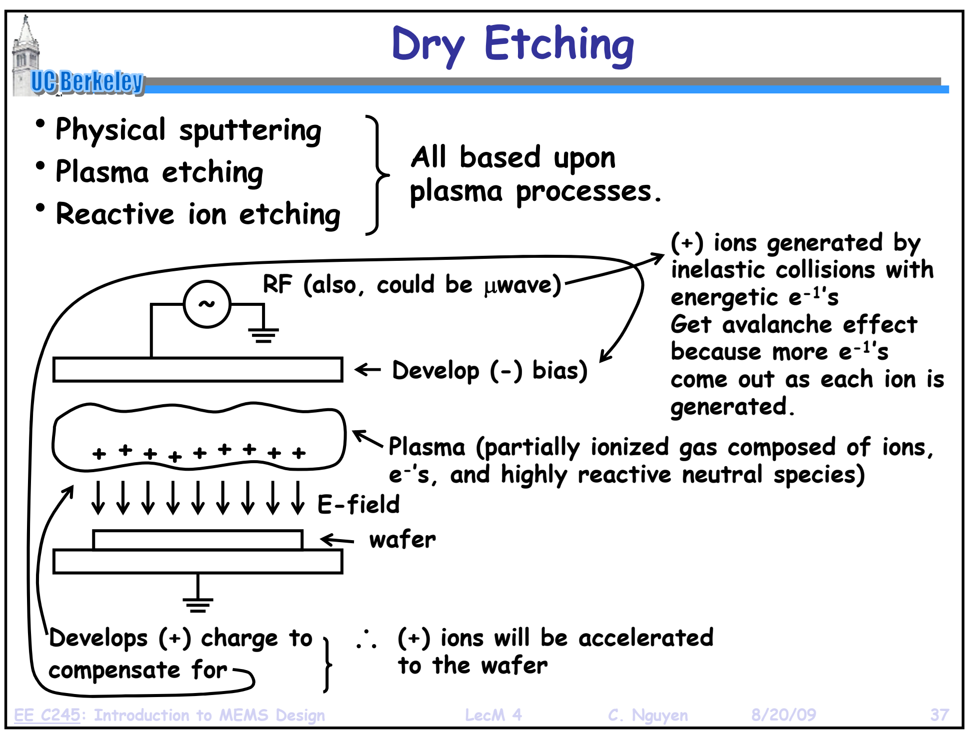

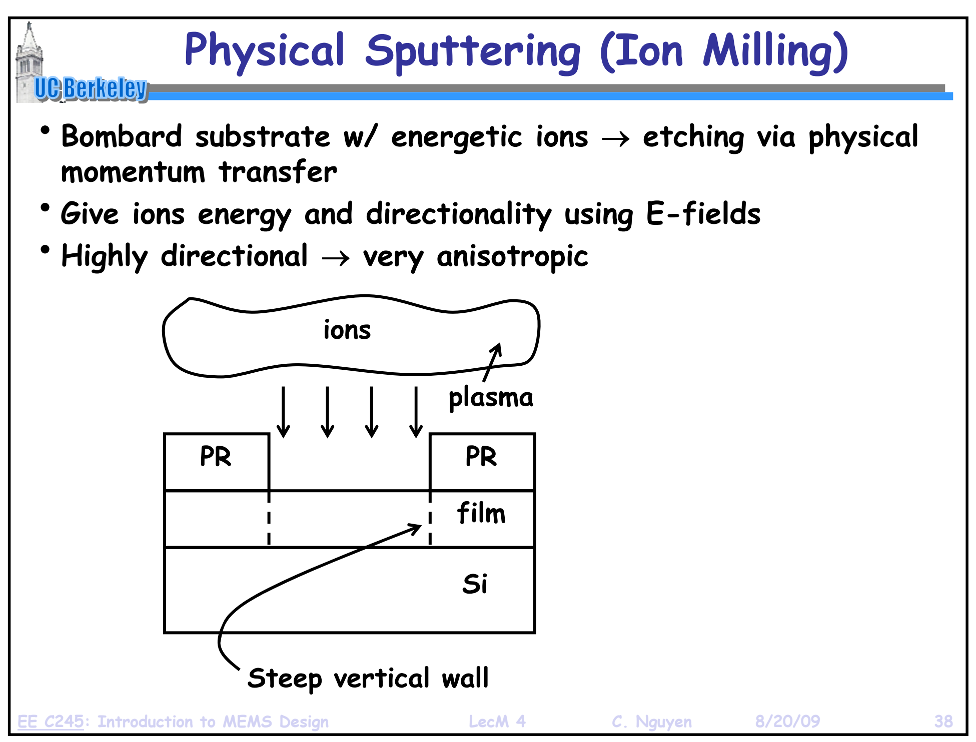

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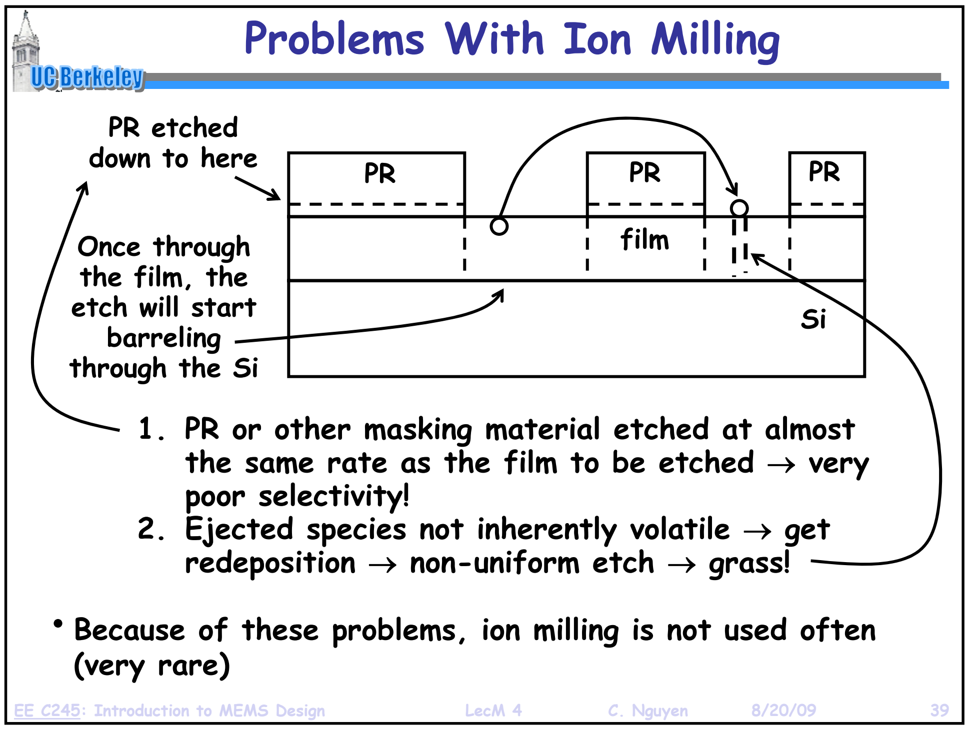

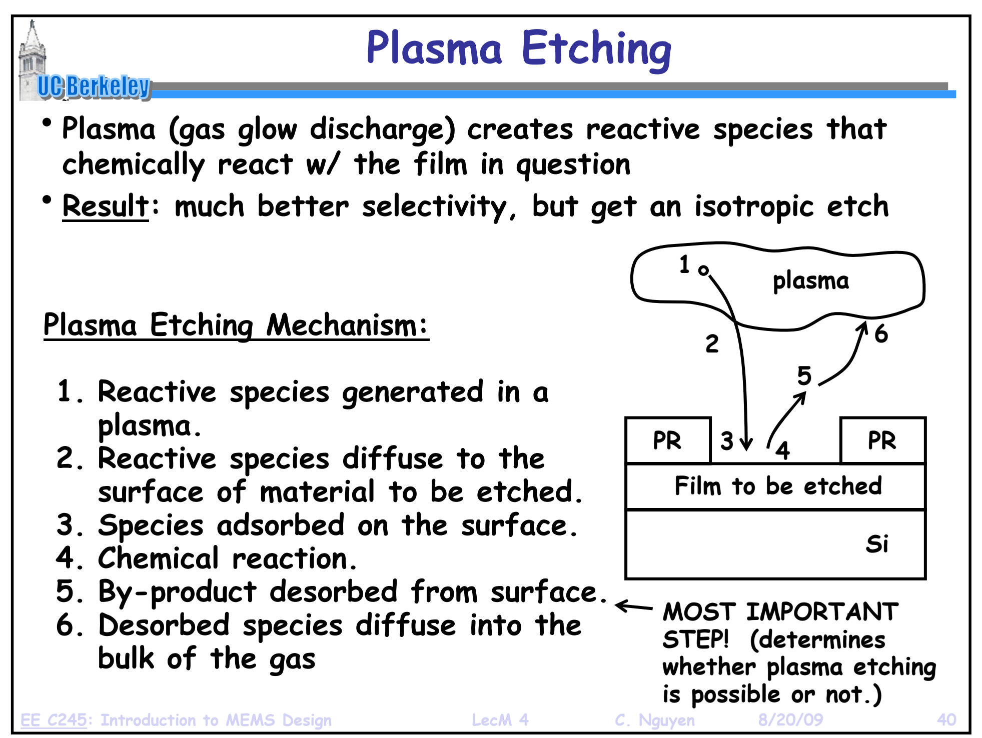

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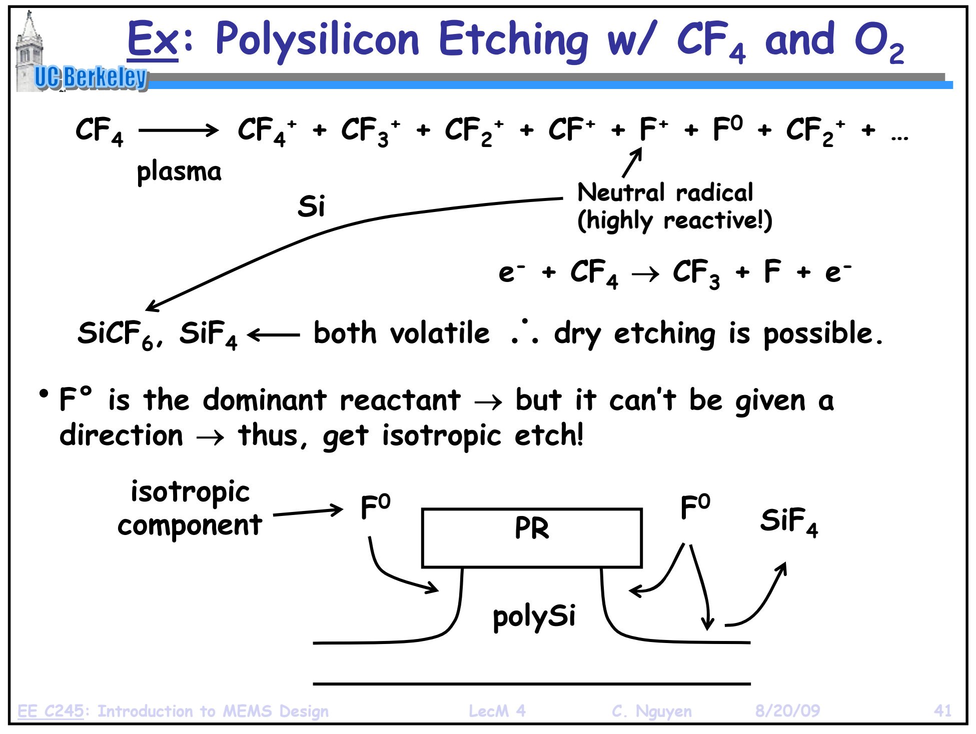

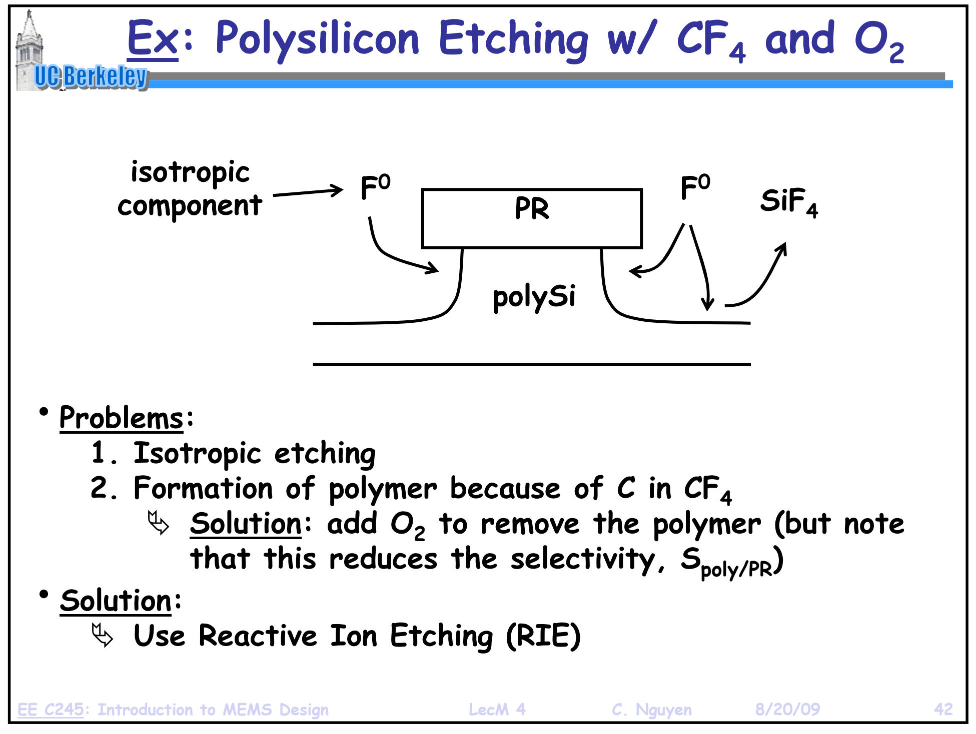

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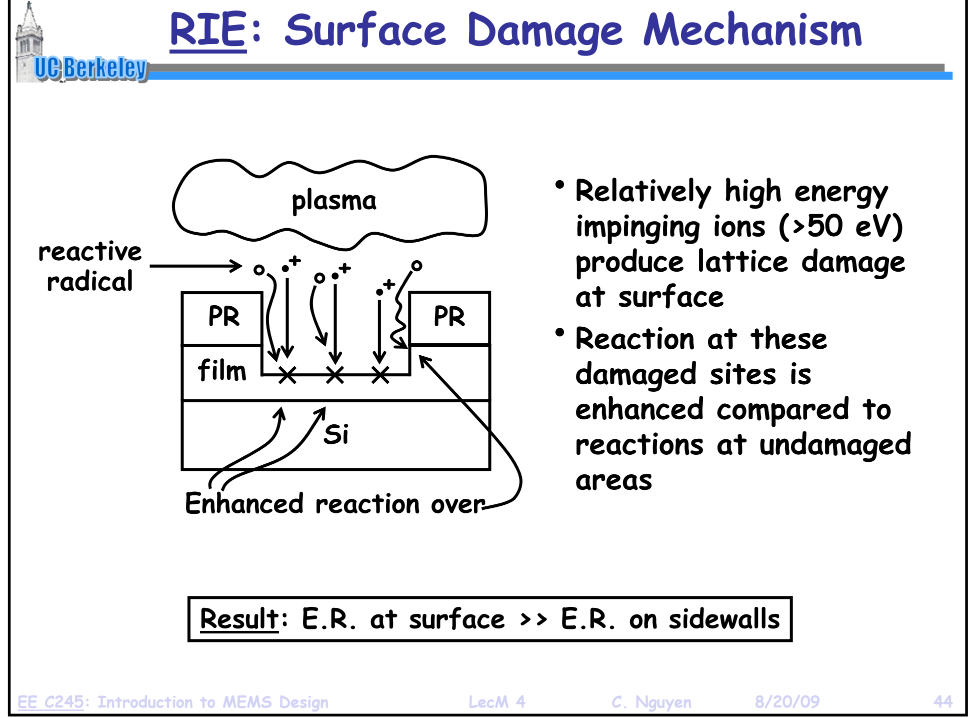
Reactive Ion Etching (RIE)

- Use ion bombardment to aid and enhance reactive etching in a particular direction
  - Result: directional, anisotropic etching!
- RIE is somewhat of a misnomer
  - It's not ions that react … rather, it's still the neutral species that dominate reaction
  - Ions just enhance reaction of these neutral radicals in a specific direction
- Two principle postulated mechanisms behind RIE
  1. Surface damage mechanism
  2. Surface inhibitor mechanism



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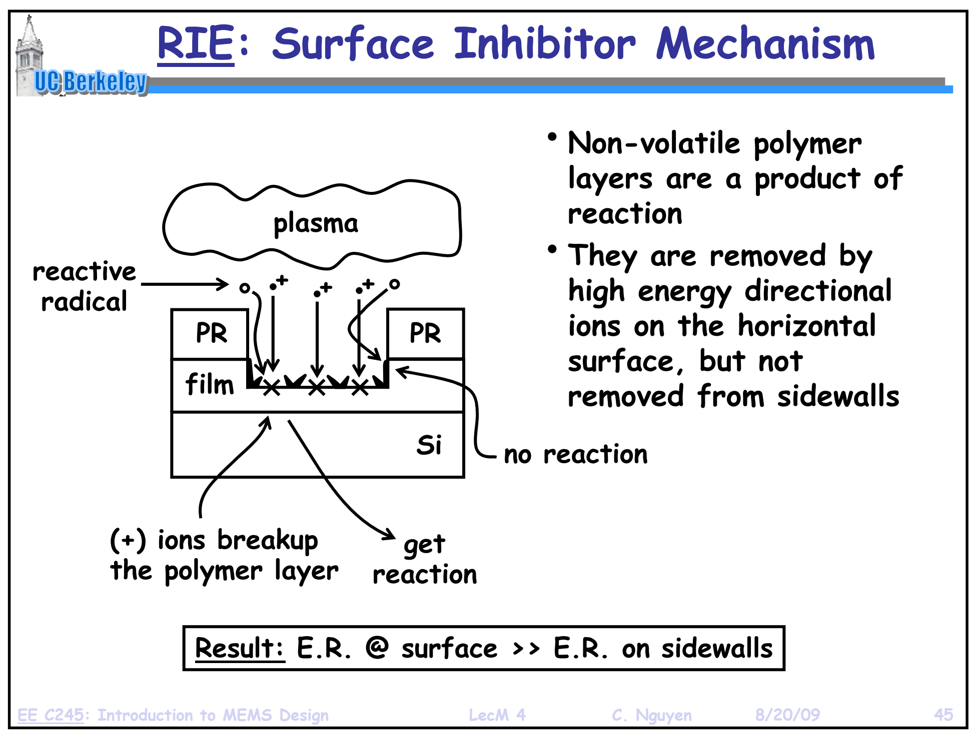

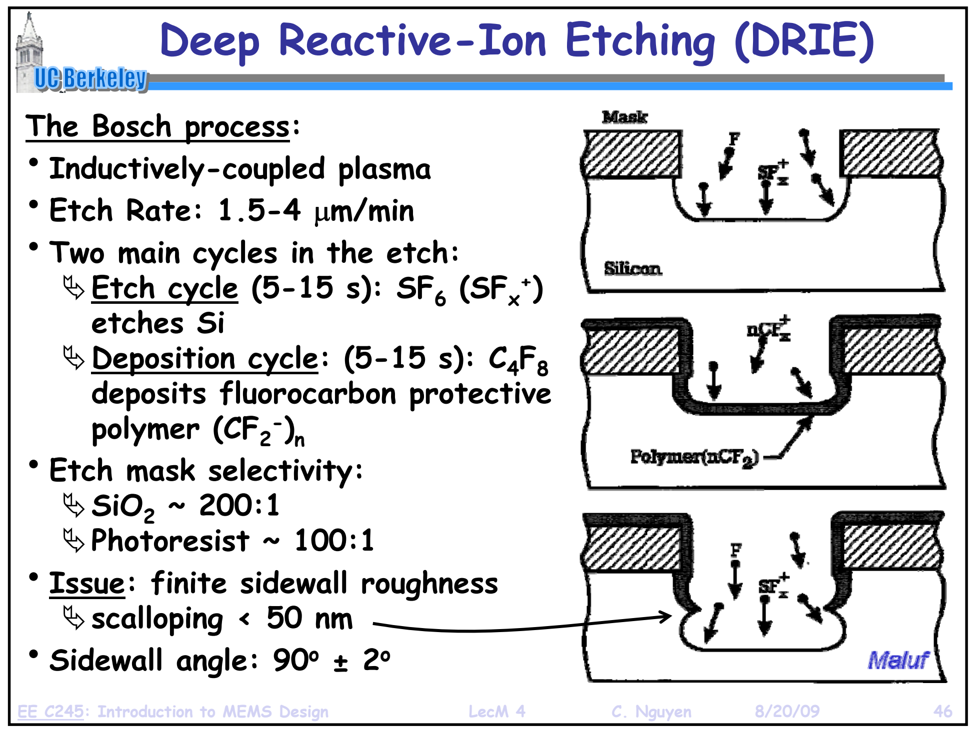

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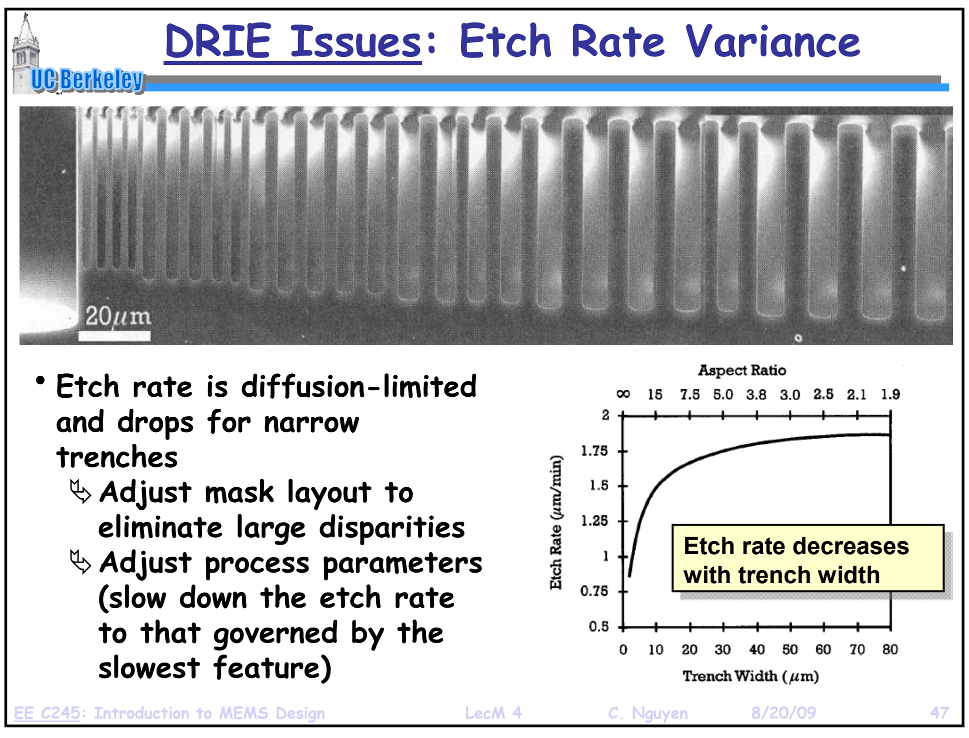

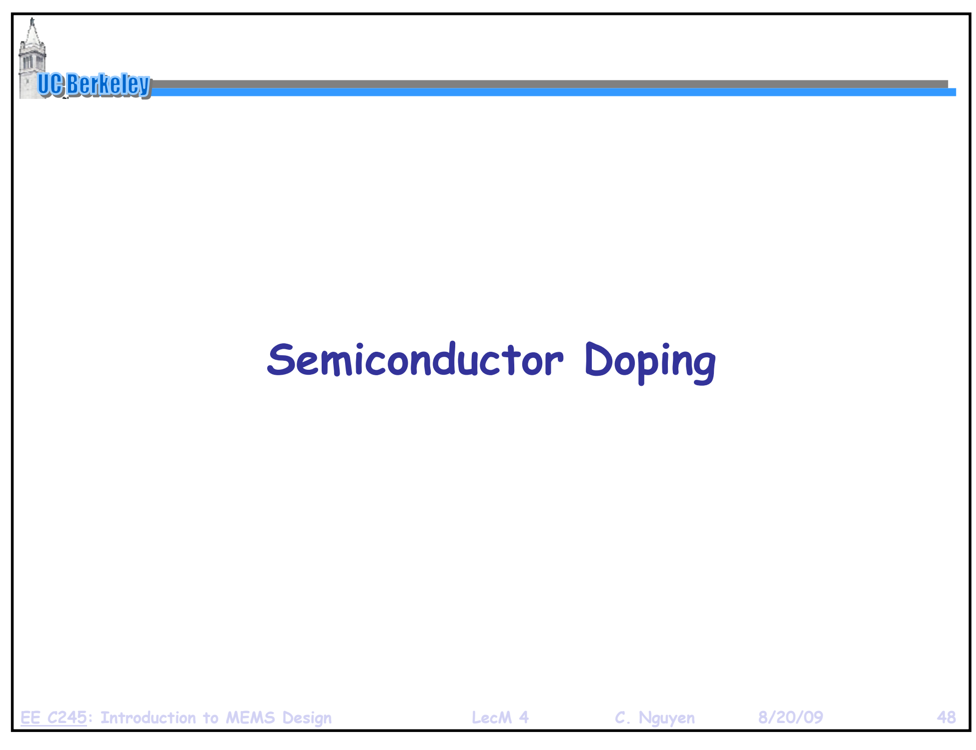

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Doping of Semiconductors

- Semiconductors are not intrinsically conductive
- To make them conductive, replace silicon atoms in the lattice with dopant atoms that have valence bands with fewer or more e^-'s than the 4 of Si
- If more e^-'s, then the dopant is a donor: P, As
  - The extra e^- is effectively released from the bonded atoms to join a cloud of free e^-'s, free to move like e^-'s in a metal

  - The larger the # of donor atoms, the larger the # of free e^-'s → the higher the conductivity

Doping of Semiconductors (cont.)

- Conductivity Equation:

$$\sigma = q\mu_n n + q\mu_p p$$

- If fewer e^-'s, then the dopant is an acceptor: B

  - Lack of an e^- = hole = h^+
  - When e^-'s move into h^+'s, the h^+'s effectively move in the opposite direction → a h^+ is a mobile (+) charge carrier

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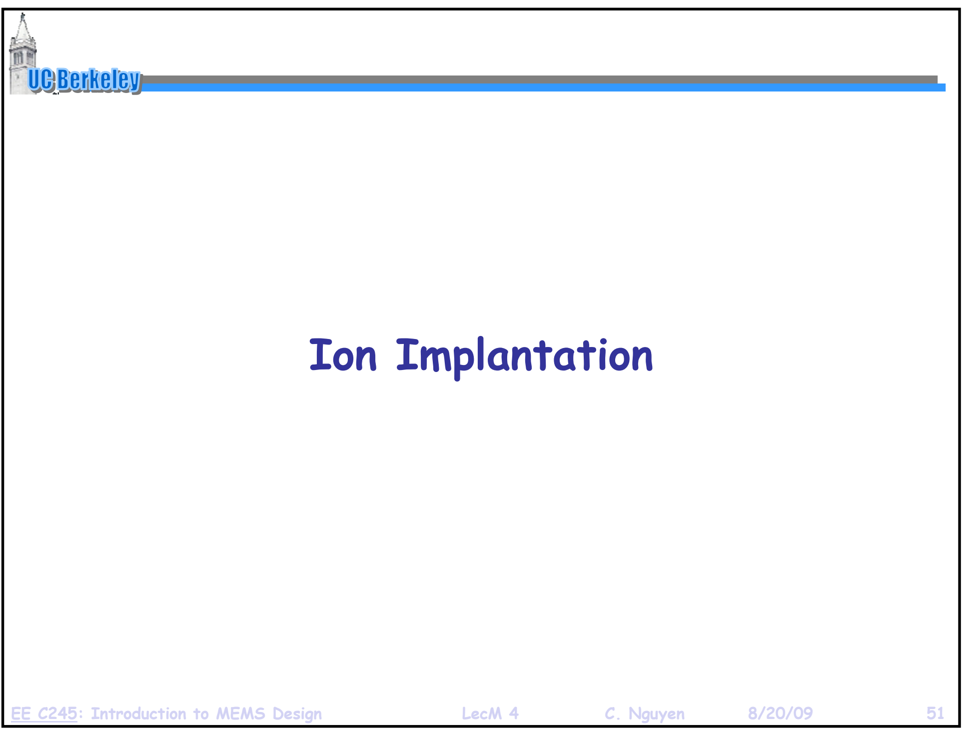

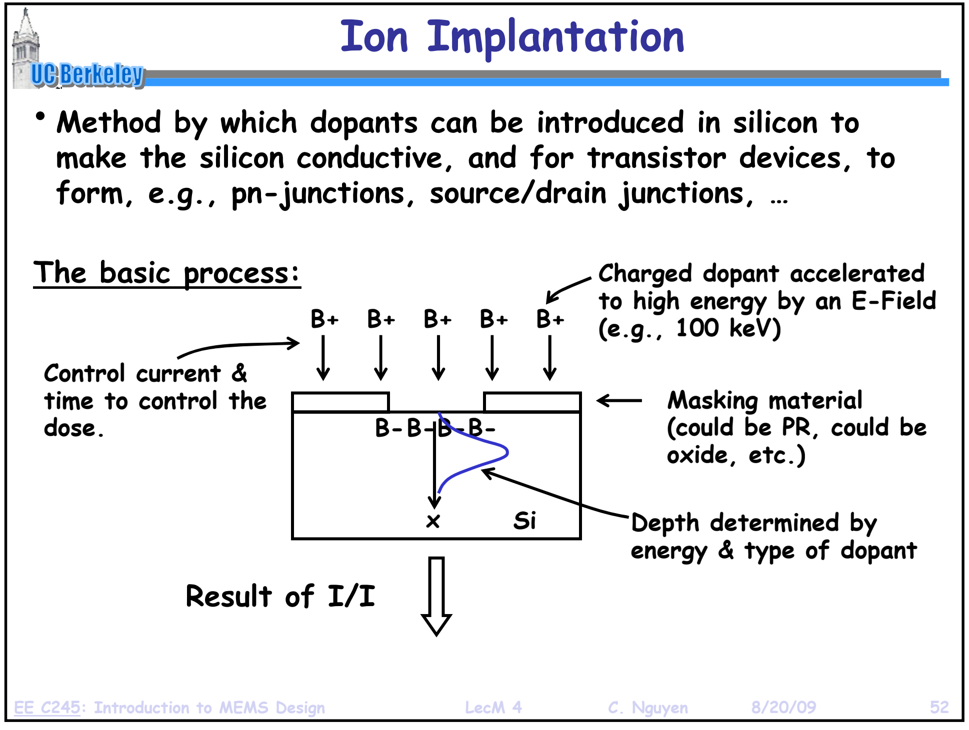

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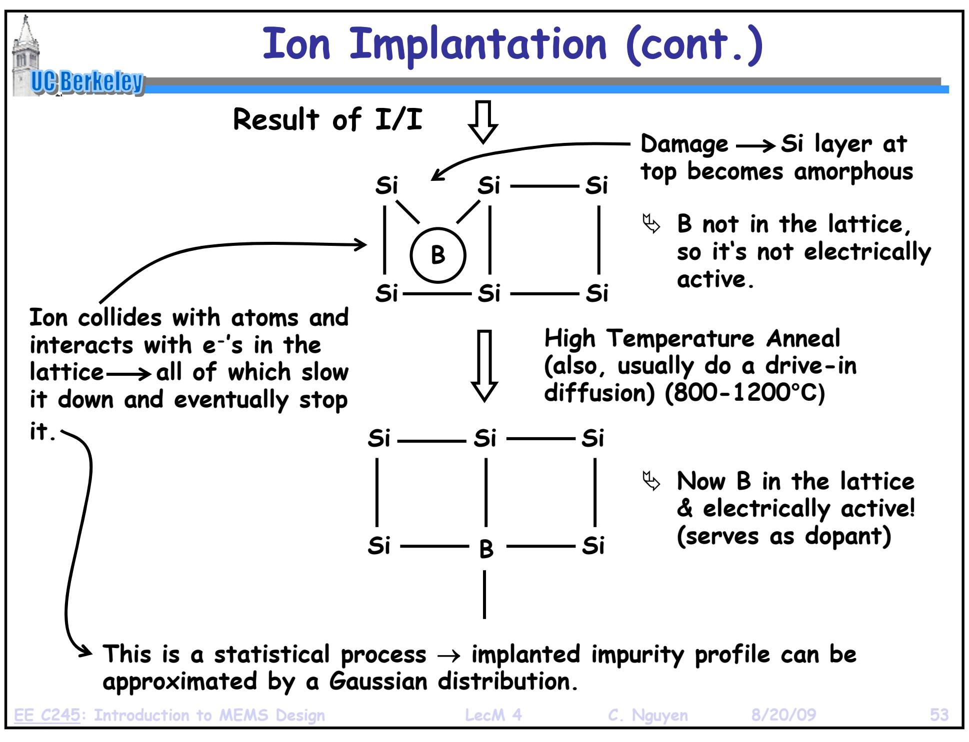

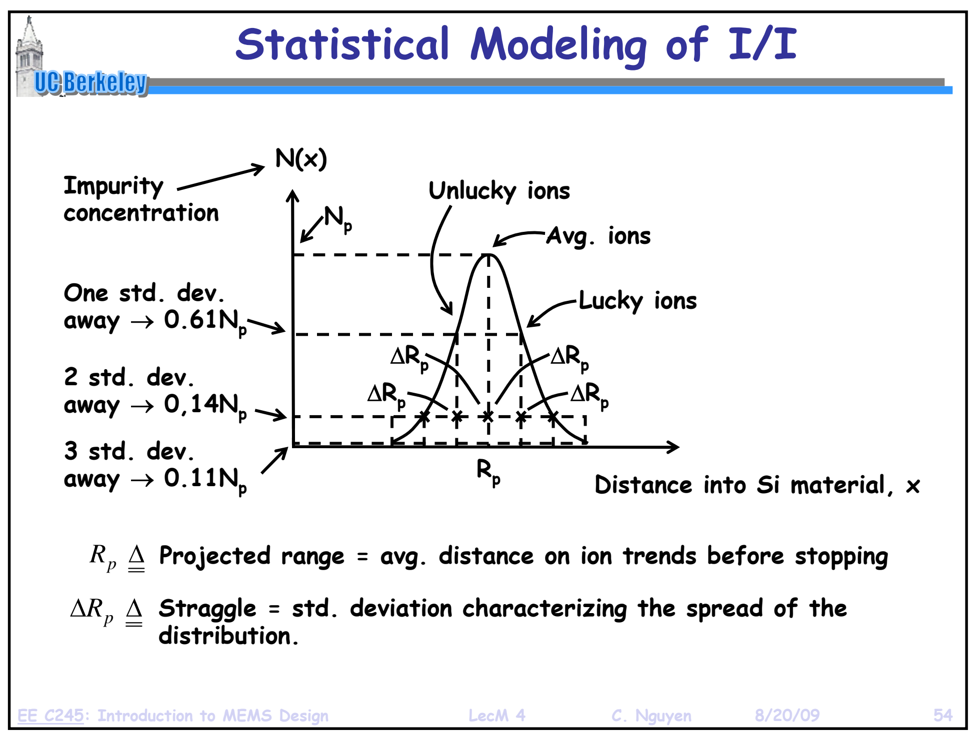

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Analytical Modeling for I/I

Mathematically:

$$N(x) = N_p \exp\left[-\frac{(x-R_p)^2}{2(\Delta R_p)^2}\right]$$

Area under the impurity distribution curve } Implanted Dose = $Q = \int_0^\infty N(x)dx \ [ions/cm^2]$

For an implant completely contained within the Si:

$$Q = \sqrt{2\pi} N_p \Delta R_p$$

Assuming the peak is in the silicon: (putting it in one-sided diffusion form) → So we can track the dopant front during a subsequent diffusion step.

$D_I = Q$

$$N(x) = \frac{D_I/2}{\sqrt{\pi (Dt)_{eff}}} \exp\left[-\frac{(x-R_p)^2}{2(\Delta R_p)^2}\right], \text{ where } (Dt)_{eff} = \frac{(\Delta R_p)^2}{2}$$

I/I Range Graphs

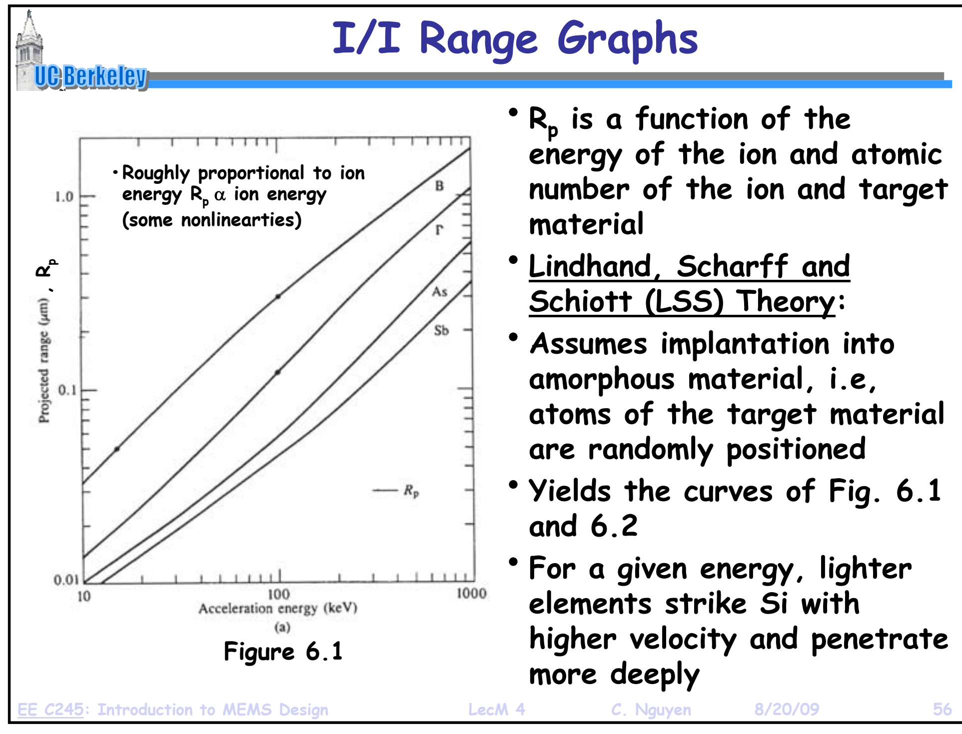

Figure 6.1

- R_p is a function of the energy of the ion and atomic number of the ion and target material
- Lindhand, Scharff and Schiott (LSS) Theory:
- Assumes implantation into amorphous material, i.e, atoms of the target material are randomly positioned
- Yields the curves of Fig. 6.1 and 6.2
- For a given energy, lighter elements strike Si with higher velocity and penetrate more deeply

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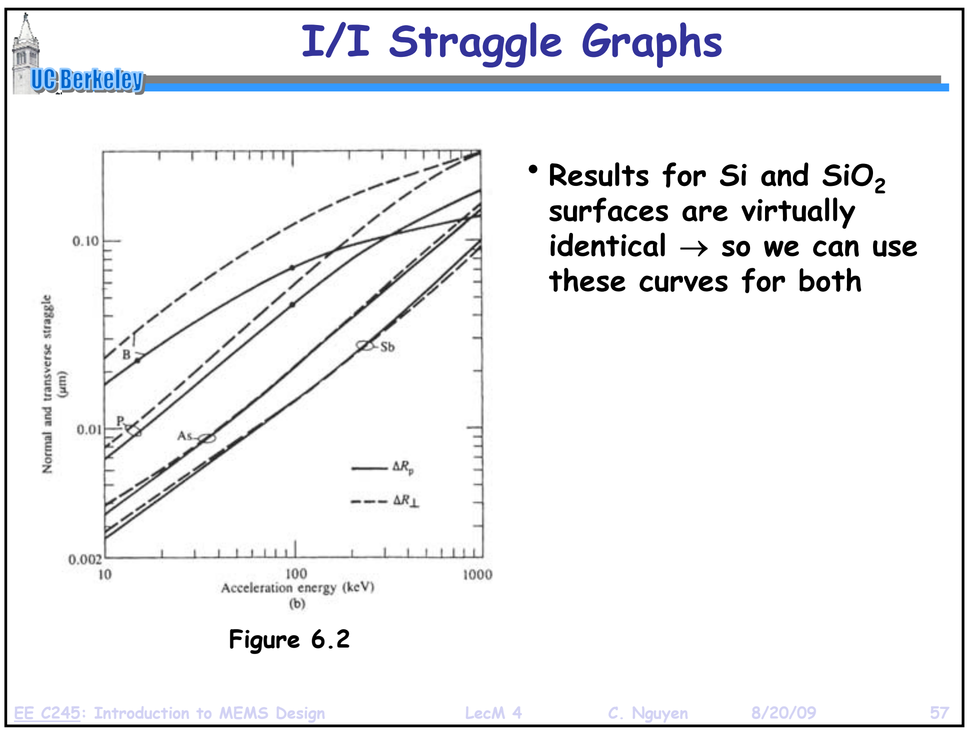

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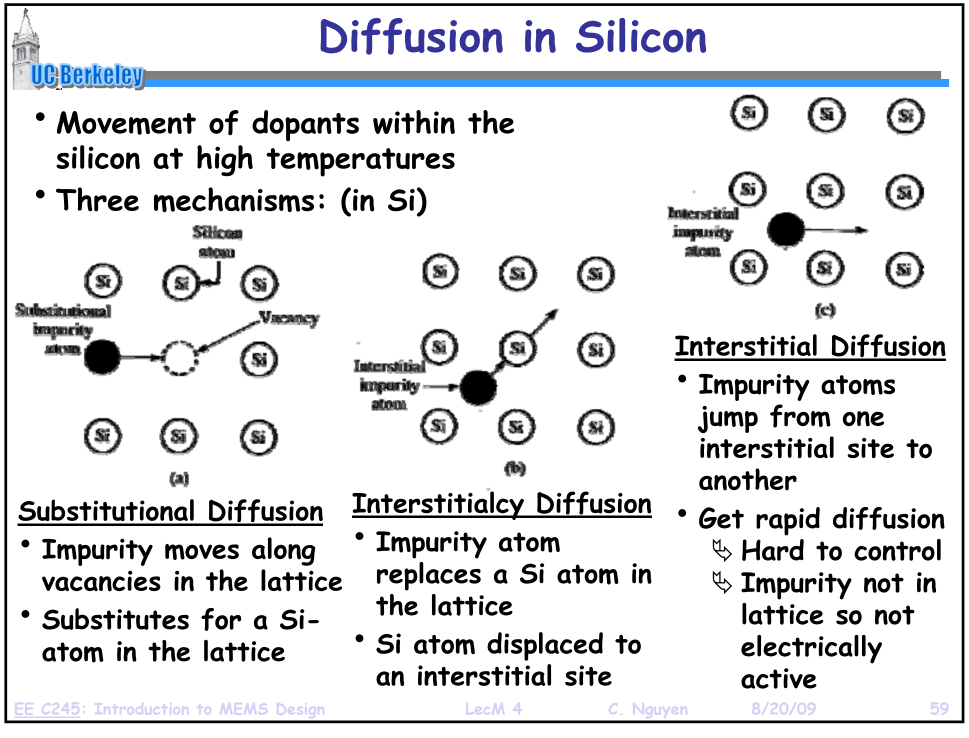

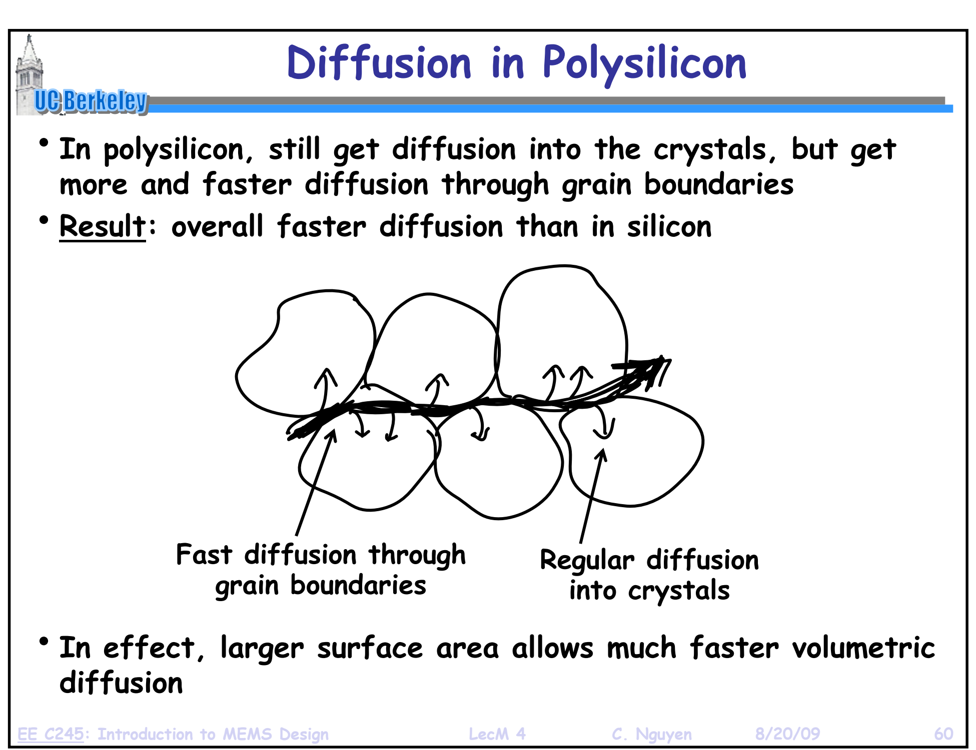

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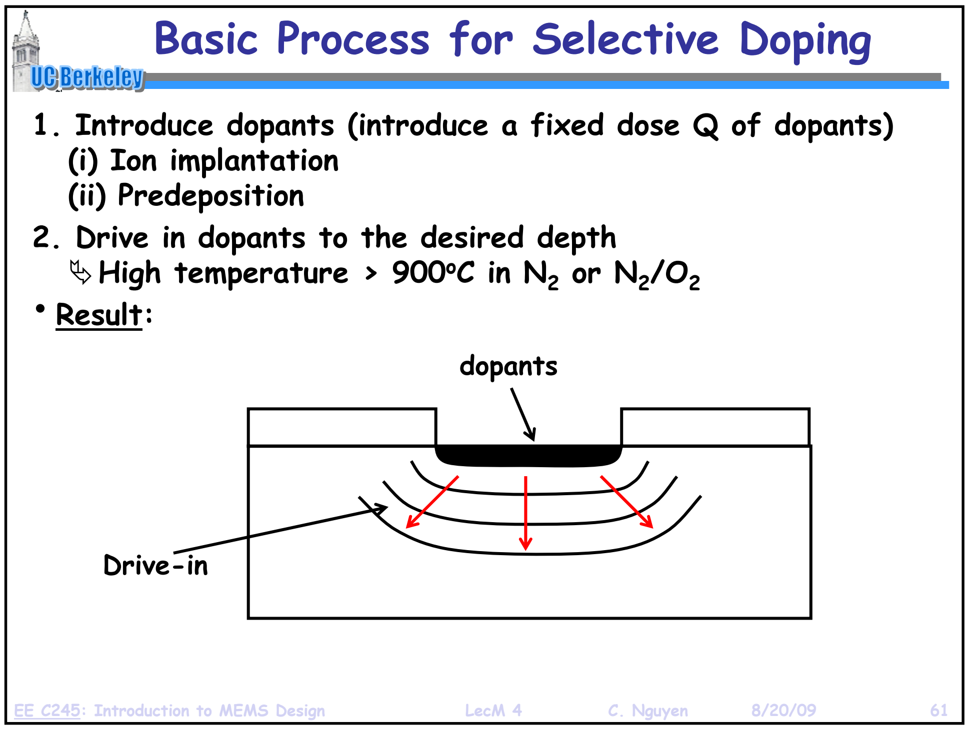

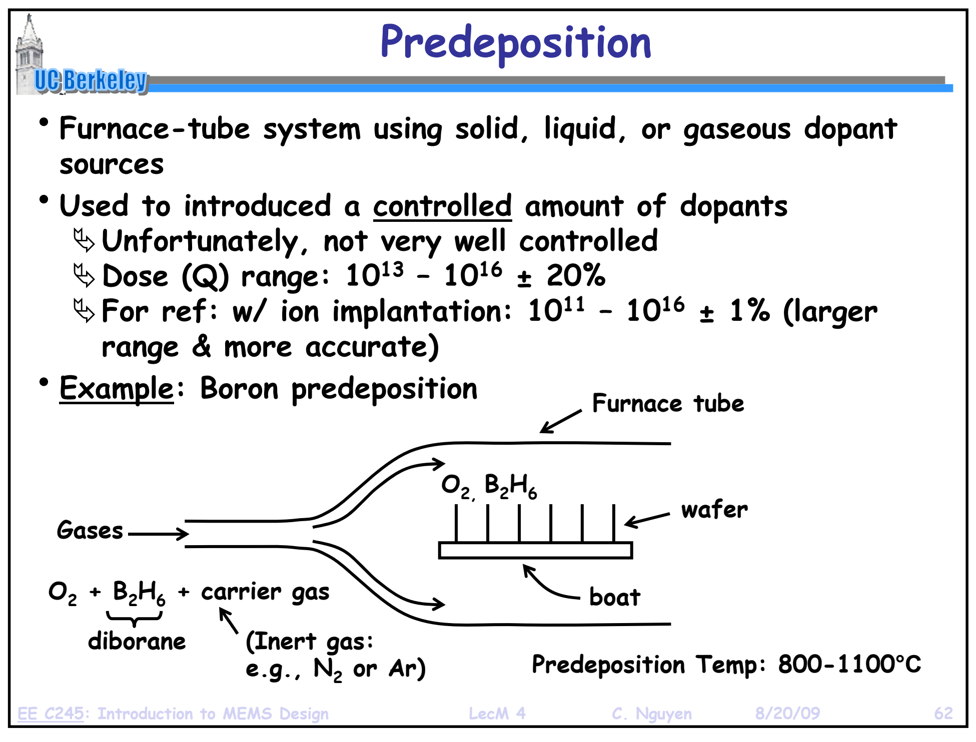

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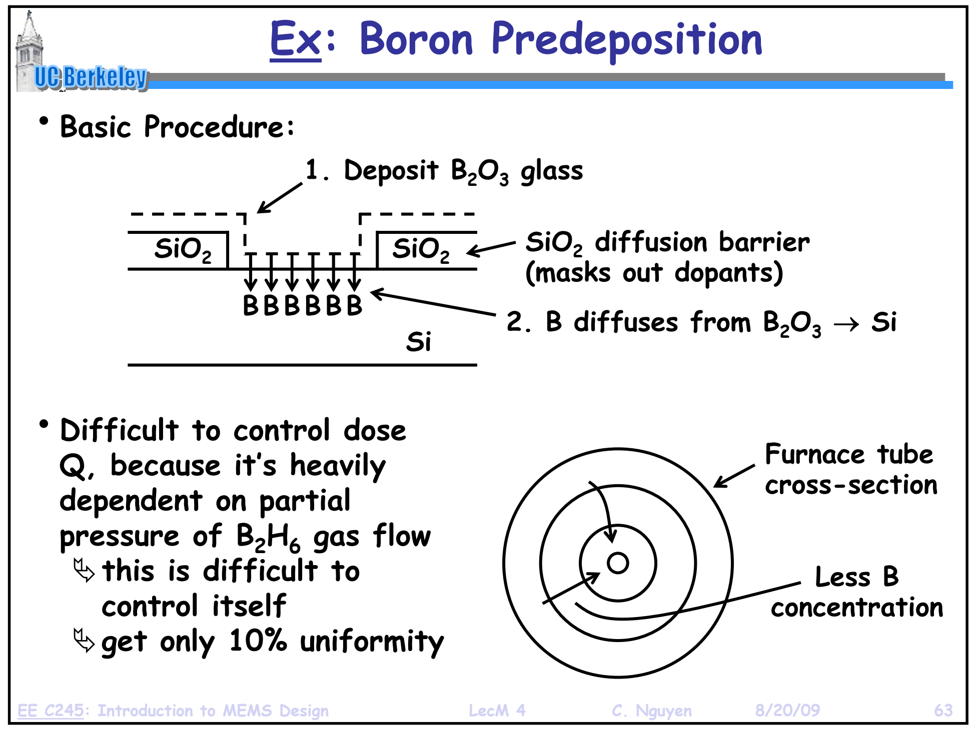
Ex: Boron Predeposition
UC Berkeley
• Basic Procedure:
1. Deposit B2O3 glass
SiO2
SiO2
SiO2 diffusion barrier
(masks out dopants)
B B B B B B
2. B diffuses from B2O3 → Si
Si
• Difficult to control dose Q, because it's heavily dependent on partial pressure of B2H6 gas flow
this is difficult to control itself
get only 10% uniformity
Furnace tube
cross-section
Less B
concentration
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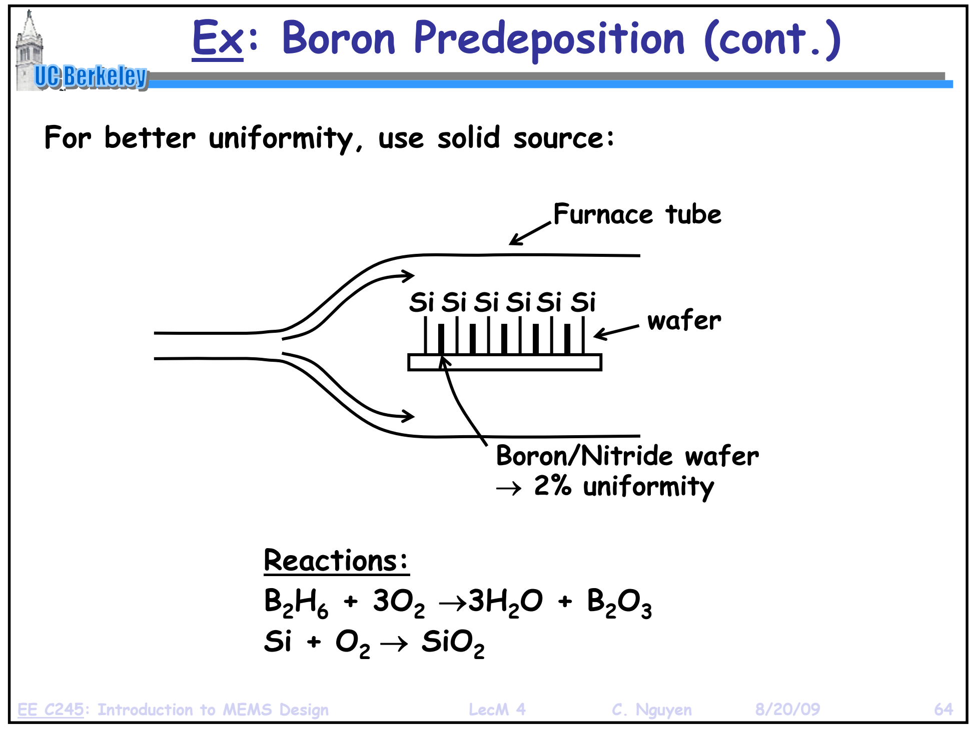
Ex: Boron Predeposition (cont.)
UC Berkeley
For better uniformity, use solid source:
Furnace tube
Si Si Si Si Si Si
wafer
Boron/Nitride wafer
→ 2% uniformity
Reactions:
B2H6 + 3O2 →3H2O + B2O3
Si + O2 → SiO2
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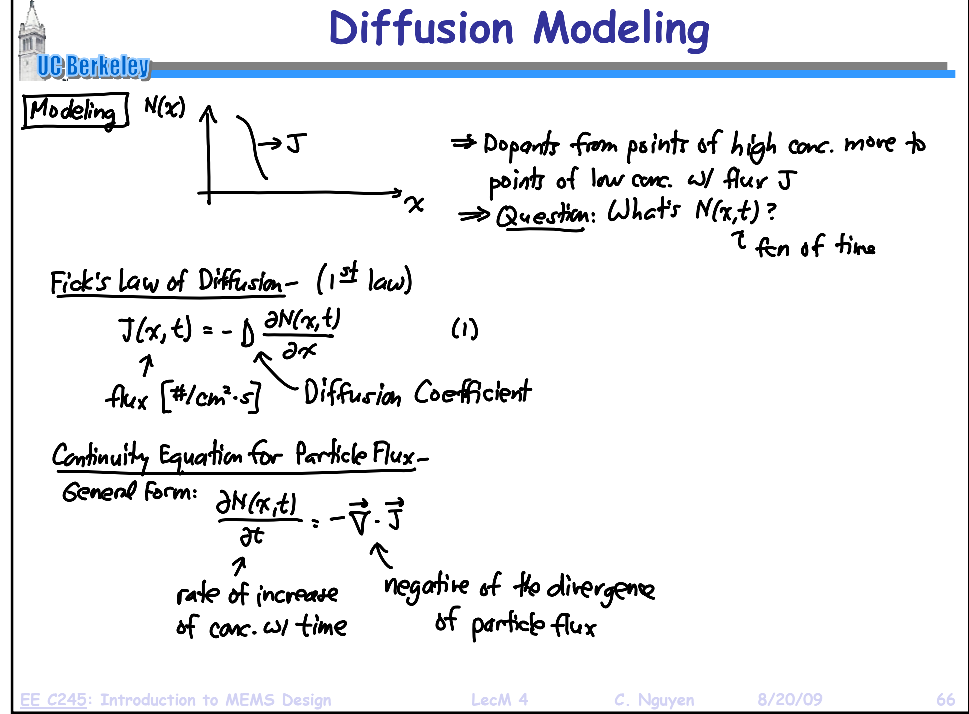
General Comments on Predeposition

- Higher doses only: Q = 10^{13} - 10^{16} cm^{-2} (I/I is 10^{11} - 10^{16})
- Dose not well controlled: ± 20% (I/I can get ± 1%)
- Uniformity is not good
  - ± 10% w/ gas source
  - ± 2% w/ solid source
- Max. conc. possible limited by solid solubility
  - Limited to ~10^{20} cm^{-3}
  - No limit for I/I → you force it in here!
- For these reasons, I/I is usually the preferred method for introduction of dopants in transistor devices
- But I/I is not necessarily the best choice for MEMS
  - I/I cannot dope the underside of a suspended beam
  - I/I yields one-sided doping → introduces unbalanced stress → warping of structures
  - I/I can do physical damage → problem if annealing is not permitted
- Thus, predeposition is often preferred when doping MEMS



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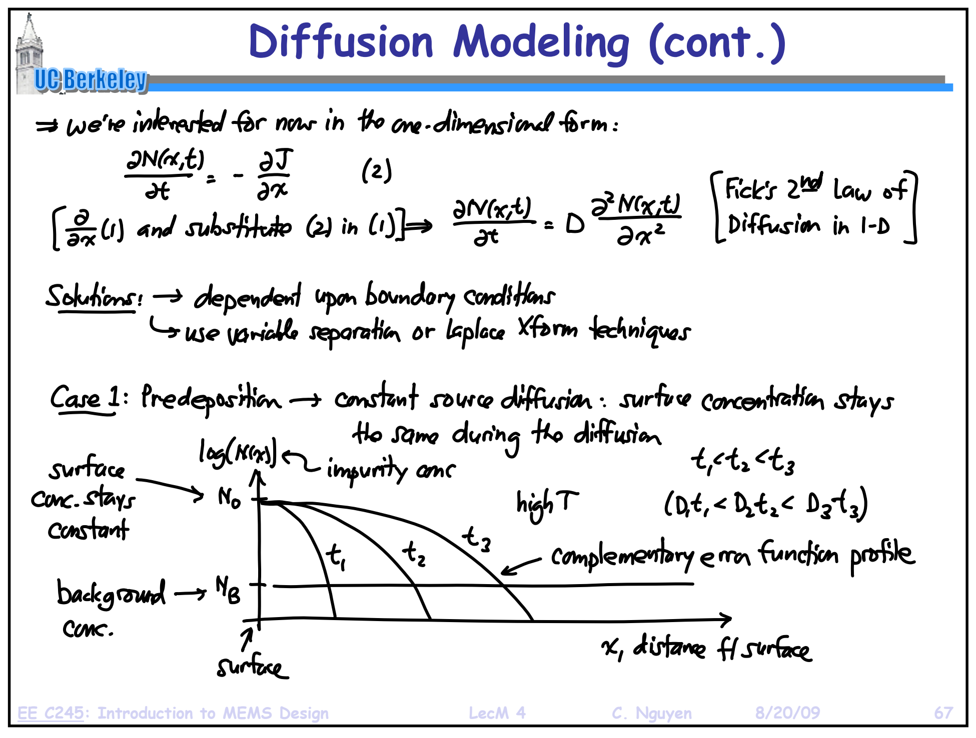

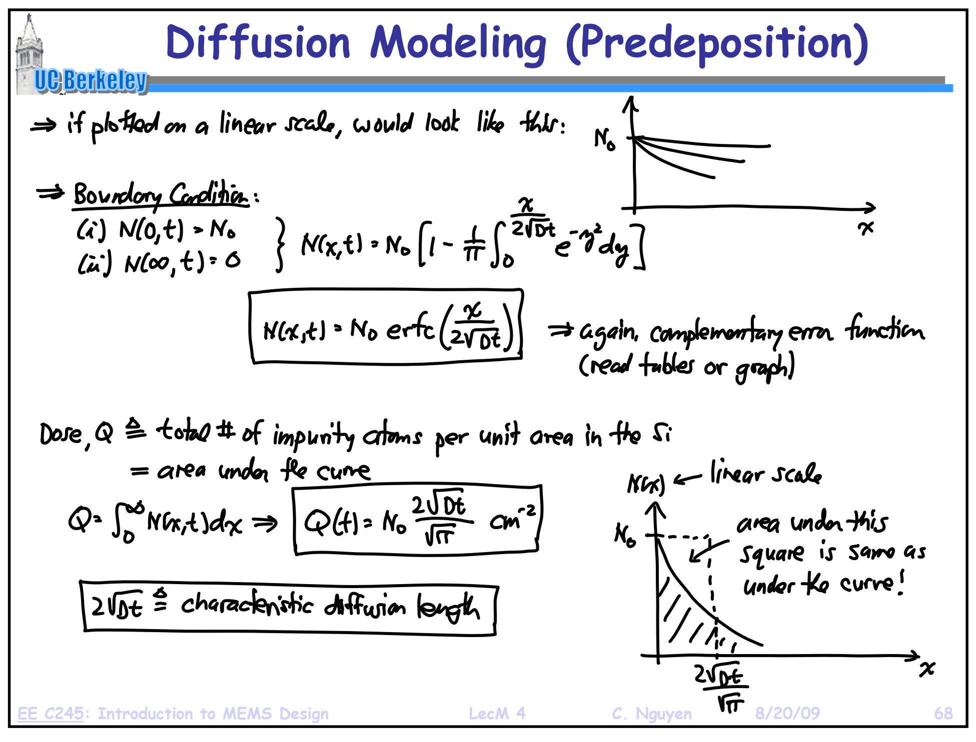

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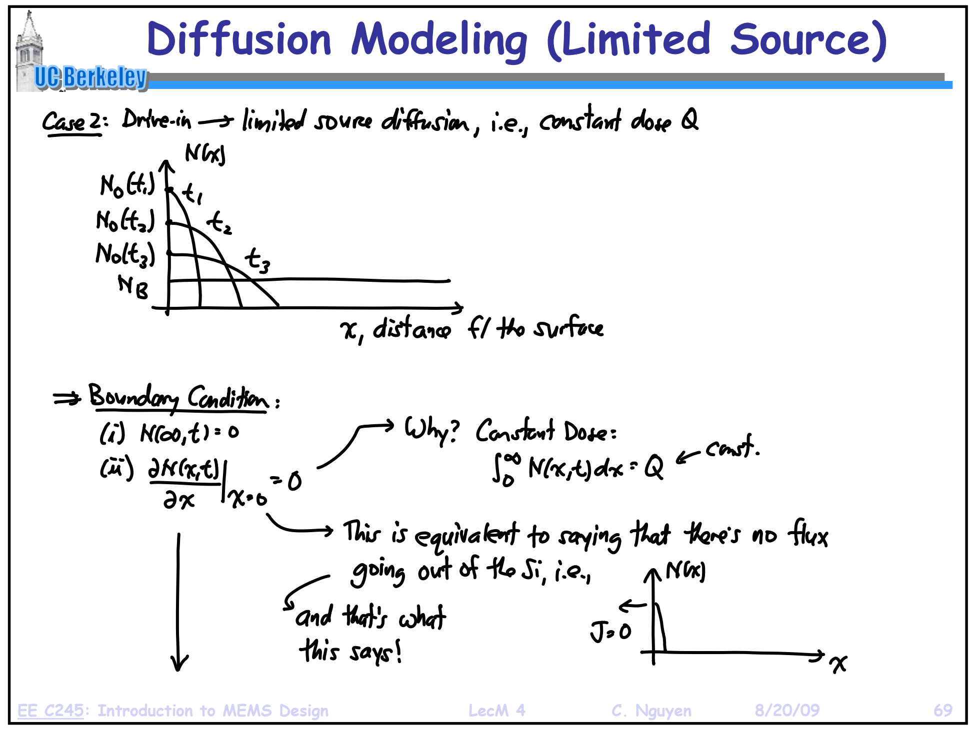

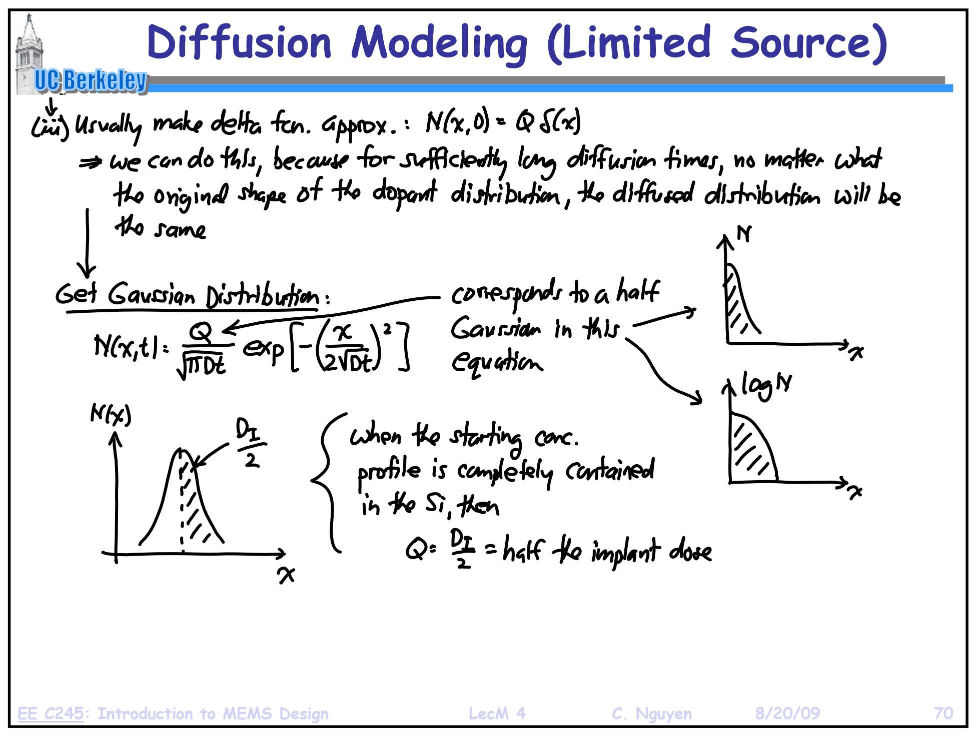

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Two-Step Diffusion

- Two step diffusion procedure:
  - Step 1: predeposition (i.e., constant source diffusion)
  - Step 2: drive-in diffusion (i.e., limited source diffusion)
- For processes where there is both a predeposition and a drive-in diffusion, the final profile type (i.e., complementary error function or Gaussian) is determined by which has the much greater Dt product:

$(Dt)_{predep} \gg (Dt)_{drive-in}$ ⇨ impurity profile is complementary error function

$(Dt)_{drive-in} \gg (Dt)_{predep}$ ⇨ impurity profile is Gaussian (which is usually the case)

Successive Diffusions

- For actual processes, the junction/diffusion formation is only one of many high temperature steps, each of which contributes to the final junction profile
- Typical overall process:
  1. Selective doping
     - Implant → effective $(Dt)_1 = (\Delta R_p)^2/2$ (Gaussian)
     - Drive-in/activation → D_2t_2
  2. Other high temperature steps
     - (eg., oxidation, reflow, deposition) → D_3t_3, D_4t_4, …
     - Each has their own Dt product
  3. Then, to find the final profile, use

$$(Dt)_{tot} = \sum_i D_i t_i$$

in the Gaussian distribution expression.

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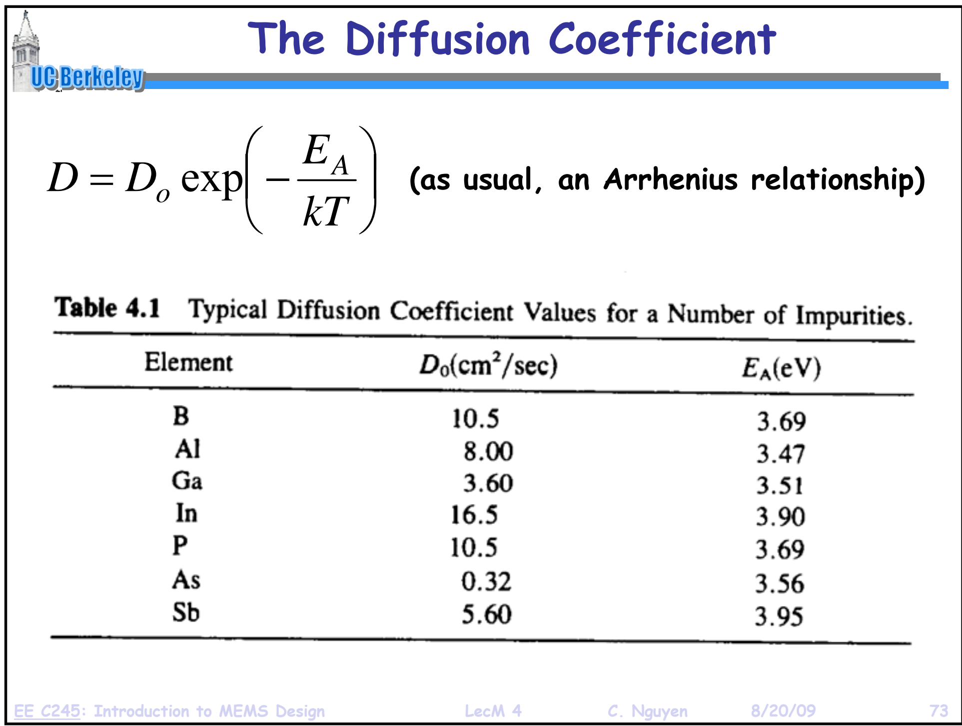
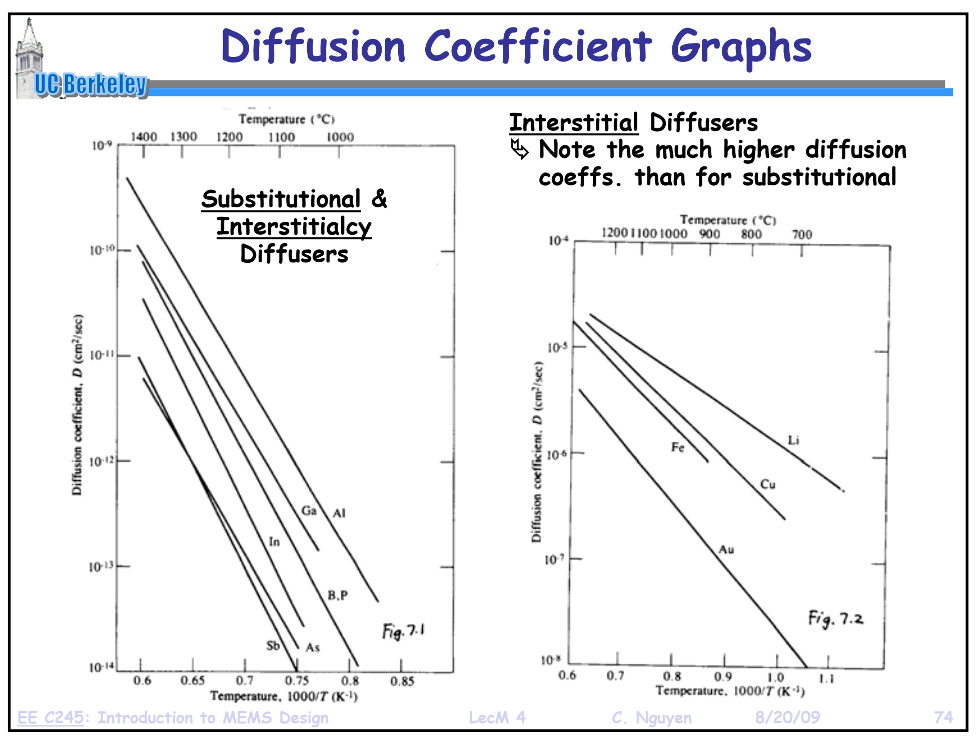


Table 4.1 Typical Diffusion Coefficient Values for a Number of Impurities.

Element	D_0(cm^2/sec)	E_A(eV)
B	10.5	3.69
Al	8.00	3.47
Ga	3.60	3.51
In	16.5	3.90
P	10.5	3.69
As	0.32	3.56
Sb	5.60	3.95



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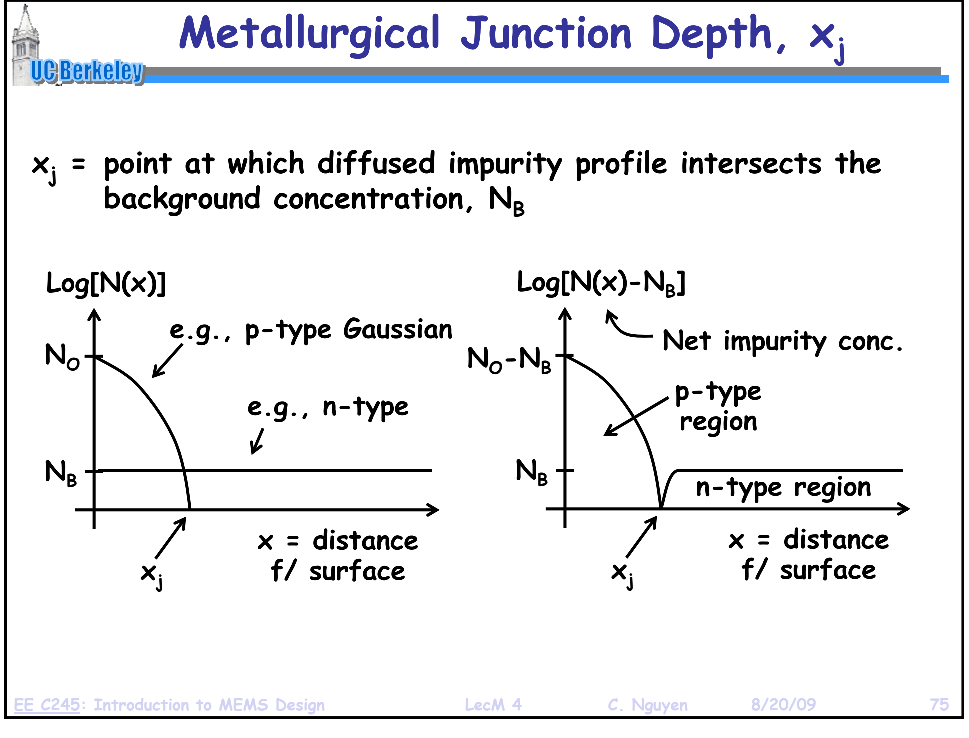

Expressions for x_j

- Assuming a Gaussian dopant profile: (the most common case)

$$N(x_j,t) = N_o \exp\left[-\left(\frac{x_j}{2\sqrt{Dt}}\right)^2\right] = N_B \quad \rightarrow \quad x_j = 2\sqrt{Dt \ln\left(\frac{N_o}{N_B}\right)}$$

- For a complementary error function profile:

$$N(x_j,t) = N_o \mathrm{erfc}\left(\frac{x_j}{2\sqrt{Dt}}\right) = N_B \quad \rightarrow \quad x_j = 2\sqrt{Dt}\,\mathrm{erfc}^{-1}\left(\frac{N_B}{N_o}\right)$$

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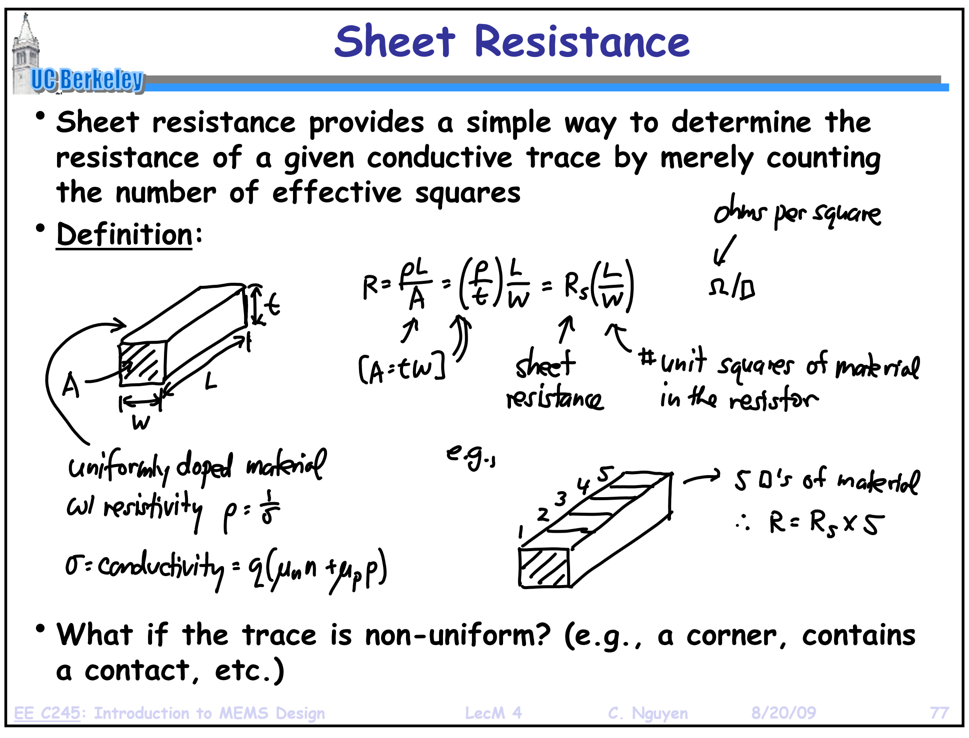

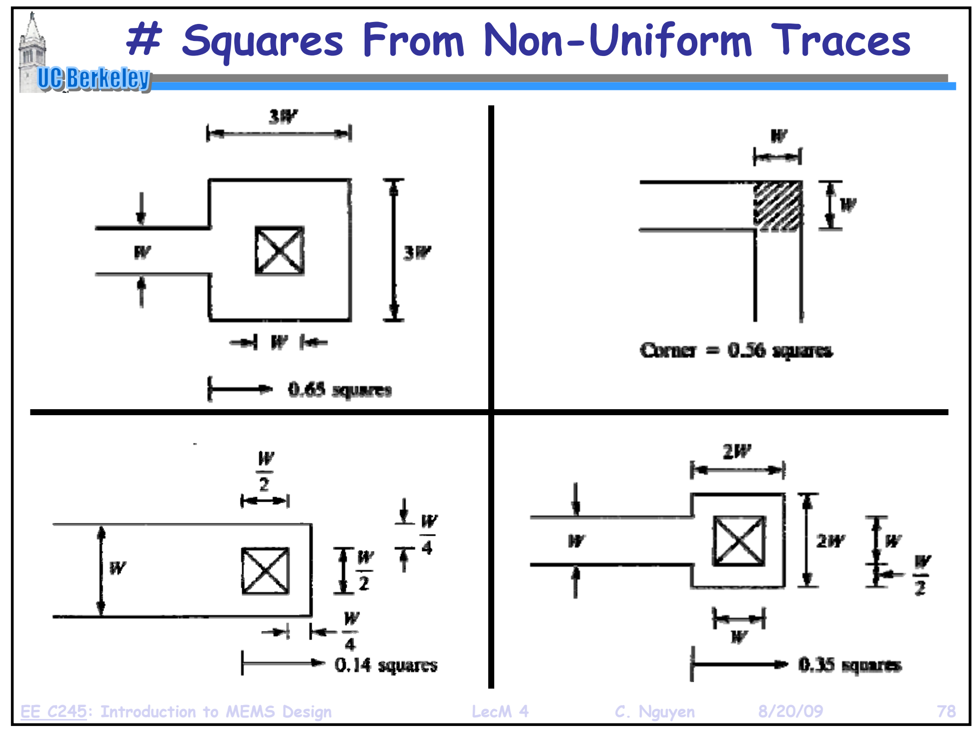

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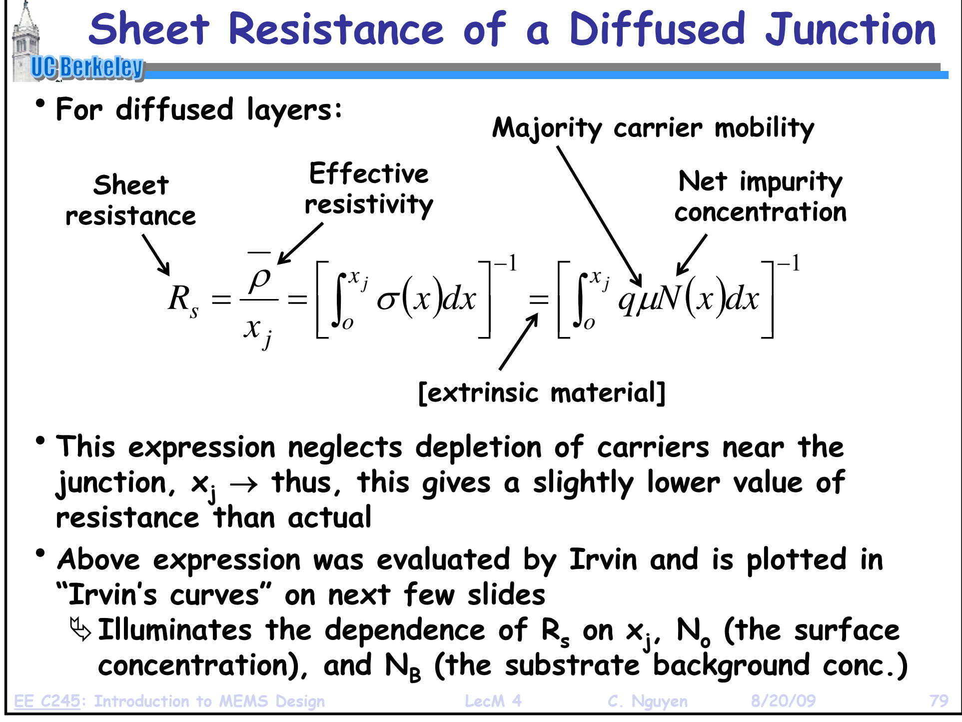

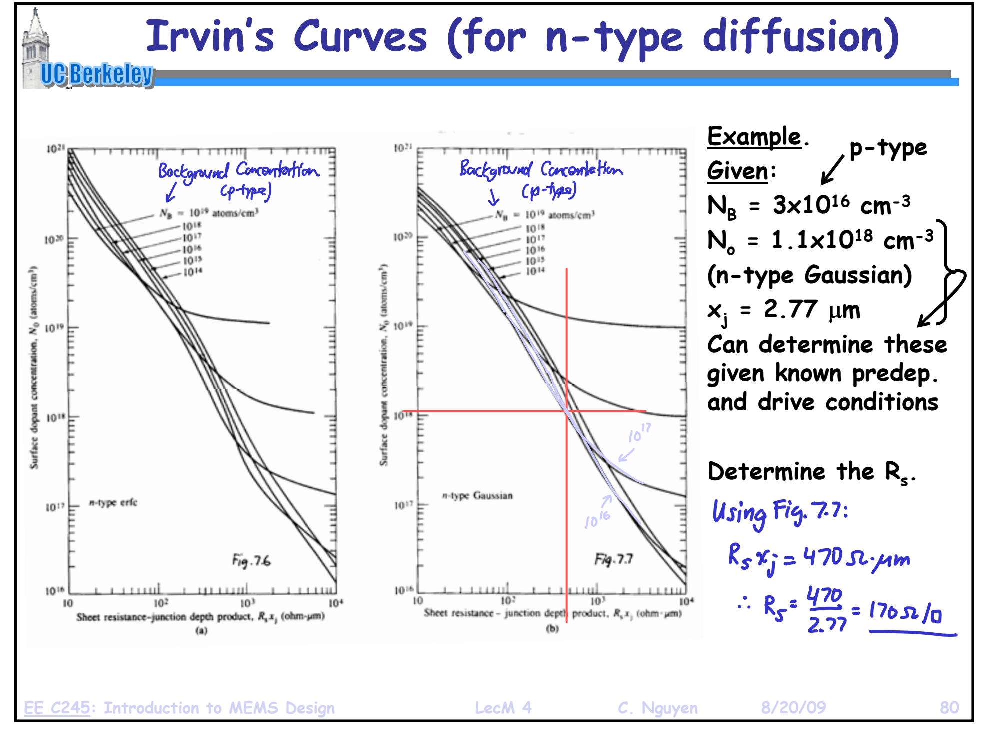

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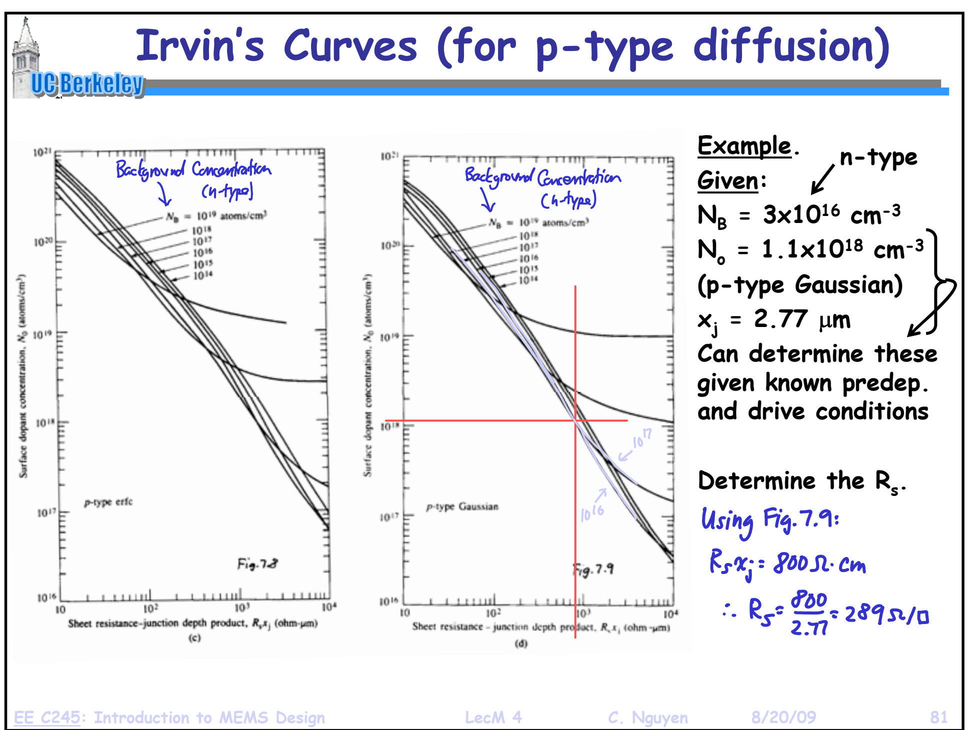

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