

Lecture 3: Benefits of Scaling II

- Announcements:
- As announced last time, I am on travel right now
- This is a pre-recorded video
- The website for this course is up; go to <https://inst.eecs.berkeley.edu/~ee247b/sp17/>
- The notes from last time are online, as well as the video - both in the Lecture link table
- Modules 1 & 2 are online (also, in the Lecture link table)
- Get your computer accounts by following the instructions at the end of the Course Info Sheet (the new one recently uploaded)

• Today:

- Reading: Senturia, Chapter 1
- Lecture Topics:

↳ Benefits of Miniaturization

↳ Examples

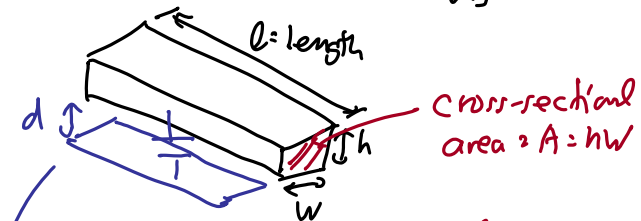
- GHz micromechanical resonators
- Chip-scale atomic clock
- Micro gas chromatograph

• Last Time:

- Going through Module 2, looking at a micromechanical circuit (specifically, a micromechanical filter)
- Continue with this now, going through Module 2, slides 9-30

Review Electrical Resistance First

(then address the thermal R analogy)



$$R_e \triangleq \text{electrical resistance} = \frac{l}{\sigma A}$$

$$C_e = \text{capacitance} = \frac{\epsilon_0 W L}{d}$$

↑
permittivity

↑
electrical conductivity

Stator Energy (charge energy): $\frac{1}{2} CV^2 = E$
 voltage across the capacitor

⇒ if want to be more exact:

Thermal Ckt.

⇒ thermal capacitance: $C_{th} = \frac{\text{volume}}{\text{density}} \times \text{specific heat}$
 stores thermal energy

⇒ thermal resistance: $R_{th} = \frac{l}{kA}$
 l ← length
 kA ← cross-sectional area
 k ← thermal conductivity

Examples

Ex. MEMS type structure (top view)

source of power: assume all energy of laser is absorbed
 What is the temperature on the platform?

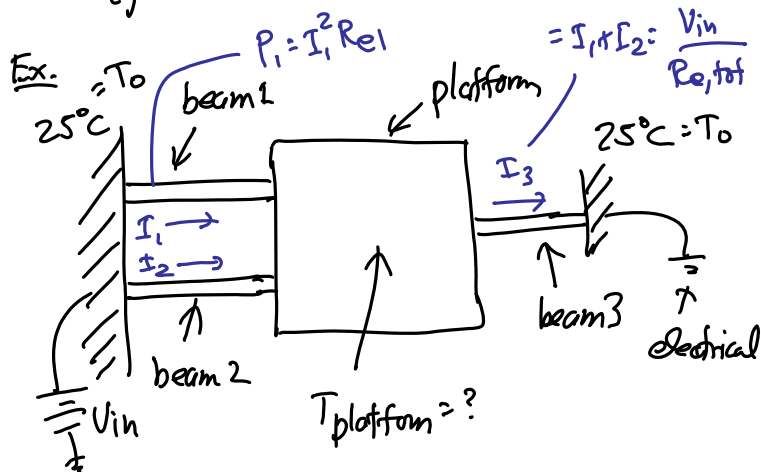
Analogies:

(temperature) $T \rightarrow V$ (voltage)
(power) $P \rightarrow I$ (current)



To Analyze:

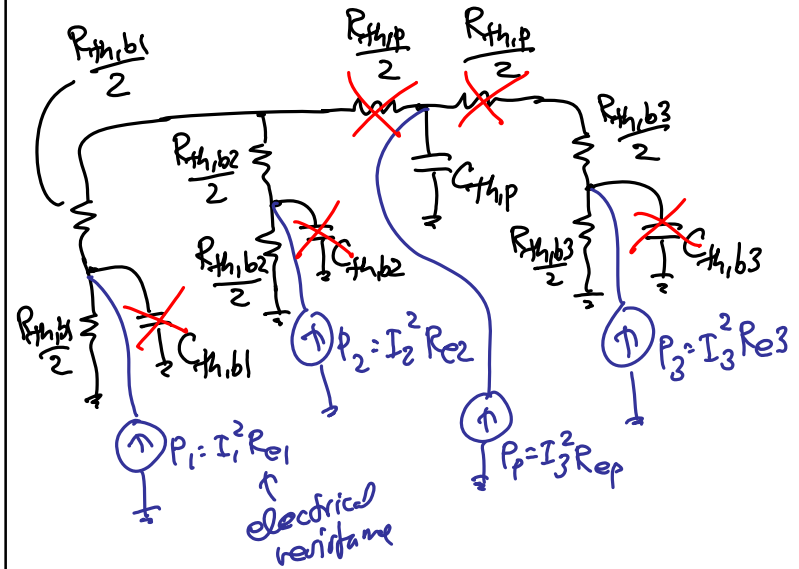
- Remove small elements to simplify the ckt.
- $T_{\text{platform}} = PR_{\text{th,b}}$ (in steady-state)
(just like $V=IR$ in an electrical system)



\rightarrow coupled thermal, electrical system



\Rightarrow Draw the thermal ckt.



To Analyze:

- $I_3 = I_1 + I_2 = \frac{V_{in}}{R_{e,tot}}$ (electrical analysis)
- Get P_i 's. (powers)
- Use superposition to solve the thermal ckt.

handle one power source at a time & sum the temperatures (i.e., thermal voltages) to get the total temperature at any node

