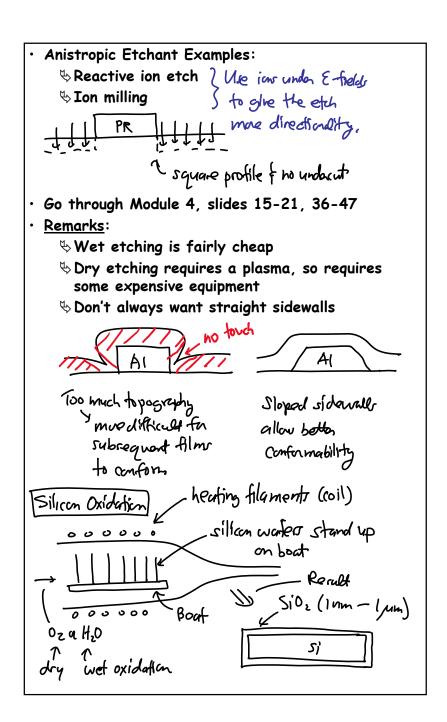
## EE C247B/ME C218: Introduction to MEMS Design

<u>Lecture 6w</u>: Process Modules II

# Lecture 6: Process Modules II Announcements: HW#2 online Lecture Modules 3 & 4 on Process Modules online · Process Module Details lecture videos online These give more details than I will give in class ♦ Watch these if your background in microfabrication is weak -Very helpful for homework (& research) Today: Senturia, Chpt. 3; Jaeger, Chpt. 2, 3, 6 \$Example MEMS fabrication processes Photolithography **♥** Etching **♥** Oxidation **♦ Film Deposition ♥** Diffusion **♥** Ion Implantation Last Time: Going through etching in Module 4 · Now, continue with this ...



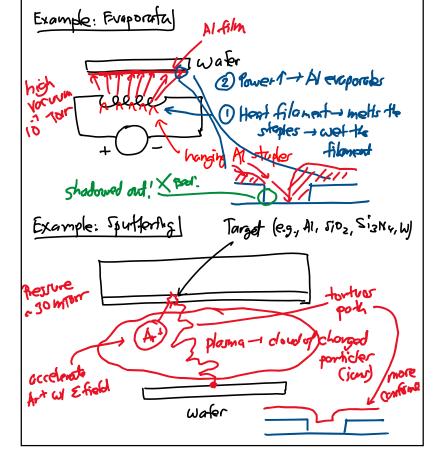
Lecture 6w: Process Modules II

#### · Remarks:

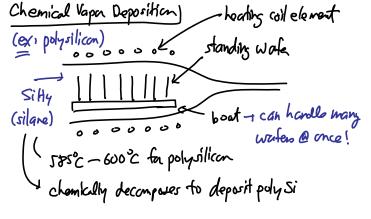
- Uniformity can be better than 2% across the wafer from lot to lot
- $\$  Need to flow the  $O_2$  fairly fast in order to minimize reactant losses from the first boat to the last one

## Thin-Film Deposition:

- For deposition of films like Al (and other metals),
  SiO<sub>2</sub>, Si<sub>3</sub>N<sub>4</sub>, and polysilicon
- · Deposition, not thermal growth



- Also, have chemical vapor deposition (CVD)
  - Schemical reaction involved in deposition of a given thin film
  - High temperature, but not nearly as high as often required for thermal growth



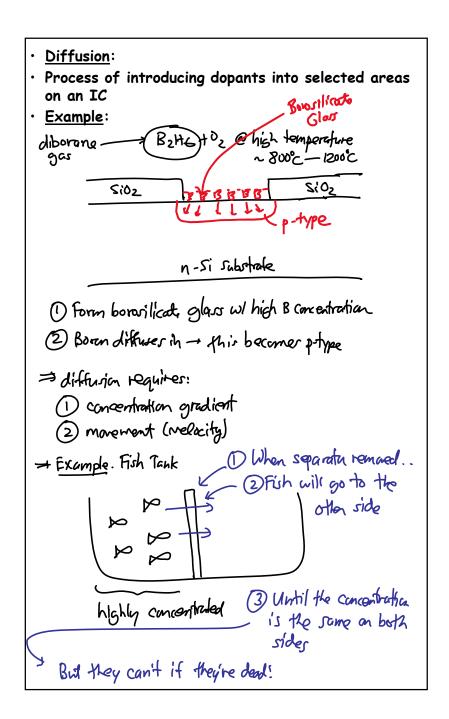
## · Remarks:

- $\$  Lot's of materials can be deposited in a similar manner: polysilicon,  $SiO_2$ ,  $Si_3N_4$ , tungsten
- Compared to sputtering, CVD is less expensive since one can coat many wafers at once; sputtering generally does it one at a time
- For higher temperature, CVD films are much more conformal than sputtered films



<u>Lecture 6w</u>: Process Modules II

Go through Module 4, slides 48-50, on Semiconductor Doping



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