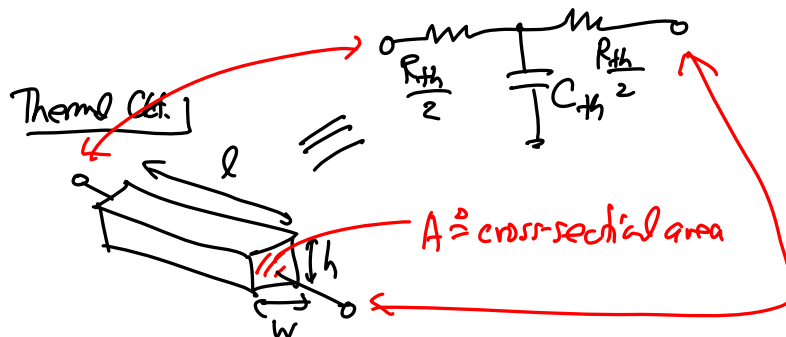


Lecture 4w: Benefits of Scaling IIILecture 4: Benefits of Scaling III

- Announcements:
- Make-up lecture (for last Thursday): Friday, 3-4:30 p.m., 521 Cory
- Problem 3 of HW#1:
  - ↳ Assume the slab is the same material as the beams, i.e., it's not silicon
  - ↳ Also, treat the layer called "isolation layer" as thermal ground
  - ↳ Ta office hours: 367 cory

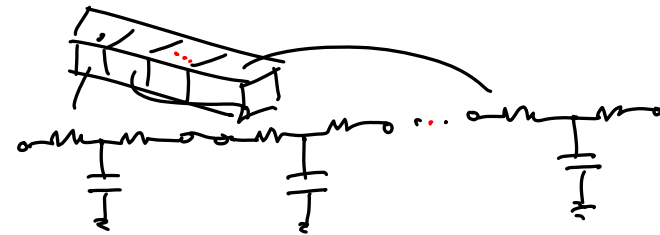
- Today:
- Reading: Senturia, Chapter 1
- Lecture Topics:
  - ↳ Benefits of Miniaturization
  - ↳ Examples
    - GHz micromechanical resonators
    - Chip-scale atomic clock
    - Thermal Circuits
    - Micro gas chromatograph

- Last Time: Covering thermal circuit modeling ... which we now continue ...



⇒ thermal capacitance:  $C_{th} = \rho V C_p$  ← specific heat  
 ↑ ↑  
 density volume → stores thermal energy

⇒ thermal resistance:  
 $R_{th} = \frac{l}{kA}$  ← length  
 ↑ cross-sectional area  
 ↑ thermal conductivity

Examples

Ex. MEMS type structure  $P_{area} = P \rightarrow$  assume all is absorbed

