

Lecture #41

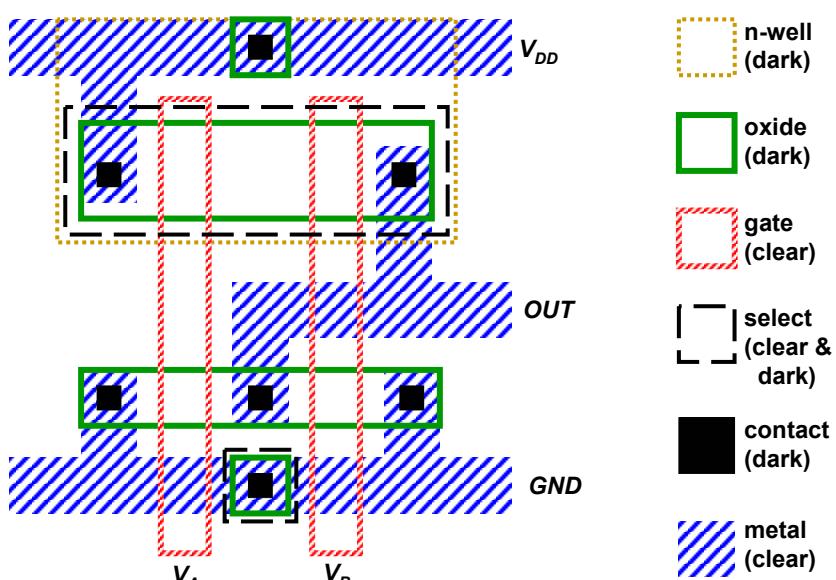
ANNOUNCEMENTS

- No extra credit will be awarded for late Tutebots!
- Chunlong's students should return their kits directly to him next week

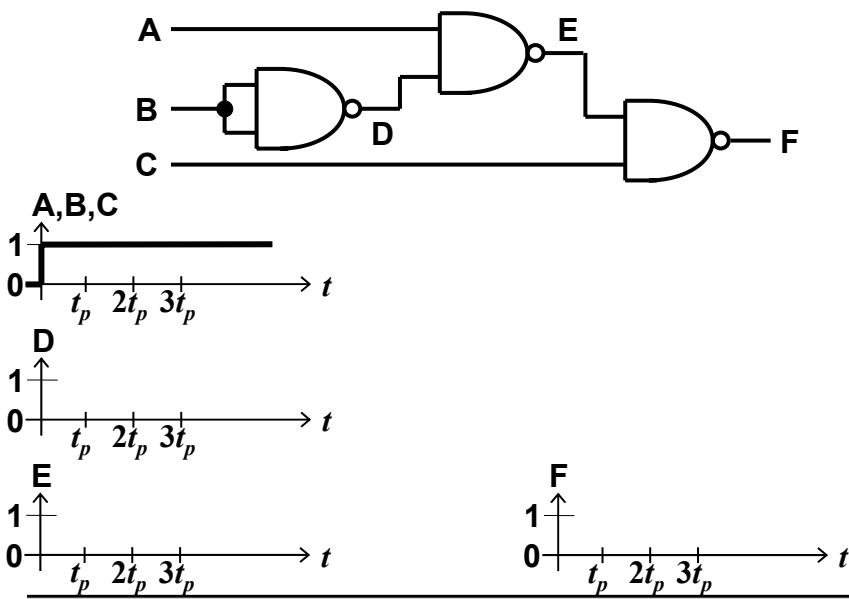
OUTLINE

- CMOS layout example
- Logic circuit example
- Diode circuit example

CMOS Layout Example



Logic Circuit Example



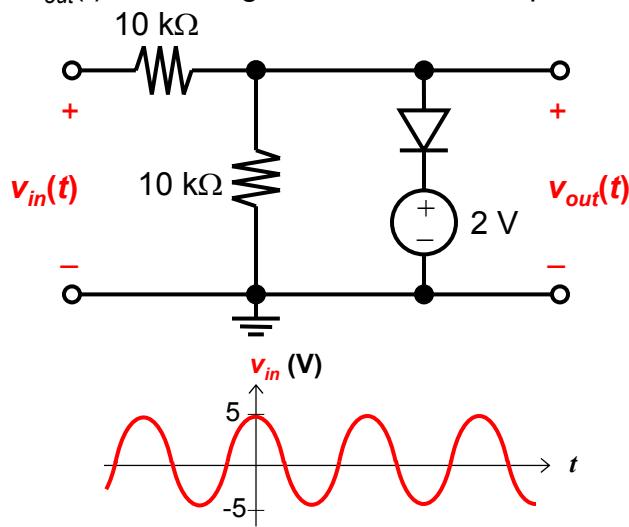
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Diode Circuit Example

Sketch $v_{out}(t)$, assuming that the diode is a perfect rectifier:



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