

EE 40 Class Schedule

Fall, 2007

		Monday	Wednesday	Friday	HW Posted on Thurs.	HW Due on Thurs.	HW Back on Thurs.	Lab	Subjects
Week 1	Date	8/27	8/29	8/31	HW1			No Lab	Definition current, voltage, KCL and KVL, Ohm's law; power and energy; resistor in parallel, series; dependent sources
	Material to be Covered	Ch 1	Ch 1	Ch 2					
Week 2		9/3	9/5	9/7	HW2	HW1		1.Introduction	Node analysis, mesh analysis,
		Holiday	Ch 2	Ch 2					
Week 3		9/10	9/12	9/14		HW2	HW1	2.Equivalent Circuits	superposition, equivalent circuits; capacitor and inductor; intro first order circuits;
		Ch 2	Ch 2	Ch 3, 4					
Week 4		9/17	9/19	9/21	HW3			No Lab	transient response first order ckts; forced response; natural response
		Test 1: Ch 1+2	Ch 4	Ch 4					
Week 5		9/24	9/26	9/28	HW4	HW3		3.RC Circuits	second order Ckt; Complex number; phasors; circuit analysis with complex impedance;
		Ch 4	Ch 4	Ch 5					
Week 6		10/1	10/3	10/5		HW4	HW3	4.RLC Transients	dB, logarithmic algebra; Bode Plots; 1st order filters
		Ch 5	Ch 6+SR-1	Ch 6+SR-1					
Week 7		10/8	10/10	10/12	HW5			No Lab	freq response; Bode plots; 1st and 2nd order filter; op amp
		Test 2: Ch 3-5	Ch 6+SR-1	Ch 6+SR-1					
Week 8		10/15	10/17	10/19	HW6	HW5		5.RC Filters	Op Amps (amp, inverting amp, summing, differential, integrating, A/D);
		Ch 6+SR-1	Ch 6+SR-1	Ch 14					
Week 9		10/22	10/24	10/26		HW6	HW5	6. Op-Amps	Op Amp; Diode circuits (loadline analysis)
		Ch 14	Ch 14	Ch 14					
Week 10		10/29	10/31	11/2	HW7			No Lab	Diode circuits (rectifier, clipper, clamping, peak detector, level shift)
		Ch 10+SR-2	Ch 10+SR-2	Test 3: Ch 6+14+SR-1					
Week 11		11/5	11/7	11/9	HW8	HW7		7. Diodes	Midterm 2; Semiconductor Physics;
		Ch 10+SR-2	Ch 10+SR-2	SR-3					
Week 12		11/12	11/14	11/16	HW9	HW8	HW7	8. Project 1	Semiconductor Physics (bandgap, doping, gauss' law and poisson eq)
		Holiday	SR-3	SR-3					
Week 13		11/19	11/21	11/23		HW9	HW8	9. Project 2	MOSFET; NMOS; PMOS;
		SR-3	Ch 12+SR-4&5	Holiday					
Week 14		11/26	11/28	11/30	HW10				Inverters; Q-point and loadline analysis
		Ch 12+SR-4&5	Ch 12+SR-4&5	Test 4: Ch 10 + SR-2&3					
Week 15		12/3	12/5	12/7		HW10		10. Project 3	Binary numbers; Boolean Algebra; logic gates; MOSFET (model)
		Ch 12+SR-4&5	Ch 12+SR-4&5	Ch 7					
		12/10					HW10		
		Ch 7							
		Project Competition 12/10 3-5 pm							
		Final Exam 8-11 am on Tuesday 12/13/2006 (Exam Group 1)							
	Notes:	SR stands for Supplementary Reader; SR-1 is SR chapter 1							
		All tests include material covered in Lecture Notes							