Lecture #6

<u>OUTLINE</u>

- Complete Mesh Analysis Example(s)
- Superposition
- Thévenin and Norton equivalent circuits
- Maximum Power Transfer

Reading

Chapter 2

Superposition

A *linear circuit* is constructed only of linear elements (linear resistors, linear dependent sources) and independent sources.

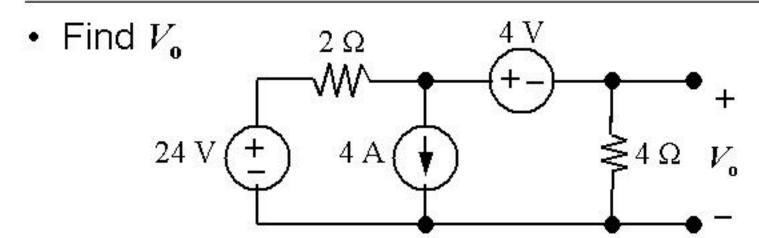
Principle of Superposition:

 In any linear circuit containing multiple independent sources, the current or voltage at any point in the network may be calculated as the algebraic sum of the individual contributions of each source acting alone.

Procedure:

- 1. Determine contribution due to an independent source
 - Set all other sources to 0
- Repeat for each independent source
- Sum individual contributions to obtain desired voltage or current

Superposition Example



Equivalent Circuit Concept

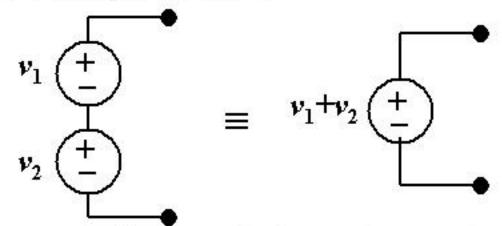
 A network of voltage sources, current sources, and resistors can be replaced by an equivalent circuit which has identical terminal properties (I-V characteristics) without affecting the operation of the rest of the circuit.



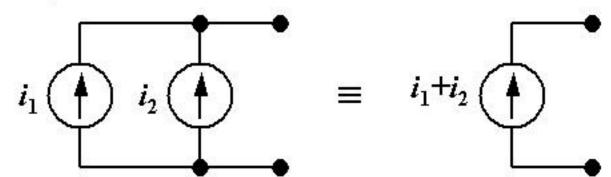
$$i_{\mathbf{A}}(v_{\mathbf{A}}) = i_{\mathbf{B}}(v_{\mathbf{B}})$$

Source Combinations

Voltage sources in series can be replaced by an equivalent voltage source:

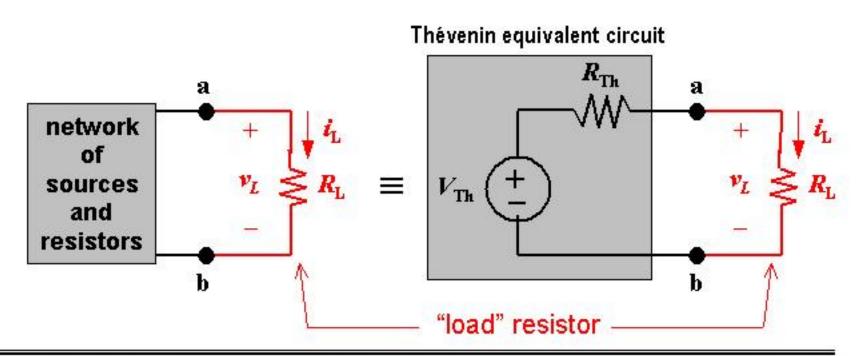


 Current sources in parallel can be replaced by an equivalent current source:



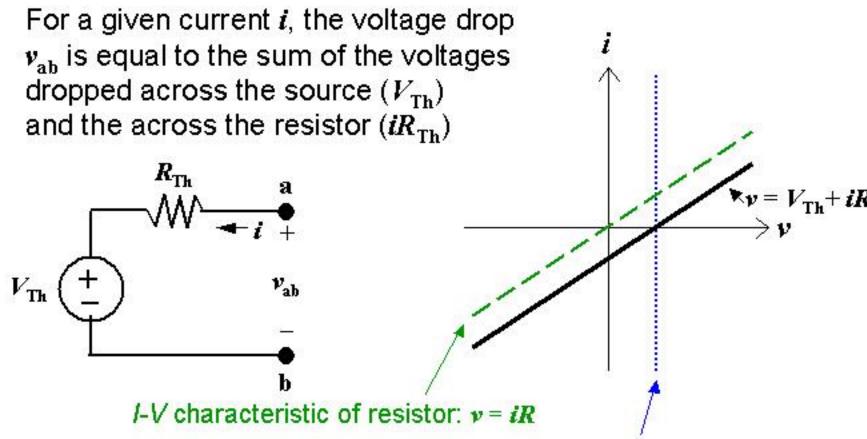
Thévenin Equivalent Circuit

 Any* linear 2-terminal (1-port) network of indep. voltage sources, indep. current sources, and linear resistors can be replaced by an equivalent circuit consisting of an independent voltage source in series with a resistor without affecting the operation of the rest of the circuit.



I-V Characteristic of Thévenin Equivalent

 The I-V characteristic for the series combination of elements is obtained by adding their voltage drops:



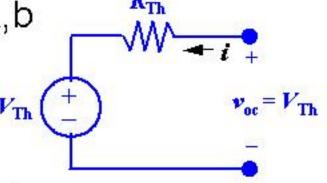
I-V characteristic of voltage source: $v = V_{Th}$

Finding V_{Th} and R_{Th}

Only two points are needed to define a line. Choose two convenient points:

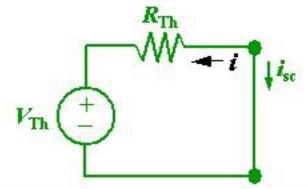
Open circuit across terminals a,b

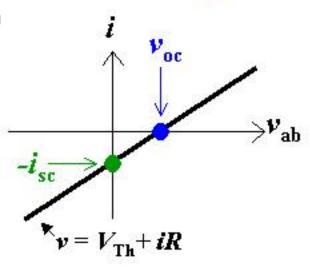
$$i = 0, v_{ab} \equiv v_{oc}$$



Short circuit across terminals a,b

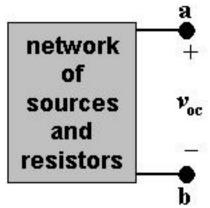
$$v_{ab} = 0$$
, $i \equiv -i_{sc} = -V_{Th}/R_{Th}$



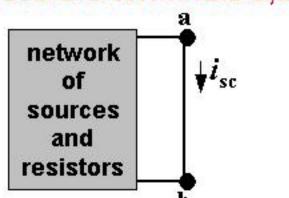


Calculating a Thévenin Equivalent

1. Calculate the **open-circuit voltage**, v_{oc}

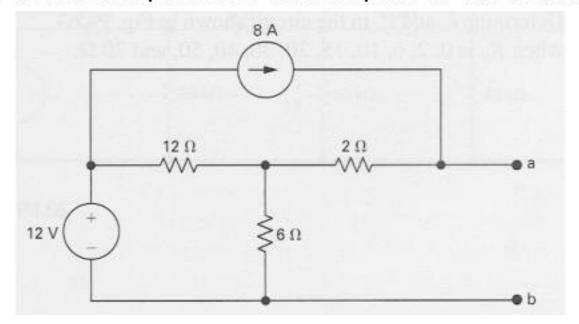


- Calculate the short-circuit current, i_{sc}
 - Note that i_{sc} is in the direction of the open-circuit voltage drop across the terminals a,b!



Thévenin Equivalent Example

Find the Thevenin equivalent with respect to the terminals a,b:



Alternative Method of Calculating R_{Th}

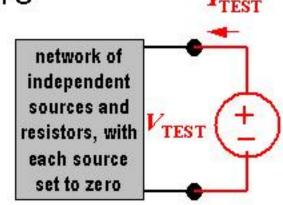
For a network containing only independent sources and linear resistors:

- Set all independent sources to zero voltage source → short circuit current source → open circuit
- 2. Find equivalent resistance $R_{\rm eq}$ between the terminals by inspection $R_{\rm Th} = R_{\rm eq}$

Or, set all independent sources to zero

- 1. Apply a test voltage source V_{TEST}
- Calculate I_{TEST}

$$R_{ ext{Th}} = rac{V_{ ext{TEST}}}{I_{ ext{TEST}}}$$



independent

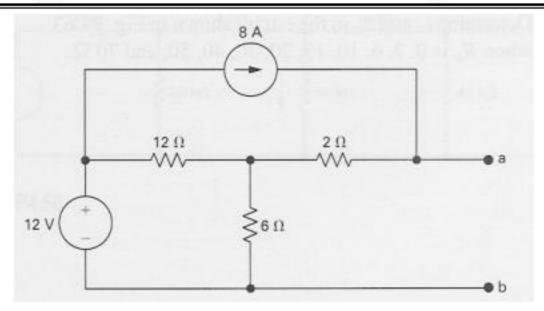
sources and

resistors, with

each source

set to zero

$R_{\rm Th}$ Calculation Example #1



Set all independent sources to 0:

Comments on Dependent Sources

A dependent source establishes a voltage or current whose value depends on the value of a voltage or current at a specified location in the circuit.

(device model, used to model behavior of transistors & amplifiers)

To specify a dependent source, we must identify:

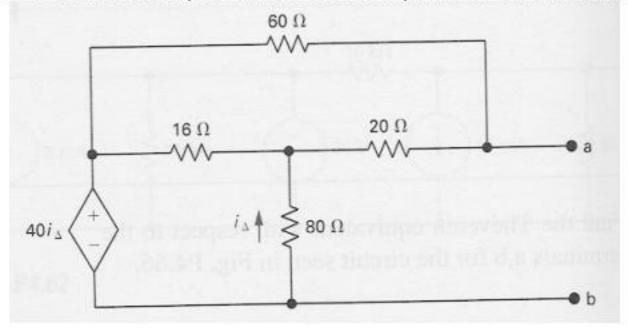
- the controlling voltage or current (must be calculated, in general)
- the relationship between the controlling voltage or current and the supplied voltage or current
- 3. the reference direction for the supplied voltage or current

The relationship between the dependent source and its reference cannot be broken!

 Dependent sources cannot be turned off for various purposes (e.g. to find the Thévenin resistance, or in analysis using Superposition).

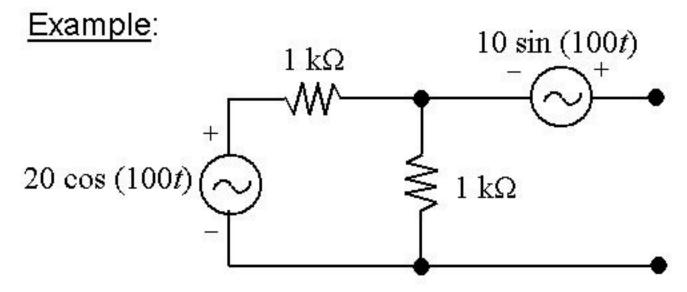
$R_{\rm Th}$ Calculation Example #2

Find the Thevenin equivalent with respect to the terminals a,b:



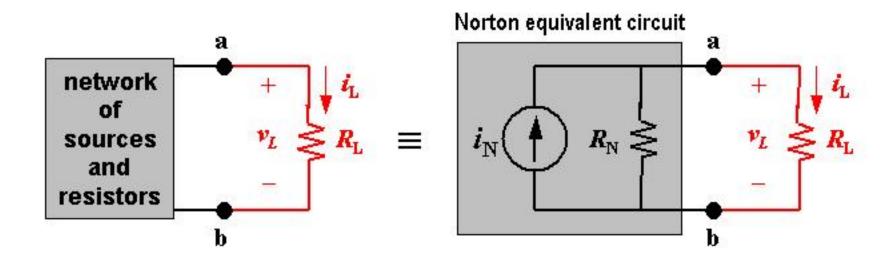
Networks Containing Time-Varying Sources

Care must be taken in summing time-varying sources!



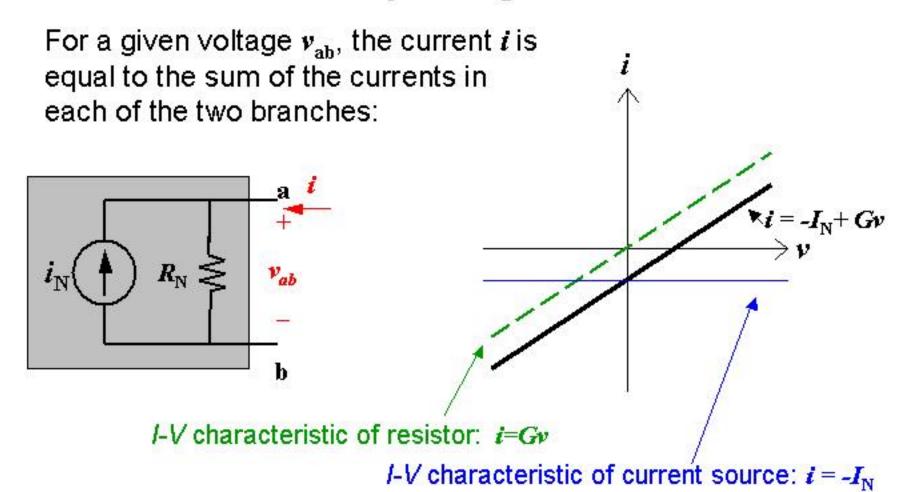
Norton Equivalent Circuit

 Any* linear 2-terminal (1-port) network of indep. voltage sources, indep. current sources, and linear resistors can be replaced by an equivalent circuit consisting of an independent current source in parallel with a resistor without affecting the operation of the rest of the circuit.



I-V Characteristic of Norton Equivalent

 The I-V characteristic for the parallel combination of elements is obtained by adding their currents:



Finding $I_{\rm N}$ and $R_{\rm N} = R_{\rm Th}$

Analogous to calculation of Thevenin Eq. Ckt:

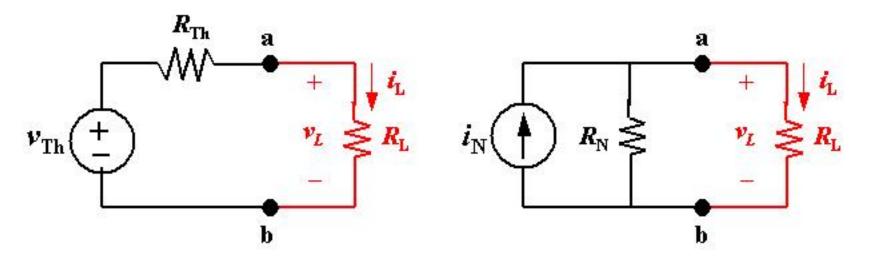
1) Find o.c voltage and s.c. current

$$I_{\rm N} \equiv i_{\rm sc} = V_{\rm Th}/R_{\rm Th}$$

2) Or, find s.c. current and Norton (Thev) resistance

Finding $I_{\rm N}$ and $R_{\rm N}$

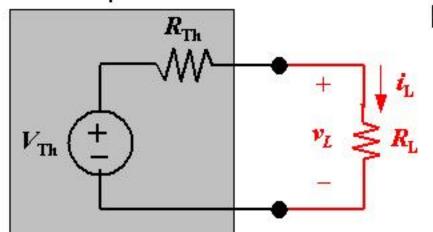
 We can derive the Norton equivalent circuit from a Thévenin equivalent circuit simply by making a source transformation:



$$R_{ ext{N}}=R_{ ext{Th}}=rac{oldsymbol{v}_{ ext{oc}}}{oldsymbol{i}_{ ext{sc}}};~~oldsymbol{i}_{ ext{N}}=rac{oldsymbol{v}_{ ext{Th}}}{R_{ ext{Th}}}=oldsymbol{i}_{ ext{sc}}$$

Maximum Power Transfer Theorem

Thévenin equivalent circuit



Power absorbed by load resistor:

$$p = i_{\mathrm{L}}^{2} R_{\mathrm{L}} = \left(\frac{V_{\mathrm{Th}}}{R_{\mathrm{Th}} + R_{\mathrm{L}}}\right)^{2} R_{\mathrm{L}}$$

To find the value of $R_{\rm L}$ for which p is maximum, set $\frac{dp}{dR_{\rm r}}$ to 0:

$$\frac{dp}{dR_{\rm L}} = V_{\rm Th}^2 \left[\frac{\left(R_{\rm Th} + R_{\rm L} \right)^2 - R_{\rm L} \times 2 \left(R_{\rm Th} + R_{\rm L} \right)}{\left(R_{\rm Th} + R_{\rm L} \right)^4} \right] = 0$$

$$\Rightarrow (R_{\rm Th} + R_{\rm L})^2 - R_{\rm L} \times 2(R_{\rm Th} + R_{\rm L}) = 0$$

$$\Longrightarrow$$
 $R_{\mathrm{Th}}=R_{\mathrm{L}}$

A resistive load receives maximum power from a circuit if the load resistance equals the Thévenin resistance of the circuit.