

## EE 40 and EE 42/43/100 Class Schedule Spring 2007

	Monday	Wednesday	Friday	HW Posted	HW Due	HW Back	HW material up to	EE 40 Lab	EE 100/42/43 Lab	Subjects	Material useful for Other EE Courses
<b>Week 1</b>		<b>1/17</b> Ch 1	<b>1/19</b> Ch 1	HW1			lec.2 (1/19)	No Lab	No Lab	Definition current, voltage, KCL and KVL	Basic Circuits; useful for all
<b>Week 2</b>	<b>1/22</b> Ch 2	<b>1/24</b> Ch 2	<b>1/26</b> Ch 2	HW2	HW1		lec. 5 (1/26)	1.Introduction	1.Introduction	resistor in parallel, series, node analysis, mesh analysis, dependent sources	Basic Circuits; useful for all
<b>Week 3</b>	<b>1/29</b> Ch 2	<b>1/31</b> Ch 3	<b>2/2</b> Ch 4	HW3	HW2	HW1	lec. 8 (2/2)	2. Scope	2. Scope	superposition, equivalent circuits, power and energy; capacitor and inductor	Basic Circuits; useful for all
<b>Week 4</b>	<b>2/5</b> Ch 4	<b>2/7</b> Ch 4	<b>2/9</b> Ch 4	HW4	HW3	HW2	lec. 11 (2/9)	3.Equivalent Circuits	3.Equivalent Circuits	transient response for first order circuits	Basic Circuits; useful for all
<b>Week 5</b>	<b>2/12</b> Ch 4	<b>2/14</b> Ch 4	<b>2/16</b> Ch 5		HW4	HW3		4.RC Circuits	4.RC Circuits	second order circuits; forced response; natural response	117
<b>Week 6</b>	<b>2/19</b> Holiday	<b>2/21</b> Midterm 1 (till 2/14)	<b>2/23</b> Ch 5	HW5			lec. 17 (2/23)	No Lab	No Lab	second order circuits;review of complex numbers	117
<b>Week 7</b>	<b>2/26</b> Ch 5	<b>2/28</b> Ch 6	<b>3/2</b> Ch 6	HW6	HW5	HW4	lec. 20 (3/2)	No Lab	No Lab	phasors; circuit analysis with complex impedance; dB, Bode Plots	105, 140
<b>Week 8</b>	<b>3/5</b> Ch 6	<b>3/7</b> Ch 6	<b>3/9</b> Ch 6	HW7	HW6	HW5	lec. 23 (3/9)	5.RC Filters	5.RC Filters	Bode plots; 1st and 2nd order filter	140, 117
<b>Week 9</b>	<b>3/12</b> Ch 6	<b>3/14</b> Ch 14	<b>3/16</b> Ch 14	HW8	HW7	HW6	lec. 26 (3/16)	6. Op-Amps	6. Op-Amps	Bode plots; Op Amps	140
<b>Week 10</b>	<b>3/19</b> Ch 14	<b>3/21</b> Ch 10	<b>3/23</b> Ch 10	HW9	HW8	HW7	lec. 29 (3/23)	7. Diodes	7. Diodes	Op Amp ; Diode circuits	140
<b>Recess</b>	<b>3/26</b> Holiday	<b>3/28</b> Holiday	<b>3/30</b> Holiday					No Lab	No Lab	Spring Recess	130, 105
<b>Week 11</b>	<b>4/2</b> Ch 10	<b>4/4</b> Ch 10	<b>4/6</b> Device Phys		HW9			8. Project 1	8. Project 1	Diodes circuits	130, 105
<b>Week 12</b>	<b>4/9</b> Device Phys	<b>4/11</b> Midterm 2 (till Ch 10)	<b>4/13</b> Device Phys	HW10		HW9	lec. 35 (4/13)	No Lab	No Lab	Semiconductor Physics	130, 105
<b>Week 13</b>	<b>4/16</b> Device Phys	<b>4/18</b> Ch12	<b>4/20</b> Ch 12	HW 11	HW10		lec. 38 (4/20)	9. Project 2	9. Project 2	Semiconductor Physics; FET, Q-point	105, 141
<b>Week 14</b>	<b>4/23</b> Ch12	<b>4/25</b> Ch12	<b>4/27</b> Ch 12	HW 12	HW11	HW10	lec. 41 (4/27)	10. Project 3	10. Circuit Simulation	Small signal model; amplifiers	105, 141
<b>Week 15</b>	<b>4/30</b> Ch 12	<b>5/2</b> Ch 12	<b>5/4</b> Ch 7		HW 12	HW11		Final Competition (5/4 3pm)	11. Digital Logic	Small signal model; inverters; logic circuits	141
<b>Week 16</b>	<b>5/7</b> Ch 7	<b>5/8 Last Day</b>				HW 12				inverters; logic circuits	141
<b>Week 17</b>	<b>Final Exam 12:30-3:30 pm on Thursday 5/17/07 (Exam Group 17)</b>										