

Working Syllabus - Version 10/21/01

Lecture	Date	Chapter/Sections	Topics
1	8/27/01	1.1-1.2	Overview, Signals, Analog and Digital
2	8/29/01	1.3-1.4	Electrical Quantities,
3	9/5/01	2.1-2.2	Kirchhoff Laws
4	9/10/01	5.1; 2.2; 3.1	Circuit Elements and I versus V Graphs
5	9/12/01	3.2-3.4	Power and Energy
6	9/17/01	8.1; Handout	RC Transient Step Response
7	9/19/01		Switching Examples; RC and R/L Time Constants
8	9/24/01	53-58; 2.5- 2.6	Nodal Analysis
9	9/26/01	11.1,11.2 393-402	Quiz; Basic Digital Blocks
10	10/1/10	11.2, 11.3 403-422	Logic Implementation and Synthesis
	10/3/01	(reading through Lec. 8)	MIDTERM EXAM
11	10/8/01	11.3, 11.4 403-422	Physical Limits of Logic Circuits
12	10/10/01	4.1, 4.3 (Ideal)	Dependent Sources and Ideal Op-Amps
13	10/15/01	4.1, 4.2	Circuit Analysis with Dependent Sources
14	10/17/01	Handout	Comparators and Implementation with Op-Amps
15	10/22/01	593-595, 604-606	Logic with State Dependent Device
16	10/24/01	607-611, Handout	Logic with Complementary devices
17	10/29/01	Handout	Logic - Switched Resistor Model
18	10/31/01	Handout	Quiz; Performance Switched Resistor
19	11/5/01	11.3; Handout	Memory and Shift Devices
	11/7/01	(reading through Lec.18)	MIDTERM EXAM
	11/12/01		HOLIDAY - VETERAN'S DAY
20	11/14/01		
21	11/19/01		
22	11/21/01		
23	11/26/01		

24	11/28/01		
25	12/3/01		
26	12/5/01		