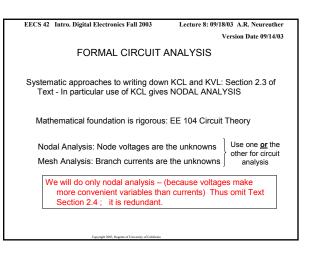
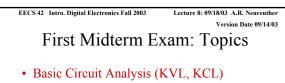
EECS 42 Intro. Digital Electronics Fall 2003 EECS 42 Introduction Digital Electronics Andrew R. Neureuther Lecture #8 Node Equations • Systematic Node Equations • Example: Voltage and Current Dividers • Example 5 Element Circuit Schwarz and Oldham 53-58, 2.5, & 2.6 Quiz 9/25 20 min: Basic Circuit Analysis and Basic Transient Midterm 10/2: Lectures # 1-9: 4 Topics – See slide 2

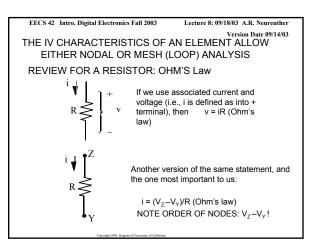
Length/Credit Review TBA http://inst.EECS.Berkeley.EDU/~ee42/





- Equivalent Circuits and Graphical Solutions for Nonlinear Loads
- Transients in Single Capacitor Circuits
- Node Analysis Technique and Checking Solutions

Exam is in class 9:40-10:45 AM, Closed book, Closed notes, Bring a calculator, Paper provided



EECS 42 Intro. Digital Electronics Fall 2003 Lecture 8: 09/18/03 A.R. Neureuther Version Date 09/14/03 Class Input on Midterm/Review

- Midterm Options
 - Option 1: 65 min and 23% of grade in course (Final is 49%)
 - Option 2: 80 min and 28% of grade in course (Final is 39%)
- · Review Options
 - Date Tu 9/30 or Wed 10/1
 - Time 5-6:30 PM or Other

