Lecture 9: 09/23/03 A.R. Neureuther

Version Date 09/14/03

EECS 42 Introduction to Digital Electronics Andrew R. Neureuther

Lecture #9 Prof. King: Node Equations - Advanced

- Supernode for voltage supplies
- Checking Solutions Schwarz and Oldham 53-58, 2.5 and 2.6 Quiz 9/25 20 min:

Basic Circuit Analysis and Basic Transient
Midterm 10/2: Lectures # 1-9: ForTopics – See slide 2

Length/Credit Review TBA

http://inst.EECS.Berkeley.EDU/~ee42/

Copyright 2003, Regents of University of California

EECS 42 Intro. Digital Electronics Fall 2003

Lecture 9: 09/23/03 A.R. Neureuther

Version Date 09/14/03

First Midterm Exam: Topics

- Basic Circuit Analysis (KVL, KCL)
- Equivalent Circuits and Graphical Solutions for Nonlinear Loads
- Transients in Single Capacitor Circuits
- Node Analysis Technique and Checking Solutions

Exam is in class 9:40-10:45 AM, Closed book, Closed notes, Bring a calculator, Paper provided

Lecture 9: 09/23/03 A.R. Neureuther

ReCan

Version Date 09/14/03

FORMAL CIRCUIT ANALYSIS USING KCL: NODAL ANALYSIS

(Memorize these steps and apply them rigorously!)

- 1 Choose a Reference Node $\stackrel{\bot}{=}$
- 2 Define unknown node voltages (those not fixed by voltage sources)
- 3 Write KCL at each unknown node, expressing current in terms of the node voltages (using the constitutive relationships of branch elements*)
- 4 Solve the set of equations (N equations for N unknown node voltages)

Copyright 2003, Regents of University of California

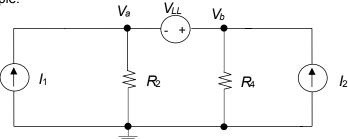
EECS 42 Intro. Digital Electronics Fall 2003

Lecture 9: 09/23/03 A.R. Neureuther

Version Date 09/14/03

NODAL ANALYSIS WITH "FLOATING" VOLTAGE SOURCES

A "floating" voltage source is a voltage source for which neither side is connected to the reference node. V_{LL} in the circuit below is an example.



What is the problem? \to We cannot write KCL at node a or b because there is no way to express the current through the voltage source in terms of

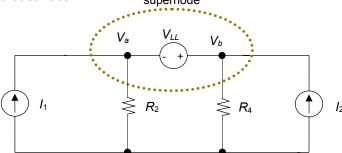
Solution: Define a "supernode" – that chunk of the circuit containing nodes a and b. Express KCL at this supernode.

^{*} With inductors or floating voltages we will use a modified Step 3: The Supernode Method – see slide 10

Lecture 9: 09/23/03 A.R. Neureuther

FLOATING VOLTAGE SOURCES (cont.) Version Date 09/14/03

Use a Gaussian surface to enclose the floating voltage source; write KCL for that surface supernode



We have two unknowns: V_a and V_b.

We obtain one equation from KCL at supernode: $I_1 - \frac{V_a}{R_2} - \frac{V_b}{R_4} + I_2 = 0$

We obtain a second "auxiliary" equation from the property of the voltage source: $V_{LL} = V_b - V_a$ (often called the "constraint")

⇒ 2 Equations & 2 Unknowns

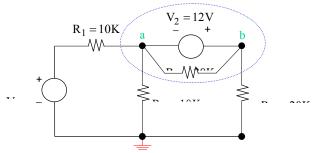
Copyright 2003, Regents of University of California

EECS 42 Intro. Digital Electronics Fall 2003

Lecture 9: 09/23/03 A.R. Neureuther

ANOTHER EXAMPLE

Version Date 09/14/03



- 1 Choose reference node (can it be chosen to avoid floating voltage source?)
- 2 Label unknowns V_a and V_b
- 3 Equation at supernode: $\frac{V_1 V_a}{V_a} = \frac{V_b}{V_a} + \frac{V_a}{R_1} + \frac{1}{R_2} + \frac{V_b}{R_4} = \frac{V_1}{R_1}$ 4 Auxiliary equation: $V_a = \frac{V_b}{V_a} + \frac{V_b}{R_1} + \frac{V_b}{R_2} + \frac{V_b}{R_2} = \frac{V_1}{R_1}$

Solve: $V_a(\frac{R_4}{R_1} + \frac{R_4}{R_2} + 1) = V_1 \frac{R_4}{R_1} - V_2$

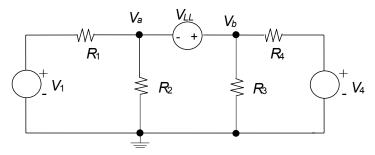
 $\textbf{SOLUTION}:\ V_a=0$

Lecture 9: 09/23/03 A.R. Neureuther

Version Date 09/14/03

NODAL ANALYSIS COMPLETE EXAMPLE

Find V_a , V_b if R_1 = R_2 = R_3 = R_4 = 1M Ω , and V_1 = V_4 =1.5V with V_{LL} = 1V



Solution: At supernode enclosing nodes a and b:

$$(V_1 - V_a)/R_1 - V_a/R_2 = V_b/R_3 + (V_b - V_4)/R_4$$
 and

 $V_b = V_a + V_{LL}$ Thus: $V_a = 0.25$ Be sure to check $V_b = 1.25$ answer with KCL!

Copyright 2003, Regents of University of California

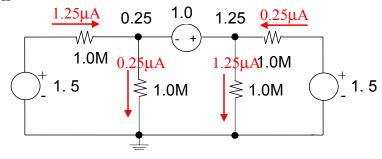
EECS 42 Intro. Digital Electronics Fall 2003

Lecture 9: 09/23/03 A.R. Neureuther

Version Date 09/14/03

ANSWER CHECKING TECHNIQUE: USE KCL

Is V_a= 1.25 and V_b = 0.25 if R₁= R₂ = R₃ = R₄ = 1M Ω , and V₁ = V₄ =1.5V with V_{LL} = 1V ????



KCL at the Supernode:

Clearly the current into the supernode is zero and we have verified that the solution is correct. :

Lecture 9: 09/23/03 A.R. Neureuther EECS 42 Intro. Digital Electronics Fall 2003 Version Date 09/14/03 Review for Quiz 9/25 **GRAPHICAL EQUIVALENT CIRCUIT** Short Circuit : $V_{OUT} = 0$ $I_{SS} = 1 \text{ mA}$ $R_1 = 1k\Omega$ $I_{IN} = -I_{SS} = -1 \text{ mA}$ $R2 = 6 k \Omega$ Open Circuit: $I_{IN} = 0$ $R3 = 3 k\Omega$ Note $R2||R3 = 2 k\Omega$ $\mathbf{V}_{\text{OUT}} = \mathbf{I}_{\text{SS}} \times \mathbf{R2} || \mathbf{R3}$ **Combined Circuit** $= 1 \text{ mA} \times 2 \text{k}\Omega = 2 \text{V}$ **Third Point** 4 $I_{IN} = 1 \text{ mA}$ $\mathbf{KCL} \Longrightarrow \mathbf{I}_{2\parallel 3} = \mathbf{I}_{SS} + \mathbf{I}_{IN}$ $\mathbf{V}_{\text{OUT}} = \mathbf{I}_{2||3} \times \mathbf{R}_{2}||\mathbf{R}_{3}$ $= 2 \mathbf{m} \mathbf{A} \times 2\mathbf{k} \mathbf{\Omega} = 4\mathbf{V}$ 2 Key: It will always be the V_{OUT} (Volt) case that for linear circuit elements the I vs. V is a straight line. Copyright 2003, Regents of University of California

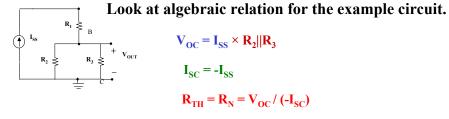
EECS 42 Intro. Digital Electronics Fall 2003

Lecture 9: 09/23/03 A.R. Neureuther

Review for Quiz 9/25

Version Date 09/14/03

$R_{TH} = R_N$ SHORTCUT METHODS

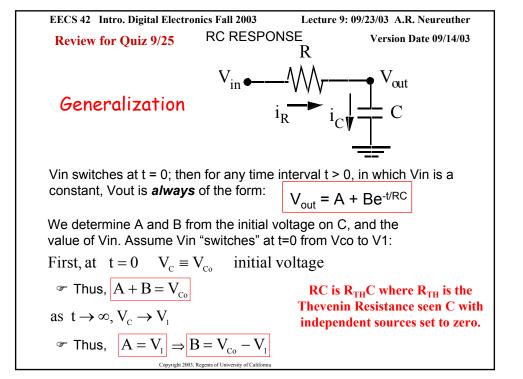


$$\mathbf{R}_{\text{TH}} = \mathbf{R}_{\text{N}} = (\mathbf{I}_{\text{SS}} \times \mathbf{R}_2 || \mathbf{R}_3) / (-(-\mathbf{I}_{\text{SS}})) = \mathbf{R}_2 || \mathbf{R}_3$$

In General turn all of the independent sources to zero and find the remaining equivalent resistance seen looking into the terminals.

Currents sources are turned to zero current (with any voltage) => OPEN CIRCUIT. Voltage sources are turned to zero voltage (with any current)=> SHORT CIRCUIT.

EECS 42 Intro. Digital Electronics Fall 2003 Lecture 9: 09/23/03 A.R. Neureuther Version Date 09/14/03 Review for Quiz 9/25 **EXAMPLE 3 Thevenin/Norton** Find the Thévenin and Norton equivalents of: Find $V_{AB} = V_{OC}$ from voltage divider. Left to right: (4 V rise across 25K + 75K) ⇒ 25 KΩ \gtrsim 75 KΩ 3 V across 75K, 1 V across 25k. So, $V_{AB} = -3 + 6 = 3V = V_{OC}$ $I_{SC} = -6V/75K+2V/25K = -0.16 \text{ mA}$ equivalent to and equivalent to 18.8K A В Thévenin В Norton Copyright 2003, Regents of University of California



Lecture 9: 09/23/03 A.R. Neureuther

Review for Quiz 9/25

Version Date 09/14/03

Re-Cap: Charging and discharging in RC Circuits

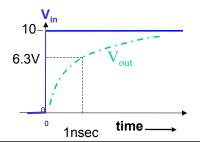
Last Time:

We learned that simple the simple RC circuit with a step input has a universal exponential solution of the form:

 $V_{out} = A + Be^{-t/RC}$

Example 0: R = 1K, C = 1pF, V_{in} steps from zero to 10V at t=0:

- 1) Initial value of V_{out} is 0
- 2) Final value of V_{out} is 10V
- 3) Time constant is RC = 10⁻⁹ sec
- 4) V_{out} reaches 0.63 X 10 in 10⁻⁹ sec



Copyright 2003, Regents of University of California

EECS 42 Intro. Digital Electronics Fall 2003

Lecture 9: 09/23/03 A.R. Neureuther

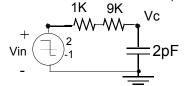
Version Date 09/14/03

Review for Quiz 9/25

Charging and discharging in RC Circuits

Find Vc(t) for the following circuit: (input switches from 2V to -1V at t = 0)

We have : Initial value of Vc is 2V, final value is -1V and τ = 20nsec



5) Sketch Vc (t):

37% of transient remaining at one time constant

What is the equation for an exponential beginning at 2V, decaying to -1V, with τ = 20nsec?

$$V_c(t) = -1 + 3e^{-t/20 \text{nsec}}$$

 $V_{\text{FINAL}} =$

 $V_{INITIAL} =$

 $\mathbf{B} = \mathbf{V}_{\mathbf{INTIAL}} - \mathbf{V}_{\mathbf{FINAL}} =$