Print your name: $\qquad$ ,
(last)
(first)

Print your student ID: $\qquad$

You have 110 minutes. There are 7 questions of varying credit (100 points total).

| Question: | 1 | 2 | 3 | 4 | 5 | 6 | 7 | Total |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Points: | 15 | 24 | 15 | 25 | 12 | 8 | 1 | 100 |

For questions with circular bubbles, you may select only one choice.
O Unselected option (completely unfilled)

- Only one selected option (completely filled)

For questions with square checkboxes, you may select one or more choices.
$\square$ You can select
$\square$ multiple squares
$\square$ (completely filled)
Anything you write that you cross out will not be graded. Anything you write outside the answer boxes will not be graded. If you write multiple answers or your answer is ambiguous, we will grade the worst interpretation. For coding questions, you may write at most one statement and you may not use more blanks than provided.

If an answer requires hex input, make sure you only use capitalized letters! For example, 0xDEADBEEF instead of $0 x d e a d b e e f$. Please include hex ( 0 x ) or binary ( 0 b ) prefixes in your answers unless otherwise specified. For all other bases, do not add any prefixes or suffixes.

## Read the following honor code and sign your name.

I understand that I may not collaborate with anyone else on this exam, or cheat in any way. I am aware of the Berkeley Campus Code of Student Conduct and acknowledge that academic misconduct will be reported to the Center for Student Conduct and may further result in, at minimum, negative points on the exam.

Sign your name: $\qquad$

Q1.1 (1 point) An $n$-bit number in bias notation and an $n$-bit two's complement number always represent the same range of numbers.
$\bigcirc$ True
○ False

Q1.2 (1 point) An $n$-bit two's complement number can represent exactly twice the number of unique values as an $n$-bit unsigned number.

O True
O False
Q1.3 (1 point) For any number representation system, you must have at least $2 n+1$ bits to represent the value $2^{2 n}$.
○ True
○ False

Q1.4 (1.5 points) Convert 0xA7 to decimal, assuming the data was stored as a two's complement onebyte integer.


Q1.5 (1 point) The output of the compiler never contains pseudoinstructions.
O True
〇 False

Q1.6 (1 point) The loader produces an executable.
○ True
O False

For Q1.7 and Q1.8, consider the following code snippet and assume that ints are 4 bytes.

```
int x = 257;
int y = strlen((char *) &x);
```

Q1.7 (1.5 points) In a little-endian system, what will y contain?


Q1.8 (1.5 points) In a big-endian system, what will y contain?
$\square$

Q1.9 (2 points) Write a Boolean expression that simplifies the Boolean expression below. You may use at most 3 Boolean operations. $\sim(N O T)$, I (OR), \& (AND) each count as one operation. We will assume standard C operator precedence, so use parentheses when uncertain.
$\sim(A \mid \sim C) \mid((\sim A \& B) \mid B)$


Q1.10 (2 points) Translate the following RISC-V instruction to its hexadecimal counterpart.
slli a0 t2 4

0x

Q1.11 (1.5 points) Represent $1.5 \times 2^{-511}$ in hex using a binary floating point representation, which follows IEEE-754 standard conventions, but has 10 exponent bits (and a standard bias of -511 ) and 21 mantissa bits.

```
0x
```

```
#define MAX_BORROWS 25
typedef struct {
    char* book_name;
    bool borrowed;
} Book;
typedef struct {
    char* user_id;
    Book* borrowed_books[MAX_BORROWS];
} User;
13 typedef struct {
14 User* users;
15 int users_len;
16 Book* books;
17 int books_len;
18 } Library;
```

2
7
12

The city of Eddy B.C wants to build a new library! The init_users function receives the following input:

- Library* lib: A pointer to an uninitialized Library struct. You may assume that memory has already been correctly allocated on the heap for the Library struct.
- char** user_ids: An array of well-formatted strings of nonzero length except the last element. The last element is NULL. You may assume that all strings are allocated on the stack.

The function should make sure the following properties are held:

- users_len should be set to the number of strings in user_ids.
- Each User in users should be initialized as follows:
- The user_id of the ith User in users should be set to the ith string in user_ids.
- borrowed_books should be an array of NULLs to indicate that no Book has been borrowed.
- Every User and its contents must persist through function calls.

Useful C function prototypes:

```
void* malloc(size_t size);
void free(void *ptr);
void* calloc(size_t num_elements, size_t size);
void* realloc(void *ptr, size_t size);
size_t strlen(char* s);
char* strcpy(char* dest, char* src);
// memset sets the first num bytes of the block of memory pointed to by ptr
// to the specified value (interpreted as an unsigned char).
void* memset(void* ptr, int value, size_t num);
```

(15 points) Fill in init_users so that it matches the described behavior. Assume that all necessary C libraries are included.


The remove_users function takes in one argument:

- Library* lib: A pointer to a Library struct where the contents have been allocated on the heap.

The remove_users function should free every User and any additional memory that might have been allocated for the User.

```
1 void remove_users(Library* lib) {
2 for (int i = 0; i < lib->users_len; i++) {
3 free(lib->users[i]);
4 }
5 }
```

Q2.13 (3 points) Is the remove_users function implemented correctly? If "Yes", no justification is required. If "No", please explain in two sentences or fewer.
O Yes
O No
$\square$

For each of the following symbols, choose which section of memory it would live in.
Q2.14 (1 point) MAX_BORROWS
○ Stack
O Heap
O Data/Static
O Code

Q2.15 (1 point) init_users
○
StackHeap
○ Data/Static
O Code

Q2.16 (1 point) lib, the parameter for init_users
$\bigcirc$
StackHeap
○ Data/Static
O Code

Thanton remembered from class that right-shifting a binary number is the same as dividing it by two (rounding down). They did get a little too eager, though, and have tried applying the concept to arbitrary numbers including floating point numbers! They wrote a function, mystery, to logically right shift the binary representation of the input value by 1 and return the result as a floating point value.
For this question, assume that we're working with standard IEEE-754 single-precision floating point numbers.

For Q3.1 to Q3.4, what would each of the following function calls return?
Q3.1 (2 points) mystery(0)


Q3.2 (2 points) mystery (-0)


Q3.3 (2 points) mystery ( $\infty$ )
$\square$
Q3.4 (2 points) mystery ( $-\infty$ )


For Q3.5 and Q3.6, "normal numbers" are numbers that are not denormalized, zero, NaN, or infinity.
Q3.5 (3.5 points) Suppose we run mystery on every possible NaN value. What are all of the possible return values? Select all that apply.

| $\square$ Denormalized numbers | $\square$ NaN | $\square$ Normal numbers |
| :--- | :--- | :--- |
| $\square$ Zero | $\square$ Infinity | $\square$ None of the above |

Q3.6 (3.5 points) Suppose we run mystery on every possible denormalized value. What are all of the possible return values? Select all that apply.

| $\square$ Denormalized numbers | $\square$ NaN | $\square$ Normal numbers |
| :--- | :--- | :--- |
| $\square$ Zero | $\square$ Infinity | $\square$ None of the above |

A wondrous sequence of positive integers is defined as follows: If $n$ is even, then the next number is $\frac{n}{2}$. Otherwise, the next number is $3 n+1$.

Q4.1 (2 points) We want to create a pseudoinstruction to check whether a number is odd or not. This instruction, written is_odd rd rs1, will put the value 1 in rd if the value in rs1 is odd, and the value 0 otherwise. What is the RISC-V instruction that is_odd rd rs1 would translate to? You may only use one instruction, and you may not use any pseudoinstructions.
Note: Your solution may include rd and rs1.


Using is_odd, write a function (that follows calling convention) to compute the next wondrous number and return it in a 0 , given the current wondrous number as input in a0. Your code may not use mul or any t registers.


Using next_number, write a function (that follows calling convention) to count the number of steps until a wondrous sequence reaches the number 1 and return the number of steps in a 0 , given a starting number as input in a0.

You may assume that a0 will be a positive integer. You may not use any additional s registers beyond those provided in the skeleton code.


## Q5 Fully Secure Machine

(12 points)
A garage door opens if it ever sees the password 011 in a transmission. More formally, this FSM takes a bitstring consisting of 0 's and 1 's as its input, and continually outputs 0 's until it sees the substring 011, after which it outputs 1's continuously. Example executions of this FSM are below:

Input: 010101001100001010101 Input: 000000000001000000010
Output: 00000000011111111111
Output: 000000000000000000000


For each of the numbered arrows, mark the correct FSM state transition.
Q5.1 (1.5 points) Arrow 1
○ 0/0
○ 0/1
○ $1 / 0$
○ $1 / 1$

Q5.2 (1.5 points) Arrow 2
○ 0/0
○ 0/1
○ $1 / 0$
○ $1 / 1$

Q5.3 (1.5 points) Arrow 3
○ 0/0
○ 0/1
○ $1 / 0$
○ $1 / 1$

Q5.4 (1.5 points) Arrow 4
○ 0/0
○ 0/1
○ $1 / 0$
○ $1 / 1$

Q5.5 (1.5 points) Arrow 5
○ $0 / 0$
○ $0 / 1$
○ $1 / 0$
○ $1 / 1$

Q5.6 (1.5 points) Arrow 6
○ 0/0
○ 0/1
○ $1 / 0$
○ $1 / 1$

Q5.7 (1.5 points) Arrow 7
○ 0/0
○ $0 / 1$
○ $1 / 0$
○ $1 / 1$

Q5.8 (1.5 points) Arrow 8
○ 0/0
○ 0/1
○ $1 / 0$
○ $1 / 1$

Consider the following circuit diagram and component delays:


Q6.1 (2 points) What is the smallest combinational delay of all paths in this circuit, in picoseconds?

| ps |
| ---: |

Q6.2 (2 points) What is the minimum allowable clock period for this circuit to function properly, in picoseconds?


Q6.3 (2 points) What is the maximum hold time the registers can have so that there are no hold time violations in the circuit above?
$\square$
Q6.4 (2 points) Suppose this circuit only deals with two's complement integers. Currently, the subtractor component has a delay of 35ps. What is the maximum delay an adder component can have such that we could replace the subtractor component with adders, NOT gates, and constants to achieve the same delay as the subtractor while maintaining the same behavior? You may assume that constants have no delay.
As a reminder, the subtract component does the following operation:
output $=$ top input - bottom input

## Q7 The Finish Line

Everyone will receive credit for this question, even if you leave it blank.
Q7.1 (1 point) Where are the RISC and IEEE-754 plaques in Soda Hall?
O 1st Floor
O 2nd Floor3rd Floor
O
4th Floor
O 5th Floor6th Floor
○ 7th Floor

Q7.2 (0 points) Is there anything you want us to know?

