EE 122 Housekeeping

- □ Welcome back. This is 122.
- □ Schedule has been changed. Check website.
- □ Project phase 2 due on Apr 11
- □ Our journey through the layers:
 - Application
 - Transport
 - Network
 - o Link
 - Physical (Wireless)

DataLink Layer

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The Data Link Layer

Our goals:

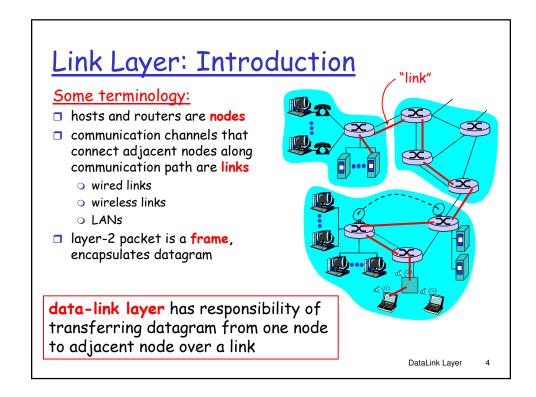
- understand principles behind data link layer services:
 - o error detection, correction
 - o sharing a common medium: multiple access
 - link layer addressing
- discussion of various link layer technologies, particularly the Ethernet
- 802.11 Wi-Fi and other wireless technologies will be discussed in greater detail later.

DataLink Layer

Link Layer

- Introduction and services
- Error detection and correction
- Multiple access protocols
- □ Link-Layer Addressing
- Ethernet

DataLink Layer



Link layer: context

- Datagram transferred by different link protocols over different links:
 - e.g., Ethernet on first link, frame relay on intermediate links, 802.11 on last link
- Each link protocol provides different services
 - o e.g., may or may not provide rdt over link

DataLink Layer

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Link Layer Services

- □ Framing, link access:
 - o encapsulate datagram into frame, adding header, trailer
 - o channel access if shared medium
 - "MAC" addresses used in frame headers to identify source, dest
 - different from IP address!
- Reliable delivery between adjacent nodes
 - we learned how to do this already (chapter 3)!
 - seldom used on low bit error link (fiber, some twisted pair)
 - o wireless links: high error rates
 - · Q: why both link-level and end-end reliability?

DataLink Layer

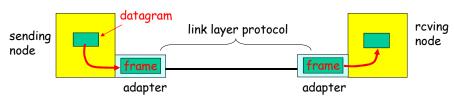
<u>Link Layer Services (more)</u>

Error Detection and Correction.

- o errors caused by signal attenuation, noise.
- receiver detects presence of errors:
 - · signals sender for retransmission or drops frame
- o receiver identifies and corrects bit error(s) without resorting to retransmission
- Half-duplex and full-duplex
 - o with half duplex, nodes at both ends of link can transmit, but not at same time

DataLink Layer





- □ link layer implemented in □ receiving side "adaptor" (aka NIC)
 - Ethernet card, PCMCI card, 802.11 card
- sending side:
 - o encapsulates datagram in a frame
 - adds error checking bits, rdt, flow control, etc.

- - o looks for errors, rdt, flow control, etc
 - o extracts datagram, passes to raving node

DataLink Layer

Link Layer

- □ Introduction and services
- Error detection and correction
- Multiple access protocols
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DataLink Layer

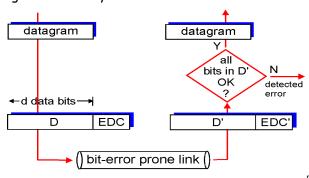
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Error Detection and Correction

EDC= Error Detection and Correction bits (redundancy)

D = Data protected by error checking, may include header fields

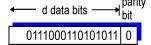
- Error detection not 100% reliable!
 - protocol may miss some errors, but rarely
 - · larger EDC field yields better detection and correction



DataLink Layer

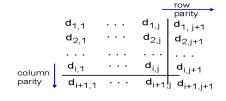
Parity Checking

Single Bit Parity: Detect how many errors?



Two Dimensional Bit Parity:

Detect how many bit errors? Correct how many errors?



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General Parity-Check Codes

- A general parity check code adds k parity checks to n data bits
- □ Data rate = n/(n+k) information bits per code bits
- Minimum Hamming distance d is the minimum number of bit flips to get from one possible transmitted sequence (codeword) to another.
- Can detect if there are less than d errors.
- Can correct if there are less than d/2 errors.
- Performance under burst errors also important

DataLink Layer

Another example: checksum

<u>Goal:</u> detect "errors" (e.g., flipped bits) in transmitted segment (note: used at transport layer *only*)

Sender:

- treat segment contents as sequence of 16-bit integers
- checksum: addition (1's complement sum) of segment contents
- sender puts checksum value into UDP checksum field

Receiver:

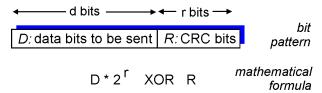
- compute checksum of received segment
- check if computed checksum equals checksum field value:
 - NO error detected
 - YES no error detected. But maybe errors nonetheless?
 More later

DataLink Layer

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Cyclic Redundancy Check (CRC)

- view data bits, D, as a binary number
- choose r+1 bit pattern (generator), G (leftmost and rightmost bits are both 1)
- goal: choose r CRC bits, R, such that
 - <D,R> exactly divisible by G (modulo 2)
 - receiver knows G, divides <D,R> by G. If non-zero remainder: error detected!
 - o can detect how many burst errors?
- widely used in link layer technologies



DataLink Layer

CRC Example

Want:

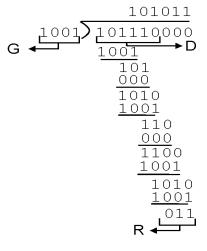
 $D \cdot 2^r XOR R = nG$ equivalently:

 $D.2^r = nG XOR R$

equivalently:

if we divide $D \cdot 2^r$ by G, want remainder R

R = remainder
$$\left[\frac{D \cdot 2^r}{G}\right]$$



DataLink Layer