Cyclic Redundancy Check (CRC)

 \Box view data bits, $d_1d_2...,d_n$, as a polynomial:

$$A(x) = \sum_{i=0}^{n-1} d_i x^i.$$

 choose r+1 bit pattern (generator), G (leftmost and rightmost bits are both 1), viewed again as polynomial

$$G(x) = \sum_{i=0}^{r} g_i x^i.$$

□ choose r CRC bits, R, such that

$$A(x)x^{r} + R(x) = G(x)H(x)$$

for some polynomial H(x). Here, addition of the polynomial coefficients is modulo 2 arithmetic.

In other words, the polynomial represented by the concatenation of the data bits and the CRC bits is divisible aver by G(x).



















Example: GSM

- Global System for Mobile (GSM): digital cellular standard developed in Europe.
- 25MHz band divided in 200 kHz subchannels, further divided into time-slots.



11









