## EECS 16A Designing Information Devices and Systems I

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## 1. Superposition


(a) For the circuit above, first calculate $V_{\text {out }}$ with only $V_{s}$ on?
(b) Now calculate $V_{\text {out }}$ with only $V_{1}$ on. Repeat this with only $V_{2}$ on.
(c) Let's now turn on $V_{s}, V_{1}$ and $V_{2}$. What is the output $V_{\text {out }}$ ? What does this circuit do to arbitrary input voltages?

## 2. Derive Series and Parallel Caps!

Derive $C_{e f f}$ for the following diagrams.
(a)

(b)

(c)


## 3. Voltages across Capacitors

For the circuits given below, calculate the voltage across the capacitors at steady state. Also, calculate the charge and energy stored in each capacitor. Let $R_{1}=1 k \Omega, R_{2}=2 k \Omega, C_{1}=1 \mu F, C_{2}=3 \mu F, V_{s}=1 V$.
(a)

(b)

(c)


