



virtutech

# Simics/IA64-460GX Target Guide

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Drottningholmsv. 14, SE-112 42 STOCKHOLM, Sweden

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## Chapter 1

# About Simics Documentation

### 1.1 Conventions

Let us take a quick look at the conventions used throughout the Simics documentation. Scripts, screen dumps and code fragments are presented in a `monospace` font. In screen dumps, user input is always presented in bold font, as in:

```
Welcome to the Simics prompt
simics> this is something that you should type
```

Sometimes, artificial line breaks may be introduced to prevent the text from being too wide. When such a break occurs, it is indicated by a small arrow pointing down, showing that the interrupted text continues on the next line:

```
This is an artificial ␣
line break that shouldn't be there.
```

The directory where Simics is installed is referred to as `[simics]`, for example when mentioning the `[simics]/README` file. In the same way, the shortcut `[workspace]` is used to point at the user's workspace directory.

### 1.2 Simics Guides and Manuals

Simics comes with several guides and manuals, which will be briefly described here. All documentation can be found in `[simics]/doc` as Windows Help files (on Windows), HTML files (on Unix) and PDF files (on both platforms). The new Eclipse-based interface also includes Simics documentation in its own help system.

#### Simics Installation Guide for Unix and for Windows

These guides describe how to install Simics and provide a short description of an installed Simics package. They also cover the additional steps needed for certain features of Simics to work (connection to real network, building new Simics modules, ...).

## **Simics User Guide for Unix and for Windows**

These guides focus on getting a new user up to speed with Simics, providing information on Simics features such as debugging, profiling, networks, machine configuration and scripting.

## **Simics Eclipse User Guide**

This is an alternative User Guide describing Simics and its new Eclipse-based graphical user interface.

## **Simics Target Guides**

These guides provide more specific information on the different architectures simulated by Simics and the example machines that are provided. They explain how the machine configurations are built and how they can be changed, as well as how to install new operating systems. They also list potential limitations of the models.

## **Simics Programming Guide**

This guide explains how to extend Simics by creating new devices and new commands. It gives a broad overview of how to work with modules and how to develop new classes and objects that fit in the Simics environment. It is only available when the DML add-on package has been installed.

## **DML Tutorial**

This tutorial will give you a gentle and practical introduction to the Device Modeling Language (DML), guiding you through the creation of a simple device. It is only available when the DML add-on package has been installed.

## **DML Reference Manual**

This manual provides a complete reference of DML used for developing new devices with Simics. It is only available when the DML add-on package has been installed.

## **Simics Reference Manual**

This manual provides complete information on all commands, modules, classes and haps implemented by Simics as well as the functions and data types defined in the Simics API.

## **Simics Micro-Architectural Interface**

This guide describes the cycle-accurate extensions of Simics (Micro-Architecture Interface or MAI) and provides information on how to write your own processor timing models. It is only available when the DML add-on package has been installed.

**RELEASENOTES and LIMITATIONS files**

These files are located in Simics's main directory (i.e., `[simics]`). They list limitations, changes and improvements on a per-version basis. They are the best source of information on new functionalities and specific bug fixes.

**Simics Technical FAQ**

This document is available on the Virtutech website at <http://www.simics.net/support>. It answers many questions that come up regularly on the support forums.

**Simics Support Forum**

The Simics Support Forum is the main support tool for Simics. You can access it at <http://www.simics.net>.

**Other Interesting Documents**

Simics uses Python as its main script language. A Python tutorial is available at <http://www.python.org/doc/2.4/tut/tut.html>. The complete Python documentation is located at <http://www.python.org/doc/2.4/>.

## Chapter 2

# Simics/IA64-460GX Overview

### 2.1 Introduction

Simics/IA64-460GX models machines based on the Itanium processor (from the *Intel Itanium* processor family, also known as *IPF*). The machines are based on the Intel 460GX chipset, and may be configured with up to 32 processors. Only Linux is supported as target operating system.

### 2.2 Supported Hardware

The Simics/IA64-460GX machine is somewhat similar to the HP i2000 workstation, also known as “BigSur”). Unlike the HP i2000 workstation, the machine in Simics can be configured with more than two processors (as long as this is supported by the target operating system).

The Intel 460GX chipset supports up to 4-way multiprocessor configurations. The simulated model of this chipset, however, supports an arbitrary number of processors. In this case, Linux will support configurations with up to 32 processors.

The 460GX chipset can be divided into several components, and the simulated model supports a subset of these components (that will allow Linux to run unmodified). In particular, parts of the following components are simulated:

#### Chipset Components

System Address Controller	(SAC)	82461GX
I/O and Firmware Bridge	(IFB)	82468GX
Programmable Interrupt Device	(PID)	UPD66566S1

---

#### Note:

The chipset components included with Simics/IA64-460GX is only a small subset of the real 460GX chipset.

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## Chapter 3

# Simulated Machines

Simics scripts for starting IA64-460GX machines are located in the `[workspace]/targets/ia64-460gx/` directory, while the actual configuration scripts can be found in `[simics]/targets/ia64-460gx/`.

### 3.1 Vasa

Vasa is a multi-processor machine based on the 460GX chipset, with a single Itanium processor running at 75 MHz, and 1024 MB of memory. It has one SCSI disk and one SCSI CD-ROM, but no network device. The default configuration can be modified as described in section 3.2.

Vasa is configured for an existing Red Hat Linux 7.1 disk dump, that can be downloaded from the Virtutech web site. There is no *SimicsFS* support on this disk dump.

Additional information:

- Red Hat 7.1 Linux.
- Linux kernel 2.4.7
- Login `root`, no password.

#### 3.1.1 Vasa Scripts

##### **`vasa-common.simics`**

Starts the Vasa machine with the default configuration.

##### **`vasa-multi.simics`**

Example script with two Vasa machines in the same session.

### 3.2 Parameters for Machine Scripts

The following parameters can be set before running the `vasa-common.simics` script. Other `.simics` scripts may set some of the parameters unconditionally, and do not allow the user to override them.

### 3.2.1 vasa-common

**\$disk\_size**

Size of the primary hard disk. This parameter must match any disk images that are added to the primary disk.

**\$freq\_mhz**

The clock frequency in MHz for all processors.

**\$memory\_megs**

The total amount of system memory, in MB.

**\$num\_cpus**

The number of processors in the machine.

**\$rtc\_time**

Date and time of the real-time clock at boot.

**\$text\_console**

Set to “yes” in order to use a text console with the VGA device (by default, a graphical console will be opened).

## Chapter 4

# Supported Components

The following sections list components that are supported for the IA64-460GX architecture. There also exist other components in Simics, such as various PCI devices, that may work for IA64-460GX but that have not been tested.

The default machines are constructed from components in the `-system.include` files in `[simics]/targets/ia64-460gx/`. See the Configuration and Checkpointing chapter in the Simics User Guide for information on how to define your own machine, or make modifications to an existing machine.

### 4.1 ia64 Components

#### 4.1.1 ia64-460gx-system

##### Description

The “ia64-460gx-system” component represents a system based on the Itanium processor with the 460GX chipset

##### Attributes

###### *memory\_megs*

**Required** attribute; **read/write** access; type: **Integer**.

The amount of RAM in mega-bytes in the machine.

###### *processor\_list*

**Optional** attribute; **read/write** access; type: `[o|n{32}]`.

Processors connected to the system.

###### *rtc\_time*

**Required** attribute; **read/write** access; type: **String**.

The date and time of the Real-Time clock.

##### Commands

**create-ia64-460gx-system** [*"name"*] *memory\_megs* *"rtc\_time"*

Creates a non-instantiated component of the class "ia64-460gx-system". If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**new-ia64-460gx-system** [*"name"*] *memory\_megs* *"rtc\_time"*

Creates an instantiated component of the class "ia64-460gx-system". If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<ia64-460gx-system>.info**

Print detailed information about the configuration of the device.

**<ia64-460gx-system>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
cpu[0-31]	ia64-cpu	down
isa-bus	isa-bus	down
pci-slot[2-23]	pci-bus	down

**4.1.2 itanium-cpu****Description**

The "itanium-cpu" component represents an Itanium processor

**Attributes***cpu\_frequency*

**Required** attribute; **read/write** access; type: **Integer**.

Processor frequency in MHz.

**Commands****create-itanium-cpu** [*"name"*] *cpu\_frequency*

Creates a non-instantiated component of the class "itanium-cpu". If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<itanium-cpu>.info**

Print detailed information about the configuration of the device.

**<itanium-cpu>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
backplane	ia64-cpu	up

## 4.2 PCI Device Components

### 4.2.1 pci-sym53c810

**Description**

The “pci-sym53C810” component represents a SYM53C810PCI based SCSI controller.

**Attributes***bios*

**Optional** attribute; **read/write** access; type: **String**.

The x86 SCSI BIOS file to use.

**Commands****create-pci-sym53c810** [*“name”*] [*“bios”*]

Creates a non-instantiated component of the class “pci-sym53c810”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<pci-sym53c810>.info**

Print detailed information about the configuration of the device.

**<pci-sym53c810>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
pci-bus	pci-bus	up
scsi-bus	scsi-bus	down

### 4.2.2 pci-sym53c875

#### Description

The “pci-sym53C875” component represents a SYM53C875PCI based SCSI controller.

#### Attributes

##### *bios*

**Optional** attribute; **read/write** access; type: **String**.

The x86 SCSI BIOS file to use.

#### Commands

##### **create-pci-sym53c875** [*“name”*] [*“bios”*]

Creates a non-instantiated component of the class “pci-sym53c875”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<pci-sym53c875>.info**

Print detailed information about the configuration of the device.

##### **<pci-sym53c875>.status**

Print detailed information about the current status of the device.

#### Connectors

Name	Type	Direction
pci-bus	pci-bus	up
scsi-bus	scsi-bus	down

### 4.2.3 pci-dec21143

#### Description

The “pci-dec21143” component represents a DEC21143 PCI based fast Ethernet adapter.

#### Attributes

##### *bios*

**Optional** attribute; **read/write** access; type: **String**.

The x86 BIOS file to use.

##### *mac\_address*

**Required** attribute; **read/write** access; type: **String**.

The MAC address of the Ethernet adapter.

**Commands**

**create-pci-dec21143** [*name*] [*mac\_address*] [*bios*]

Creates a non-instantiated component of the class "pci-dec21143". If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<pci-dec21143>.info**

Print detailed information about the configuration of the device.

**<pci-dec21143>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
pci-bus	pci-bus	up
ethernet	ethernet-link	down

**4.2.4 pci-ragexl****Description**

The "pci-ragexl" component represents a Rage XL PCI based VGA compatible graphics adapter.

**Commands**

**create-pci-ragexl** [*name*]

Creates a non-instantiated component of the class "pci-ragexl". If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<pci-ragexl>.info**

Print detailed information about the configuration of the device.

**<pci-ragexl>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
pci-bus	pci-bus	up
console	graphics-console	down

## 4.3 PC Legacy Components

### 4.3.1 ps2-keyboard-mouse

#### Description

The “ps2-keyboard-mouse” component represents the PS/2 8042 keyboard controller with a connected 105 key keyboard and three button mouse.

#### Commands

##### **create-ps2-keyboard-mouse** [*“name”*]

Creates a non-instantiated component of the class “ps2-keyboard-mouse”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<ps2-keyboard-mouse>.info**

Print detailed information about the configuration of the device.

##### **<ps2-keyboard-mouse>.status**

Print detailed information about the current status of the device.

#### Connectors

Name	Type	Direction
isa-bus	isa-bus	up
reset	x86-reset-bus	up
kbd-console	keyboard	down
mse-console	mouse	down

### 4.3.2 pc-dual-serial-ports

#### Description

The “pc-dual-serial-ports” component represents two PC compatible serial ports.

#### Commands

##### **create-pc-dual-serial-ports** [*“name”*]

Creates a non-instantiated component of the class “pc-dual-serial-ports”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<pc-dual-serial-ports>.info**

Print detailed information about the configuration of the device.



**<pc-dual-serial-ports>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
isa-bus	isa-bus	up
com[1-2]	serial	down

**4.3.3 isa-vga****Description**

The “isa-vga” component represents an ISA bus based VGA compatible graphics adapter.

**Attributes*****bios***

**Optional** attribute; **read/write** access; type: **String**.

The VGA BIOS file to use.

**Commands****create-isa-vga [“*name*”] [“*bios*”]**

Creates a non-instantiated component of the class “isa-vga”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<isa-vga>.info**

Print detailed information about the configuration of the device.

**<isa-vga>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
isa-bus	isa-bus	up
console	graphics-console	down

## 4.4 Standard Components

### 4.4.1 std-ethernet-link

#### Description

The “std-ethernet-link” component represents a standard Ethernet link.

#### Attributes

##### *frame\_echo*

**Optional** attribute; **read/write** access; type: **Integer**.

Set this attribute to echo frames back to the sender. Default is not to echo frames.

##### *link\_name*

**Optional** attribute; **read/write** access; type: **String**.

The name to use for the **ethernet-link** object. An error will be raised at instantiation time if the link cannot be given this name.

#### Commands

##### **create-std-ethernet-link** [*“name”*] [*“link\_name”*] [*frame\_echo*]

Creates a non-instantiated component of the class “std-ethernet-link”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **new-std-ethernet-link** [*“name”*] [*“link\_name”*] [*frame\_echo*]

Creates an instantiated component of the class “std-ethernet-link”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<std-ethernet-link>.info**

Print detailed information about the configuration of the device.

##### **<std-ethernet-link>.status**

Print detailed information about the current status of the device.

#### Connectors

Name	Type	Direction
device	ethernet-link	any

### 4.4.2 std-service-node

#### Description

The “std-service-node” component represents a network service node that can be connected to Ethernet links to provide services such as DNS, DHCP/BOOTP, RARP and TFTP. A service node component does not have any connectors by default. Instead, connectors have to be added using the **<std-service-node>.add-connector** command.

#### Attributes

##### *dynamic\_connectors*

**Optional** attribute; **read/write** access; type: **[[iss]\*]**.

List of user added connectors

##### *next\_connector\_id*

**Optional** attribute; **read/write** access; type: **Integer**.

Next service-node device ID.

#### Commands

##### **create-std-service-node** [*“name”*]

Creates a non-instantiated component of the class “std-service-node”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **new-std-service-node** [*“name”*]

Creates an instantiated component of the class “std-service-node”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<std-service-node>.add-connector** *“ip”* [*“netmask”*]

Adds a connector to the service-node with specified IP address and netmask. A connector must be created for the service-node before an Ethernet link can be connected to it. The *ip* argument is the IP address that the service node will use on the link. The *netmask* argument is optional, and defaults to 255.255.255.0. The name of the new connector is returned.

##### **<std-service-node>.info**

Print detailed information about the configuration of the device.

##### **<std-service-node>.status**

Print detailed information about the current status of the device.

### 4.4.3 std-scsi-bus

#### Description

The “std-scsi-bus” component represents a 16 slot SCSI bus.

#### Commands

##### **create-std-scsi-bus** [*“name”*]

Creates a non-instantiated component of the class “std-scsi-bus”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<std-scsi-bus>.info**

Print detailed information about the configuration of the device.

##### **<std-scsi-bus>.status**

Print detailed information about the current status of the device.

#### Connectors

Name	Type	Direction
scsi-bus	scsi-bus	any

### 4.4.4 std-scsi-disk

#### Description

The “std-scsi-disk” component represents a SCSI-2 disk.

#### Attributes

##### *file*

**Optional** attribute; **read/write** access; type: **String**.

File with disk contents for the full disk Either a raw file or a CRAFF file.

##### *scsi\_id*

**Required** attribute; **read/write** access; type: **Integer**.

The ID on the SCSI bus.

##### *size*

**Required** attribute; **read/write** access; type: **Integer**.

The size of the SCSI disk in bytes.

#### Commands

**create-std-scsi-disk** [*“name”*] *scsi\_id* *size* [*“file”*]

Creates a non-instantiated component of the class “std-scsi-disk”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<std-scsi-disk>.info**

Print detailed information about the configuration of the device.

**<std-scsi-disk>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
scsi-bus	scsi-bus	up

**4.4.5 std-scsi-cdrom****Description**

The “std-scsi-cdrom” component represents a SCSI-2 CD-ROM.

**Attributes***scsi\_id*

**Required** attribute; **read/write** access; type: **Integer**.

The ID on the SCSI bus.

**Commands****create-std-scsi-cdrom** [*“name”*] *scsi\_id*

Creates a non-instantiated component of the class “std-scsi-cdrom”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<std-scsi-cdrom>.info**

Print detailed information about the configuration of the device.

**<std-scsi-cdrom>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
scsi-bus	scsi-bus	up

### 4.4.6 std-text-console

#### Description

The “std-text-console” component represents a serial text console.

#### Attributes

##### *bg\_color*

**Optional** attribute; **read/write** access; type: **String**.

The background color.

##### *fg\_color*

**Optional** attribute; **read/write** access; type: **String**.

The foreground color.

##### *height*

**Optional** attribute; **read/write** access; type: **Integer**.

The height of the console window.

##### *title*

**Optional** attribute; **read/write** access; type: **String**.

The Window title.

##### *width*

**Optional** attribute; **read/write** access; type: **Integer**.

The width of the console window.

##### *win32\_font*

**Optional** attribute; **read/write** access; type: **String**.

Font to use in the console on Windows host.

##### *x11\_font*

**Optional** attribute; **read/write** access; type: **String**.

Font to use in the console when using X11 (Linux/Solaris host).

#### Commands

**create-std-text-console** [*“name”*] [*“title”*] [*“bg\_color”*] [*“fg\_color”*] [*“x11\_font”*] [*“win32\_font”*] [*“width”*] [*“height”*]

Creates a non-instantiated component of the class “std-text-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**new-std-text-console** [*name*] [*title*] [*bg\_color*] [*fg\_color*] [*x11\_font*] [*win32\_font*] [*win32\_console*]

Creates an instantiated component of the class “std-text-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<std-text-console>.info**

Print detailed information about the configuration of the device.

**<std-text-console>.status**

Print detailed information about the current status of the device.

### Connectors

Name	Type	Direction
serial	serial	up

## 4.4.7 std-server-console

### Description

The “std-server-console” component represents a serial console accessible from the host using telnet.

### Attributes

*telnet\_port*

**Required** attribute; **read/write** access; type: **Integer**.

TCP/IP port to connect the telnet service of the console to.

### Commands

**create-std-server-console** [*name*] *telnet\_port*

Creates a non-instantiated component of the class “std-server-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**new-std-server-console** [*name*] *telnet\_port*

Creates an instantiated component of the class “std-server-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<std-server-console>.info**

Print detailed information about the configuration of the device.

**<std-server-console>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
serial	serial	up

**4.4.8 std-graphics-console****Description**

The “std-graphics-console” component represents a graphical console for displaying output from a simulated graphics adapters and getting input for mouse and keyboard devices.

**Attributes***window*

**Optional** attribute; **read/write** access; type: **b**.

Try to open window if TRUE (default). FALSE disabled the window.

**Commands****create-std-graphics-console** [*“name”*] [*window*]

Creates a non-instantiated component of the class “std-graphics-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**new-std-graphics-console** [*“name”*] [*window*]

Creates an instantiated component of the class “std-graphics-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

**<std-graphics-console>.info**

Print detailed information about the configuration of the device.

**<std-graphics-console>.status**

Print detailed information about the current status of the device.

**Connectors**

Name	Type	Direction
device	graphics-console	up
keyboard	keyboard	up
mouse	mouse	up



#### 4.4.9 std-text-graphics-console

##### Description

The “std-text-graphics-console” component represents a text console for use with VGA instead of a graphics console.

##### Commands

##### **create-std-text-graphics-console [“name”]**

Creates a non-instantiated component of the class “std-text-graphics-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **new-std-text-graphics-console [“name”]**

Creates an instantiated component of the class “std-text-graphics-console”. If *name* is not specified, the component will get a class-specific default name. The other arguments correspond to class attributes.

##### **<std-text-graphics-console>.info**

Print detailed information about the configuration of the device.

##### **<std-text-graphics-console>.status**

Print detailed information about the current status of the device.

##### Connectors

Name	Type	Direction
device	graphics-console	up
keyboard	keyboard	up

## 4.5 Base Components

The base components are abstract classes that contain generic component attributes and commands available for all components.

### 4.5.1 component

##### Description

Base component class, should not be instantiated.

##### Attributes

*connections*

**Optional** attribute; **read/write** access; type: **[[sos]\*]**.

List of connections for the component. The format is a list of lists, each containing the name of the connector, the connected component, and the name of the connector on the other component.

*connectors*

**Pseudo class** attribute; **read-only** access; type: **D**.

Dictionary of dictionaries with connectors defined by this component class, indexed by name. Each connector contains the name of the connector “type”, a “direction” (“up”, “down” or “any”), a flag indicating if the connector can be “empty”, another flag that is set if the connector is “hotplug” capable, and finally a flag that is TRUE if multiple connections to this connector is allowed.

*instantiated*

**Optional** attribute; **read/write** access; type: **b**.

Set to TRUE if the component has been instantiated.

*object\_list*

**Optional** attribute; **read/write** access; type: **D**.

Dictionary with objects that the component consists of.

*object\_prefix*

**Optional** attribute; **read/write** access; type: **String**.

Object prefix string used by the component. The prefix is typically set by the **set-component-prefix** command before the component is created.

*top\_component*

**Optional** attribute; **read/write** access; type: **Object**.

The top level component. Attribute is not valid until the component has been instantiated.

*top\_level*

**Optional** attribute; **read/write** access; type: **b**.

Set to TRUE for top-level components, i.e. the root of a hierarchy.

## 4.5.2 top-component

### Description

Base top-level component class, should not be instantiated.

### Attributes

*components*

**Optional** attribute; **read/write** access; type: **[o\*]**.

List of components below the the top-level component. This attribute is not valid until the object has been instantiated.

*cpu\_list*

**Optional** attribute; **read/write** access; type: [o\*].

List of all processors below the the top-level component. This attribute is not valid until the object has been instantiated.

## Chapter 5

# Miscellaneous Notes

### 5.1 The Firmware (ia64-fakeprom)

The Simics/IA64-460GX package contains a firmware implementation called the *ia64-fakeprom*. The fakeprom handles initial bootstrapping, and is basically an emulation of the PAL, SAL, and EFI layers of a real Itanium system.

#### 5.1.1 Firmware Interface

The ia64-fakeprom accepts a number of arguments, passed as register values when calling the firmware entry point. These arguments are located in input register 0 to 7. A complete list is given in figure 5.1.

Register	Name	Usage
in0	fnc	Function selector If 0, boot with a real EFI implementation ( <i>this is currently unsupported</i> ) If 1, use an EFI emulation layer in the fakeprom If 2, jump directly to <code>sal_vector[2]</code>
in1	debug_flags	
in2	iobase	The base address for memory-mapped i/o
in3	entry	The entry point of kernel or EFI image
in4	rdstart	The start address of a Linux ramdisk (initrd)
in5	rdstart	The size (in bytes) of a Linux ramdisk
in6	cmdline	Pointer to the boot command line
in7	num_cpus	If <code>fnc &lt; 2</code> : the number of CPUs in the system
in7	cpuidx	If <code>fnc = 2</code> : the index of the current CPU

Figure 5.1: The interface to the ia64-fakeprom

## 5.2 Bundles and instruction addresses

In the IA-64 architectures, instructions are encoded in *bundles*, which are 128 bits (16 bytes). Each instruction uses 41 bits, and there is an extra 5-bit template in the bundle. The first instruction in a bundle is said to be in *slot 0* of the bundle, the second instruction in *slot 1*, and the last instruction in *slot 2*.

Since individual instructions do not have well-defined addresses, Simics uses the encoding scheme (bundle address + slot number) when disassembling instructions. Bundle addresses are always 16-byte aligned, and thus the lower 4 bits in the bundle address are always zero. When encoding the slot number, the lowest two bits of the address are used.

This encoding is also used when setting execution breakpoints. To break on execution of the instruction in slot 1 in the bundle located at the address 0x12340, execute the command **break 0x12341**.

## 5.3 Changing the Processor Clock Frequency

The clock frequency of a simulated processor can be set arbitrarily in Simics. This will not affect the actual speed of simulation, but it will affect the number of instructions that need to be executed for a certain amount of simulated time to pass. If your execution only depends on executing a certain number of instructions, increasing the clock frequency will take the same amount of host time (but a shorter amount of target time). However, if there are time based delays of some kind in the simulation, these will take longer to execute.

At a simulated 1 MHz, one million target instructions will correspond to a simulated second (assuming the simple default timing of one cycle per instruction). At 100 MHz, on the other hand, it will take 100 million target instructions to complete a simulated second. So with a higher clock frequency, less simulated target time is going to pass for a certain period of host execution time.

If Simics is used to emulate an interactive system (especially one with a graphical user interface) it is a good idea to set the clock frequency quite low. Keyboard and mouse input events are handled by periodic interrupts in most operating systems, using a higher clock frequency will result in longer delays between invocations of periodic interrupts. Thus, the simulated system will feel slower in its user response, and update the mouse cursor position etc. less frequently. If this is a problem, the best technique for running experiments at a high clock frequency is to first complete the configuration of the machine using a low clock frequency. Save all configuration changes to a disk diff (like when installing operating systems). Then change the configuration to use a higher clock frequency and reboot the target machine.

Note that for a lightly-loaded machine (for example, working at an interactive prompt on a serial console to an embedded Linux system), Simics will often execute quickly enough at the real target clock frequency that there is no need to artificially lower it.

## **Chapter 6**

# **Limitations**

### **6.1 Limitations of the Simulated Model**

- The simulated Itanium processor is incomplete in the following areas:
  - The IA32 mode is not implemented.
  - Some floating-point exceptions are unimplemented.
  - The only supported OS is Linux.
  - The ALAT is not implemented, which means that data speculation always fails.
  - Parts of the instruction set are still missing.
  - Big-endian memory access modes are not supported.
  - Stalling from a timing model is not supported.

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**Virtutech, Inc.**

1740 Technology Dr., suite 460  
San Jose, CA 95110  
USA

Phone +1 408-392-9150  
Fax +1 408-608-0430

<http://www.virtutech.com>