Goals for Today

CS162 Operating Systems and Systems Programming Lecture 27

Peer-to-peer Systems, ManyCore OSes and Other Topics

December 6th, 2010 Prof. John Kubiatowicz http://inst.eecs.berkeley.edu/~cs162

- A couple of requested topics
 - Peer-to-Peer Systems
 - ManyCore OSes
 - Realtime OSs
 - Trusted Computing

Note: Some slides and/or pictures in the following are adapted from slides ©2005 Silberschatz, Galvin, and Gagne

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Peer-to-Peer: Fully equivalent components



- \cdot Peer-to-Peer has many interacting components
 - View system as a set of equivalent nodes » "All nodes are created equal"
 - Any structure on system must be self-organizing
 » Not based on physical characteristics, location, or ownership

Research Community View of Peer-to-Peer



• Old View:

- A bunch of flakey high-school students stealing music

• New View:

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- A philosophy of systems design at extreme scale
- Probabilistic design when it is appropriate
- New techniques aimed at unreliable components
- A rethinking (and recasting) of distributed algorithms
- Use of Physical, Biological, and Game-Theoretic techniques to achieve guarantees

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Why the hype???

- File Sharing: Napster (+Gnutella, KaZaa, etc)
 - Is this peer-to-peer? Hard to say.
 - Suddenly people could contribute to active global network » High coolness factor
 - Served a high-demand niche: online jukebox
- Anonymity/Privacy/Anarchy: FreeNet, Publis, etc
 - Libertarian dream of freedom from the man » (ISPs? Other 3-letter agencies)
 - Extremely valid concern of Censorship/Privacy
 - In search of copyright violators, RIAA challenging rights to privacy
- Computing: The Grid
 - Scavenge numerous free cycles of the world to do work
 - Seti@Home most visible version of this
- Management: Businesses

Publisher@

SetLoc("title", N4)

Key="title" Value=MP3 data...

- Businesses have discovered extreme distributed computing
- Does P2P mean "self-configuring" from equivalent resources?
- Bound up in "Autonomic Computing Initiative"?



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The lookup problem















- Cross-administrative domain
- Contractual Quality of Service ("someone to sue")

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Two Types of OceanStore Data

- Active Data: "Floating Replicas"
 - Per object virtual server
 - Interaction with other replicas for consistency
 - May appear and disappear like bubbles
- Archival Data: OceanStore's Stable Store
 - m-of-n coding: Like hologram
 - » Data coded into *n* fragments, any *m* of which are sufficient to reconstruct (e.g m=16, n=64)
 - » Coding overhead is proportional to n+m (e.g 4)
 - » Other parameter, rate, is 1/overhead
 - Fragments are cryptographically self-verifying
- Most data in the OceanStore is archival!



Self-Organizing Soft-State Replication

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- Simple algorithms for placing replicas on nodes in the interior
 - Intuition: locality properties of Tapestry help select positions for replicas
 - Tapestry helps associate parents and children to build multicast tree
- Preliminary results encouraging
- Current Investigations:
 - Game Theory
 - Thermodynamics



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ManyCore Chips: The future is here

- Intel 80-core multicore chip (Feb 2007) Dual-core SCC Tile 80 simple cores Two FP-engines / core - Mesh-like network 100 million transistors - 65nm feature size Intel Single-Chip Cloud D Computer (August 2010) - 24 "tiles" with two cores/tile - 24-router mesh network - 4 DDR3 memory controllers - Hardware support for message-passing • "ManyCore" refers to many processors/chip - 64? 128? Hard to say exact boundary How to program these? - Use 2 CPUs for video/audio - Use 1 for word processor, 1 for browser - 76 for virus checking??? Parallelism must be exploited at all levels 12/06/10 Kubiatowicz CS162 ©UCB Fall 2010 Lec 27,49
 - PARLab OS Goals: RAPPidS
- Responsiveness: Meets real-time guarantees
 - Good user experience with UI expected
 - Illusion of Rapid I/O while still providing guarantees
 - Real-Time applications (speech, music, video) will be assumed
- Agility: Can deal with rapidly changing environment
 - Programs not completely assembled until runtime
 - User may request complex mix of services at moment's notice
 - Resources change rapidly (bandwidth, power, etc)
- Power-Efficiency: Efficient power-performance tradeoffs
 - Application-Specific parallel scheduling on Bare Metal partitions
 - Explicitly parallel, power-aware OS service architecture
- Persistence: User experience persists across device failures
 - Fully integrated with persistent storage infrastructures
 - Customizations not be lost on "reboot"
- Security and Correctness: Must be hard to compromise
 - Untrusted and/or buggy components handled gracefully
 - Combination of *verification* and *isolation* at many levels
 - Privacy, Integrity, Authenticity of information asserted



Services Support for Applications

- What systems support do we need for new ManyCore applications?
 - Should we just port parallel Linux or Windows 7 and be done with it?
 - A lot of functionality, hard to experiment with, possibly fragile, ...
- Clearly, these new applications will contain:
 - Explicitly parallel components
 - » However, parallelism may be "hard won" (not embarrassingly parallel)
 - » Must not interfere with this parallelism
 - Direct interaction with Internet and "Cloud" services
 - » Potentially extensive use of remote services
 - » Serious security/data vulnerability concerns
 - Real Time requirements
 - » Sophisticated multimedia interactions
 - » Control of/interaction with health-related devices
 - Responsiveness Requirements

» Provide a good interactive experience to users Kubiatowicz CS162 ©UCB Fall 2010

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- The Problem with Current OSs
- What is wrong with current Operating Systems?
 - They (often?) do not allow expression of application requirements
 - » Minimal Frame Rate, Minimal Memory Bandwidth, Minimal QoS from system Services, Real Time Constraints, ...
 - » No clean interfaces for reflecting these requirements
 - They (often?) do not provide guarantees that applications can use
 - » They do not provide performance isolation
 - » Resources can be removed or decreased without permission
 - » Maximum response time to events cannot be characterized
 - They (often?) do not provide fully custom scheduling
 - » In a parallel programming environment, ideal scheduling can depend crucially on the programming model
 - They (often?) do not provide sufficient Security or Correctness
 - » Monolithic Kernels get compromised all the time
 - » Applications cannot express domains of trust within themselves without using a heavyweight process model

The advent of ManyCore both:

- Exacerbates the above with a greater number of shared resources
- Provides an opportunity to change the fundamental model









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Defining the Partitioned Environment

Our new abstraction: Cell

- A user-level software component, with guaranteed resources
- Is it a process? Is it a Virtual Private Machine? Neither, Both
- Different from Typical Virtual Machine Environment which duplicates many Systems components in each VM
- Properties of a Cell
 - Has full control over resources it owns ("Bare Metal")
 - Contains at least one address space (memory protection domain), but could contain more than one
 - Contains a set of secured channel endpoints to other Cells
 - Contains a security context which may protect and decrypt information
 - Interacts with trusted layers of Tessellation (e.g. the "NanoVisor") via a heavily Paravirtualized Interface
 - » E.g. Manipulate address mappings without knowing format of page tables
- When mapped to the hardware, a Cell gets:
 - Gang-schedule hardware thread resources ("Harts")
 - Guaranteed fractions of other physical resources » Physical Pages (DRAM), Cache partitions, memory bandwidth, power
 - Guaranteed fractions of system services

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It's all about the communication

- We are interested in communication for many reasons:
 - Communication crosses resource and security boundaries
 - Efficiency of communication impacts (de)composability
- Shared components complicate resource isolation:
 - Need distributed mechanism for tracking and accounting of resources
 - » E.g.: How guarantee that each partition gets guaranteed fraction of service?

Application A

Shared File Service

Application B

- How does presence of a message impact Cell activation?
 - Not at all (regular activation) or immediate change (interrupt-like)
- Communication defines Security Model
 - Mandatory Access Control Tagging (levels of information confidentiality)
- Ring-based security (enforce call-gate structure with channels) 12/06/10 Kubiatowicz CS162 ©UCB Fall 2010 Lec 27.59



Monitor

And

Adapt

Video &

Window

Drivers

Device

Drivers

Real-Time

Application

Identit

HCI/

Voice

Rec

Persistent

Storage &

File System

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- » TCP/IP stack
- » Firewall
- » Virus Checking
- » Intrusion Detection
- Persistent Storage (Performance Security, Reliability)
- Monitoring services
- » Performance counters
- » Introspection
- Identity/Environment services (Security)
 - » Biometric, GPS, Possession Tracking

Applications Given Larger Partitions

Freedom to use resources arbitrarily Lec 27.60 Kubiatowicz CS162 ©UCB Fall 2010





Modeling and Adaptation Policies



Discussion

- How to divide application into Cell?
 - Cells probably best for coarser-grained components
 - » Fine-grained switching between Cells antithetical to stable resource guarantees
 - Division between Application components and shared OS services natural (obvious?)
 - » Both for security reasons and for functional reasons
 - Division between types of scheduling
 - » Real-time (both deadline-driven and rate-based), pre-scheduled
 - » GUI components (responsiveness most important)
 - » High-throughput (As many resources as can get)
 - » Stream-based (Parallelism through decomposition into pipeline stages)
- What granularity of Application component is best for Policy Service?
 - Fewer Cells in system leads to simpler optimization problem
- Language-support for Cell model?
 - Task-based, not thread based
 - Cells produced by annotating Software Frameworks with QoS needs?
 - Cells produced automatically by just-in-time optimization? » i.e. Selective Just In Time Specialization or SEJITS

Scheduling inside a cell

- · Cell Scheduler can rely on:
 - Coarse-grained time quanta allows efficient fine-grained use of resources
 - Gang-Scheduling of processors within a cell
 - No unexpected removal of resources
 - Full Control over arrival of events
 - » Can disable events, poll for events, etc.
- Pure environment of a Cell \Rightarrow Autotuning will return same performance at runtime as during training phase
- · Application-specific scheduling for performance
 - Lithe Scheduler Framework (for constructing schedulers)
 - » Will be able to handle premptive scheduling/cross-address-space scheduling
 - Systematic mechanism for building composable schedulers
 » Parallel libraries with different parallelism models can be easily composed
 - Of course: preconstructed thread schedulers/models (Silk, pthreads...) as libraries for application programmers
- Application-specific scheduling for Real-Time
- Label Cell with Time-Based Labels. Examples:
 - \gg Run every 1s for 100ms synchronized to \pm 5ms of a global time base
- » Pin a cell to 100% of some set of processors
- Then, maintain own deadline scheduler

What we might like from Hardware

- A good parallel computing platform (Obviously!)
 - Good synchronization, communication (Shared memory would be nice)
 - Vector, GPU, SIMD (Can exploit data parallel modes of computation)
 - Measurement: performance counters
- Partitioning Support
 - Caches: Give exclusive chunks of cache to partitions
 - High-performance barrier mechanisms partitioned properly
 - System Bandwidth
- Power (Ability to put partitions to sleep, wake them up quickly)
- QoS Enforcement Mechanisms
- Ability to give restricted fractions of bandwidth (memory, on-chip network)
- Message Interface: Tracking of message rates with source-suppression for QoS
- Examples: Globally Synchronized Frames (ISCA 2008, Lee and Asanovic)
- Fast messaging support (for channels and possible intra-cell)
 - Virtualized endpoints (direct to destination Cell when mapped, into memory FIFO when not)
 - User-level construction and disposition of messages
 - DMA, user-level notification mechanisms
 - Trusted Computing Platform (automatic decryption/encryption of channel data)

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Realtime OS/Embedded Applications

- Embedded applications:
 - Limited Hardware
 - Dedicated to some particular task
 - Examples: 50-100 CPUs in modern car!
- What does it mean to be "Realtime"?
 - Meeting time-related goals in the real world
 - » For instance: to show video, need to display X frames/sec
 - Hard real-time task:
 - » one which we must meet its deadline
 - » otherwise, fatal damage or error will occur.
 - Soft real-time task:
 - » one which we should meet its deadline, but not mandatory.
 - » We should schedule it even if the deadline
 - Firm real time
 - » Result has no utility outside deadline window, but system can withstand a few missed results
- Determinism:
 - Sometimes, deterministic behavior is more important than high performance

Type of Real-Time Scheduling

- Dynamic vs. Static
 - Dynamic schedule computed at run-time based on tasks really executing
 - Static schedule done at compile time for all *possible* tasks
- Preemptive permits one task to preempt another one of lower priority
- Schedulability:
 - NP-hard if there are any resources dependencies
 - Options:

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- » Prove it definitely cannot be scheduled
- » Find a schedule if it is easy to do
- » Stuck in the middle somewhere

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Scheduling Parameters

Static Schedule

 Assume N CPUs available for execution of a single task set Set of tasks {Ti} Periods pi Deadline di (completion deadline after task is queued) Execution time ci (amount of CPU time to complete) Handy values: Laxity li = di - ci (amount of slack time before Ti must begin execution) Utilization factor ui = ci/pi(portion of CPU used) 	 Assume non-preemptive system with 5 Restrictions: Tasks {Ti} are periodic, with hard deadlines and no jitter Tasks are completely independent Deadline = period pi = di Computation time ci is known and constant Context switching is free (zero cost) INCLUDING network messages to send context to another CPU(!) 		
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 Static Schedule Consider least common multiple of periods pi This considers all possible cases of period phase differences Worst case is time that is product of all periods; usually not that bad If you can figure out (somehow) how to schedule this, you win Performance Optimal if all tasks always run; can get up to 100% utilization If it runs once, it will always work 	 EDF: Earliest Deadline First Assume a preemptive system with dynamic priorities, and (same 5 restrictions) Scheduling policy: Always execute the task with the nearest deadline Performance Optimal for uniprocessor (supports up to 100% of CPU usage in all situations) If you're overloaded, ensures that a lot of tasks don't complete Everyone gets a chance to fail at expense of later tasks Variation: Constant Bandwidth Service (CBS) Allows one or more of the EDF-scheduled tasks to be scheduled as "servers" with a guaranteed (minimum) fraction of the CPU When deadline is "up", simply go on to next task and refresh the total fraction of CPU time for later use Set new deadline in future and new maximum CPU time 		

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Least Laxity

- Assume a *preemptive* system with *dynamic* priorities, and (same 5 restrictions)
- Scheduling policy:
 - Always execute the task with the smallest laxity
- Performance:
 - Optimal for uniprocessor (supports up to 100% of CPU usage in all situations)
 - » Similar in properties to EDF
 - A little more general than EDF for multiprocessors
 - » Takes into account that slack time is more meaningful than deadline for tasks of mixed computing sizes
 - Probably more graceful degradations
 - » Laxity measure can dump tasks that are hopeless causes

- · Pro:
 - If it works, it can get 100% efficiency (on a uniprocessor)
- · Con:
 - It is not always feasible to prove that it will work in all cases
 - » And having it work for a while doesn't mean it will always work
 - Requires dynamic prioritization
 - The laxity time hack for global priority has limits
 - » May take too many bits to achieve fine-grain temporal ordering
 - » May take too many bits to achieve a long enough time horizon

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 Assume of priorities Scheduling Highen period Performation Provide CPU late Scan If all works 	Rate Monotonic a preemptive system with static s, and (same 5 restrictions) plus ng policy: st static priority goes to shortest d; always execute highest priority $u = \sum \mu_i = \sum \frac{c_i}{p_i} \le N(2^{\frac{1}{N}} - 1)$; $\mu \approx 0.7$ for large ance: les a guarantee for schedulability wo bad of ~70% en with arbitrarily selected task periods a do better if you know about periods & or periods are multiple of shortest per for CPU load of 100%	N vith ffsets zriod,		Trusted Computing	

Trusted Computing

- Problem: Can't trust that software is correct
 - Viruses/Worms install themselves into kernel or system without users knowledge
 - Rootkit: software tools to conceal running processes, files or system data, which helps an intruder maintain access to a system without the user's knowledge
 - How do you know that software won't leak private information or further compromise user's access?
- A solution: What if there were a secure way to validate all software running on system?
 - Idea: Compute a cryptographic hash of BIOS, Kernel, crucial programs, etc.
 - Then, if hashes don't match, know have problem
- Further extension:
 - Secure attestation: ability to *prove* to a remote party that local machine is running correct software
 - Reason: allow remote user to avoid interacting with compromised system
- Challenge: How to do this in an unhackable way
 - Must have hardware components somewhere

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TCPA: Trusted Computing Platform Alliance

- Idea: Add a Trusted Platform Module (TPM)
- Founded in 1999: Compaq, HP, IBM, Intel, Microsoft
- Currently more than 200 members
- Changes to platform
 - Extra: Trusted Platform Module (TPM)
 - Software changes: BIOS + OS
- Main properties
 - Secure bootstrap
 - Platform attestation
 - Protected storage
- Microsoft version:
 - Palladium

- ATMEL TPM Chip (Used in IBM equipment)
- Note quite same: More extensive hardware/software system

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Trusted Platform Module

Functional Units	Non-volatile Memory	Volatile Memory	
Random Num Generator	Endorsement Key (2048 Bits)	RSA Key Slot-0	
SHA-1 Hash	Storage Root Key (2048 Bits)	RSA Key Slot-9	
HMAC	Owner Auth Secret(160 Bits)	PCR-0	
RSA Encrypt/ Decrypt		Key Handles	
RSA Key Generation		Auth Session Handles	

- Cryptographic operations
 - Hashing: SHA-1, HMAC
 - Random number generator
 - Asymmetric key generation: RSA (512, 1024, 2048)
 - Asymmetric encryption/ decryption: RSA
 - Symmetric encryption/ decryption: DES, 3DES (AES)
- Tamper resistant (hash and key) storage

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