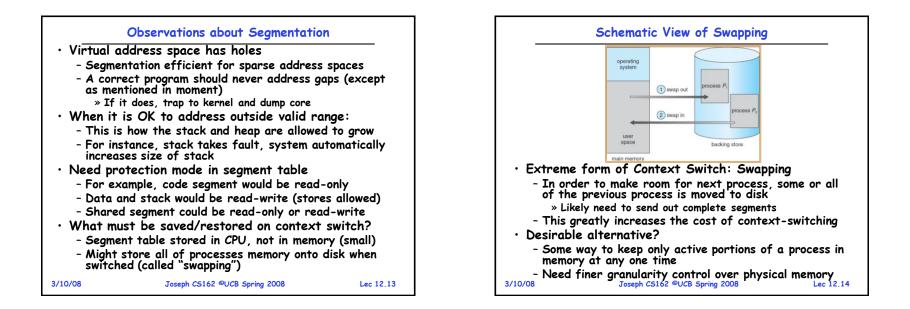
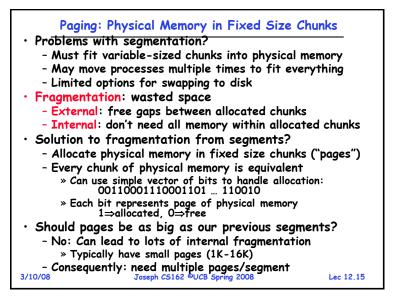
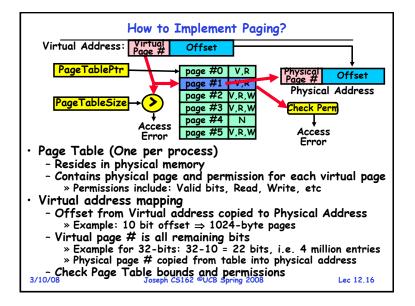
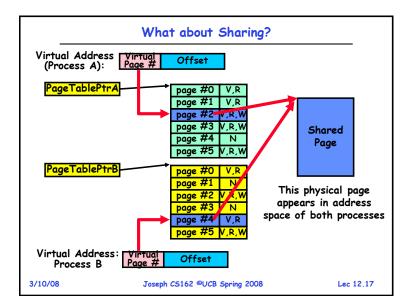


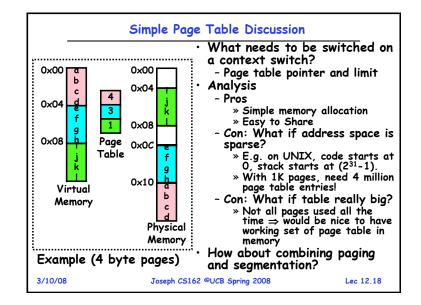
0x240	main:	1a \$	a0, varx				
0x244		jal	strlen		Seg ID #	Base	Limit
			• • • •		0 (code)	0x4000	0x0800
0x360 0x364	strlen: loop:		\$v0, 0 ;count \$t0, (\$a0)		1 (data)	0x4800	0x1400
0x368	100p.		\$r0,\$t1, done		2 (shared)	0xF000	0x1000
 0x4050			0x314159		3 (stack)	0x0000	0×3000
· Fe Phy Fe Ma 2. Fe	tch 0x240. vsical addre tch instruct ve 0x4050 tch 0x244.	Virtu ess? B tion at → \$c Tran	this code to see wi nal segment #? 0; Base=0x4000, so pi t 0x4240. Get "la a0, Move PC+4→PC slated to Physical=	Of 1y: \$a 0x	fset? 0x240 sical addr=0 0, varx" 4244. Get) x4240 "jal strle	
 Fe Phy Fe Ma 2. Fe Ma 3. Fe Ma 4. Fe 	tch 0x240. ysical addre tch instruct ve 0x4050 tch 0x244. ve 0x0248 tch 0x360. ve 0x0000 tch 0x364.	Virtu ess? B tion at → \$c Tran: → \$r Tran: → \$v Tran:	al segment #? 0; asse=0x4000, so pi t 0x4240. Get "la ado, Move PC+4→PC slated to Physical= ra (return address! slated to Physical= A, Move PC+4→PC slated to Physical=	Of 1y: \$a 0x 0x 0x	fset? 0x24(sical addr=0 0, varx" 4244. Get Move 0x036 4360. Get" 4364. Get") ×4240 ¹⁰ → PC 11 \$v0,0" 16 \$t0,(\$	n"
Fe Phy Fe 2. Fe Ma 3. Fe Ma 4. Fe Sir Phy	tch 0x240. vsical addre tch instruct ve 0x40500 tch 0x244. ve 0x0248 tch 0x360. ve 0x0000 tch 0x364. ice \$a0 is ice \$a0 is canslate 0x4	Virtu ess? E tion at → \$c Tran: → \$r Tran: 0x405 Ю50. ess? E	al segment #? 0; Base=0x4000, so pl t 0x4240. Get "la a0, Move PC+4→PC slated to Physical= ra (return address! slated to Physical= 60, Move PC+4→PC	Of 1y: \$a 0x 0x 0x 2 1 ? :ica	ifset? 0x24(sical addr=0. 0, varx" 4244. Get Move 0x036 4360. Get " 4364. Get " ram 0x4050 1; Offset? (1) addr = 0x) ×4240 0 → PC 1i \$v0,0" 1b \$t0,(\$) 0x50	n"

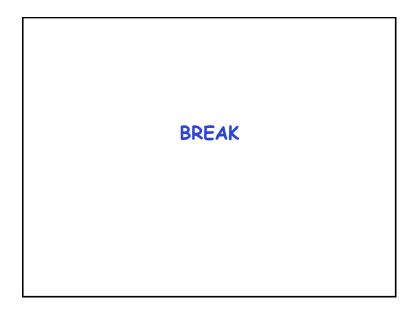


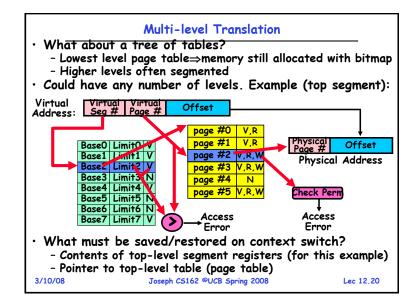


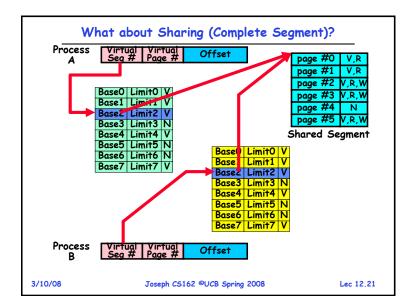


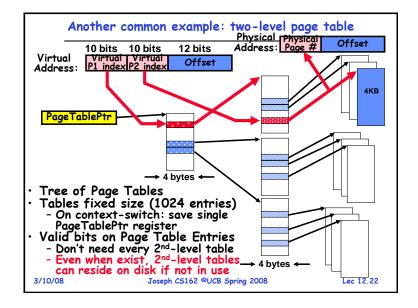


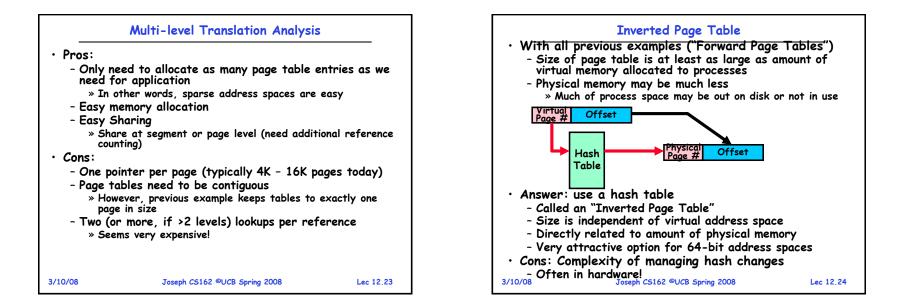


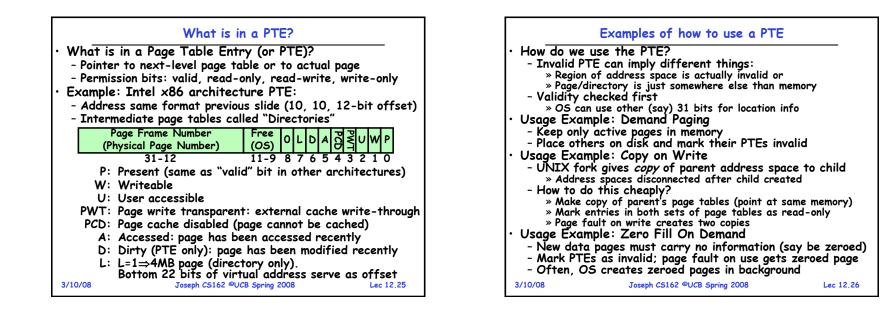


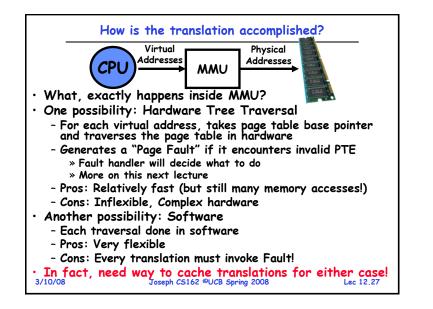


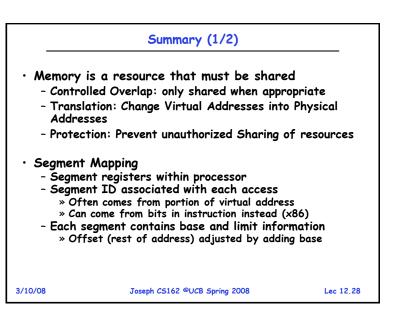












Summary (2/2)						
- Virtua throu <u>c</u> - Offse	bles ry divided into fixed-sized chunks of I page number from virtual address h page table to physical page numbe t of virtual address same as physica page tables can be placed into virtu	mapped r I address				
- Virtua	vel Tables I address mapped to series of tables sparse population of address space	5				
	l page table f page table related to physical mer	nory size				
3/10/08	Joseph CS162 ©UCB Spring 2008	Lec 12.29				