

CS250: DISCUSSION #6

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OVERVIEW

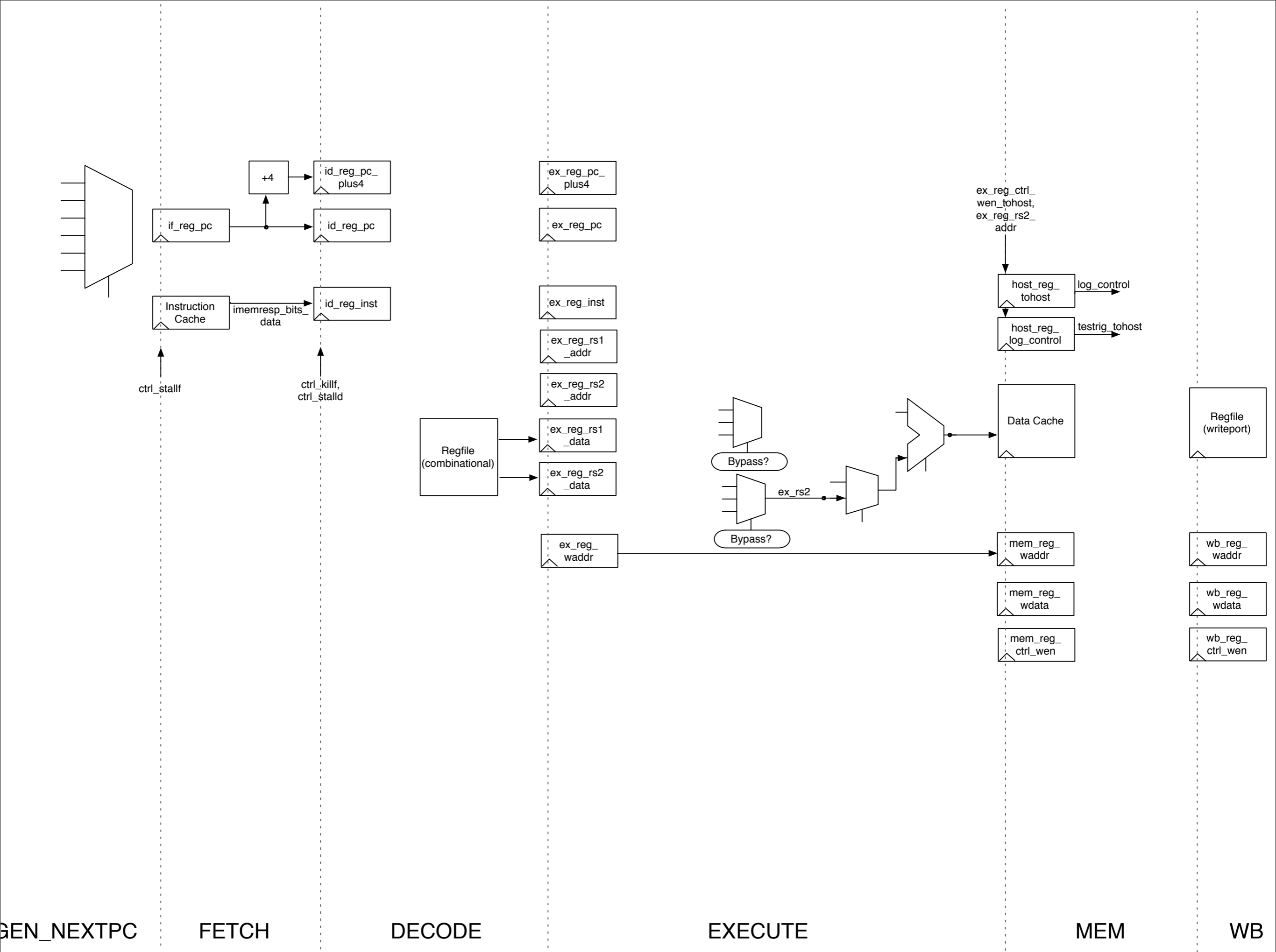
- Logistics
- Memories
- Design space exploration
- Matlab

LOGISTICS

- Start lab 3 now!
 - Make use of Piazza, office hours, and discussion

LAB 3 HINTS

- Draw it!
 - Get Visio: <http://msdnaa.eecs.berkeley.edu/>
 - Posted template to lab-templates/5stage_template
- Debugging code provided
 - You won't have these signals, so comment out for now...

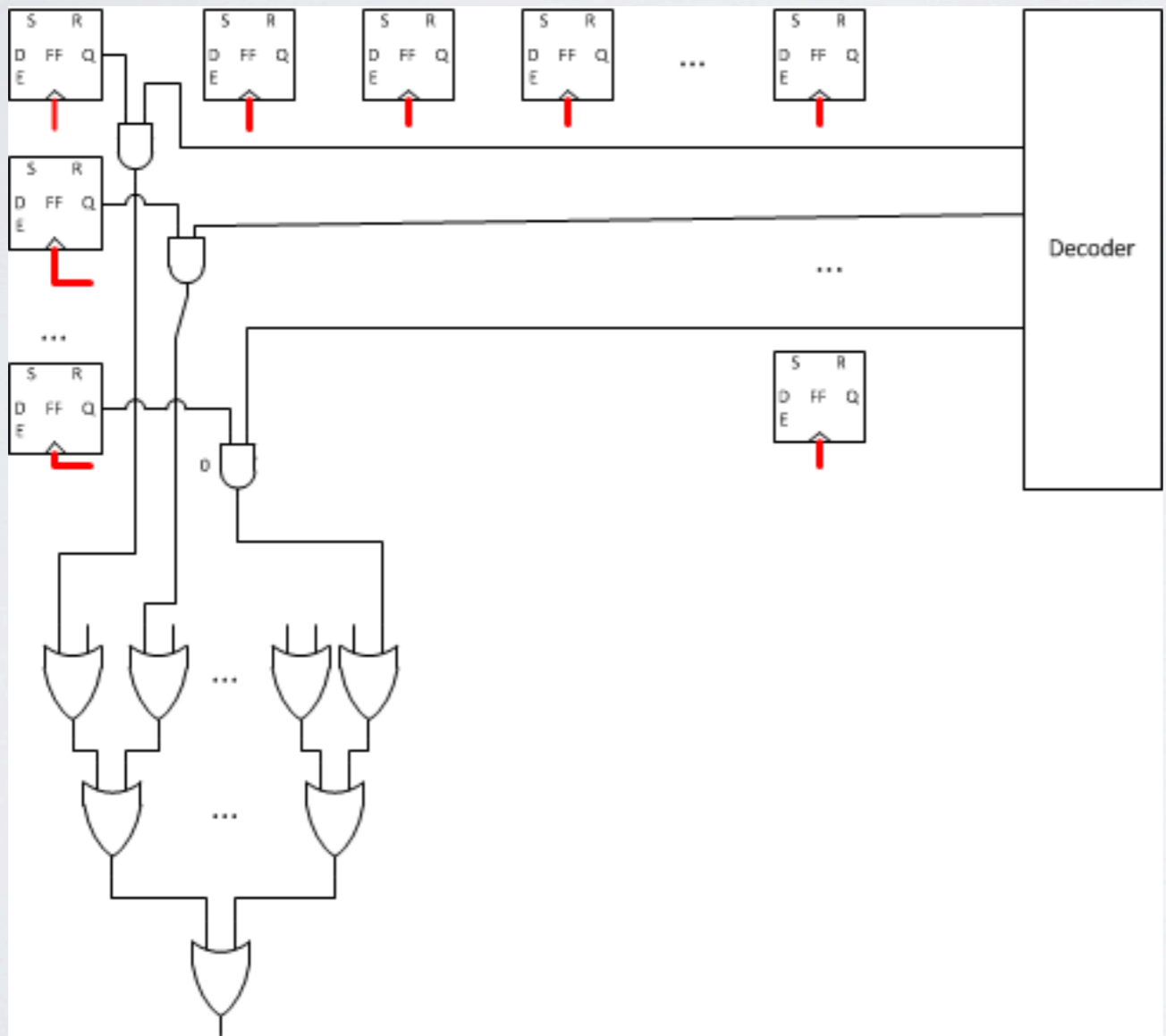


HOW THE TEST HARNESS WORKS

- Chisel
- Verilog

MEMORY OPTIONS

- Flip-flops (eg. register file)
- Latches (eg. register file)
- 6T SRAM (eg. caches)
- 8T SRAM (eg. register file)



DESIGN SPACE EXPLORATION

- Two ways to compare things:
 - Different directories (file system) or different commit points (Git)
- Templating
 - Start with a template, then copy it and insert the desired value

EXAMPLE: BRANCH PREDICTION

STORING RESULTS

- Multiple CSV files?
- A single CSV file?
- A database?

PLOTTING

- Script it to plot directly from data
 - Don't redo your plots constantly in Excel...
- Make the plots nice to start with
 - eg. Matlab:

```
set(0,'DefaultTextFontName','Times',...  
'DefaultTextFontSize',18,...  
'DefaultAxesFontName','Times',...  
'DefaultAxesFontSize',18,...  
'DefaultLineLineWidth',1,...  
'DefaultLineMarkerSize',7.75)  
  
print('-depsc2','-r600',strcat('plot.eps'))
```