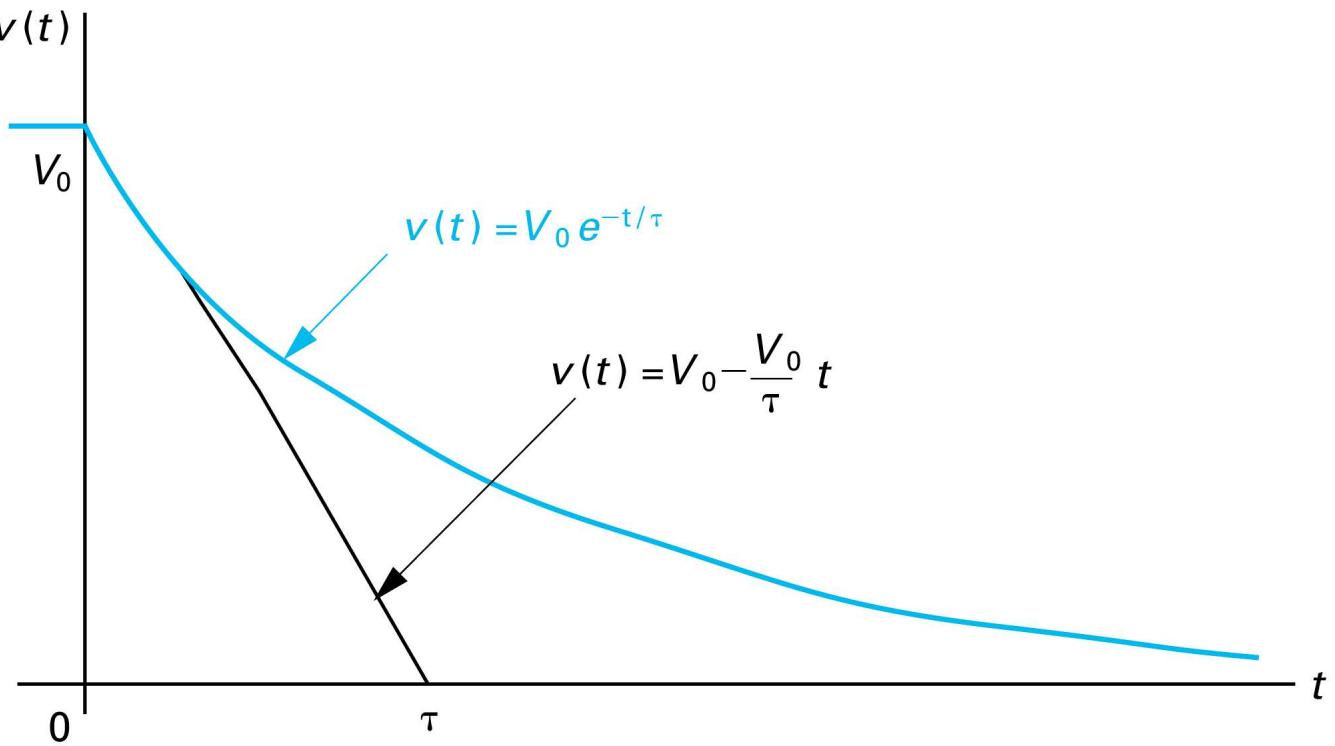


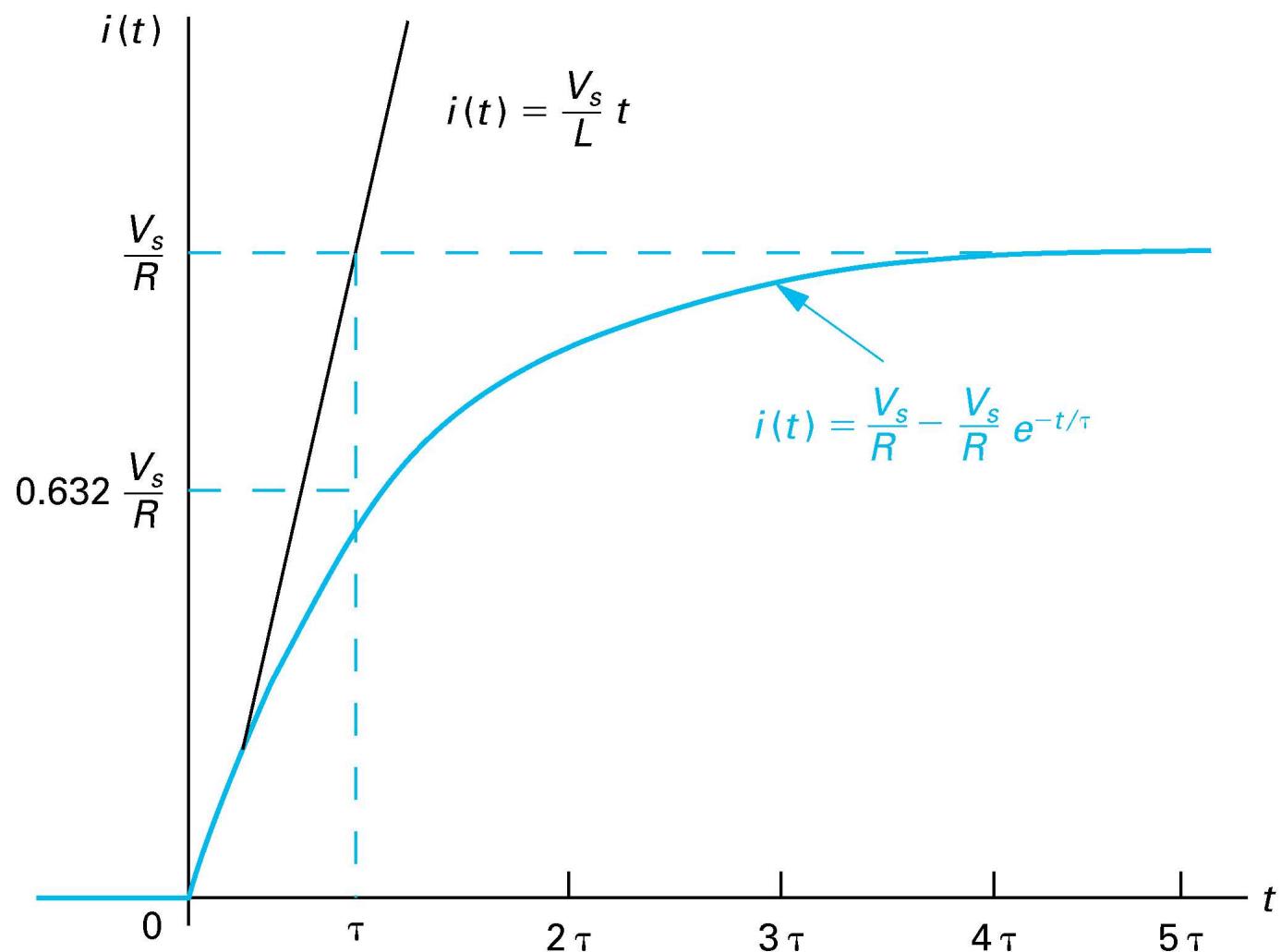
**Figure 7.6** A graphic interpretation of the time constant of the RL circuit shown in Fig. 7.4.

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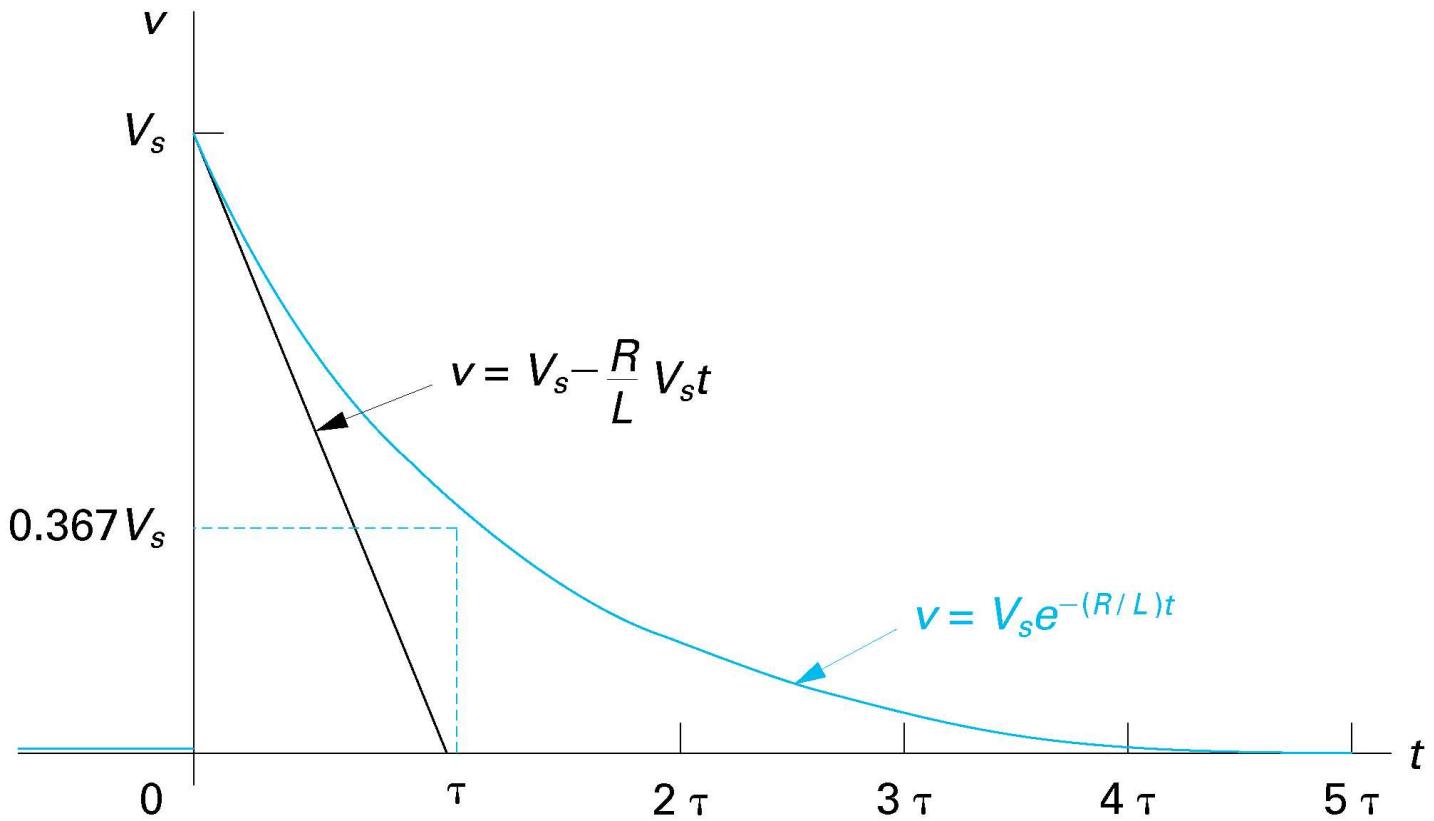
**Figure 7.12** The natural response of an RC circuit.

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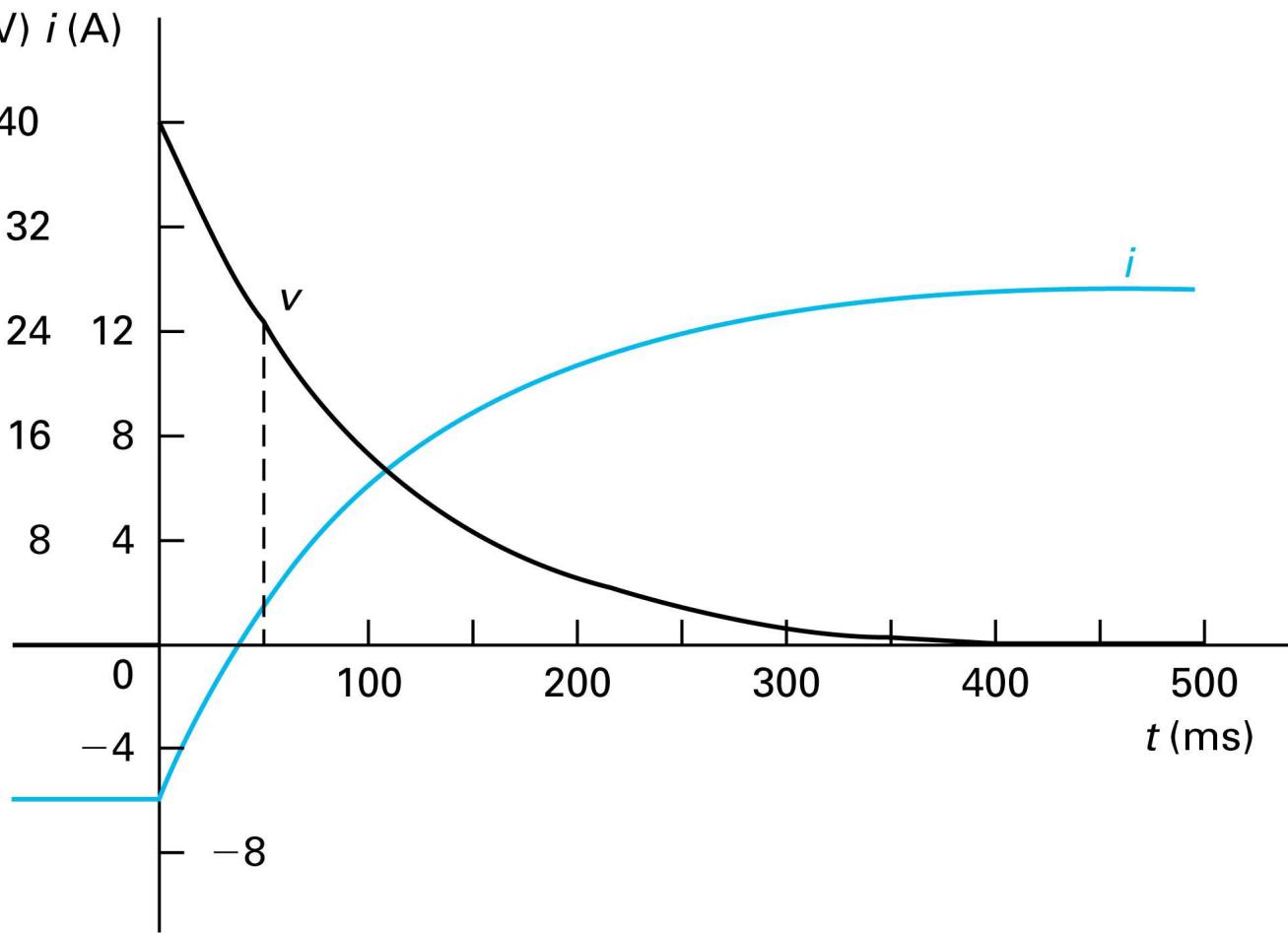
**Figure 7.17** The step response of the RL circuit shown in Fig. 7.16 when  $I_0 = 0$ .

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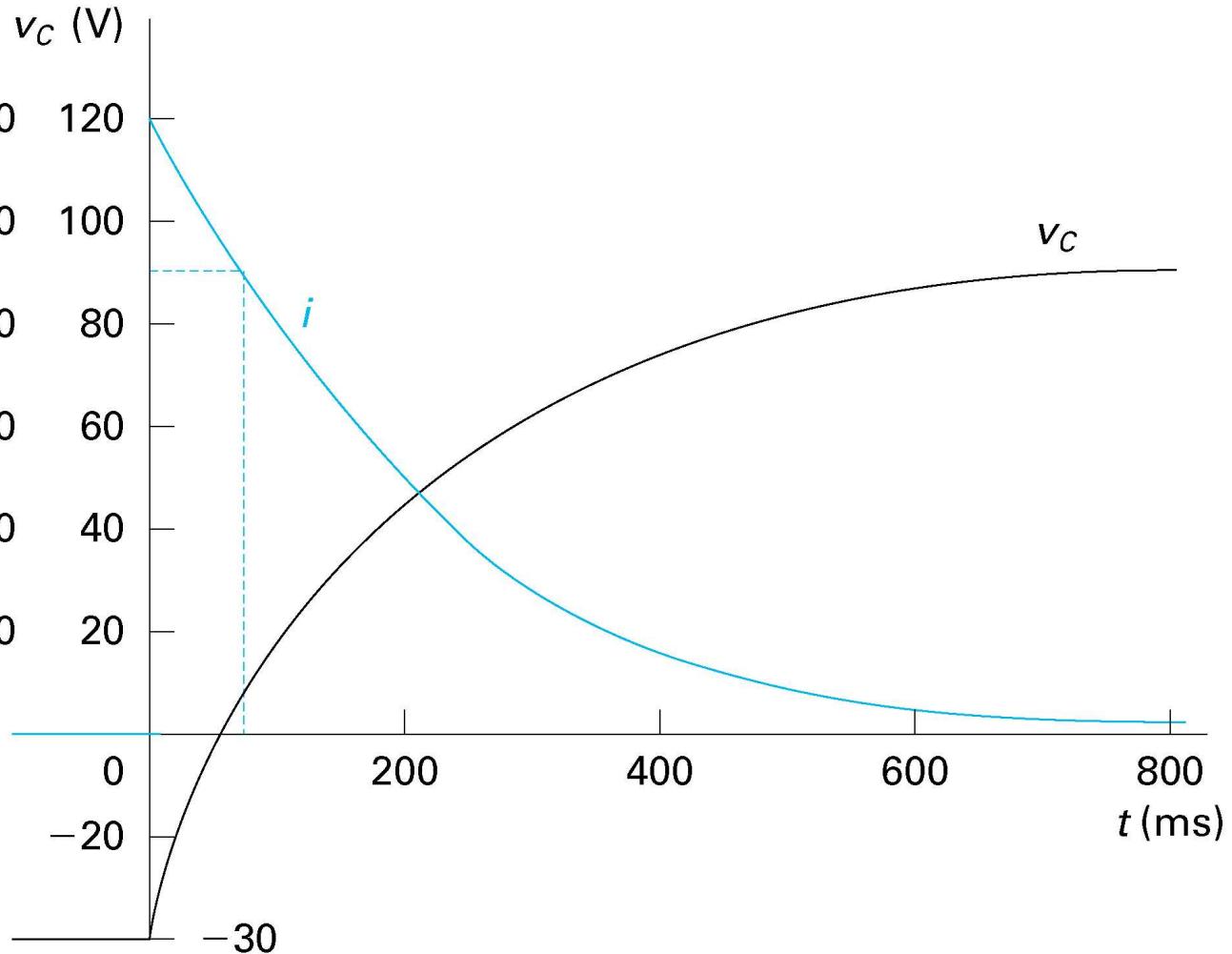
**Figure 7.18** Inductor voltage versus time.

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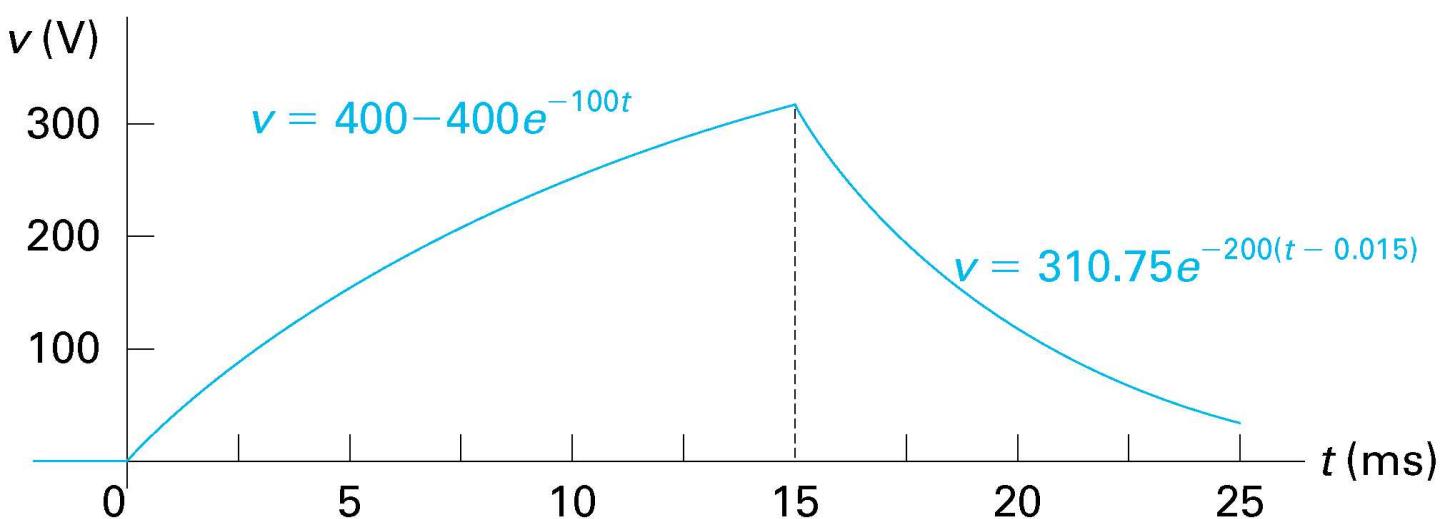
**Figure 7.20** The current and voltage waveforms for Example 7.5.

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**Figure 7.26** The current and voltage waveforms for Example 7.7.

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**Figure 7.36** The capacitor voltage for Example 7.12.

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