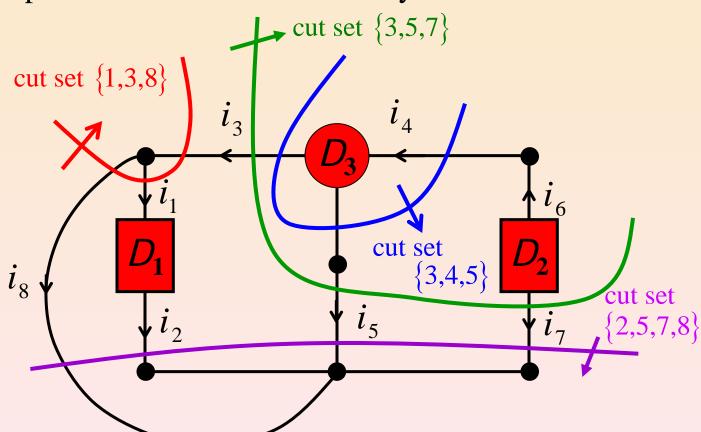
KCL

Before writing KCL on a cut set, we assign *arbitrarily* a positive reference direction by an arrowhead.

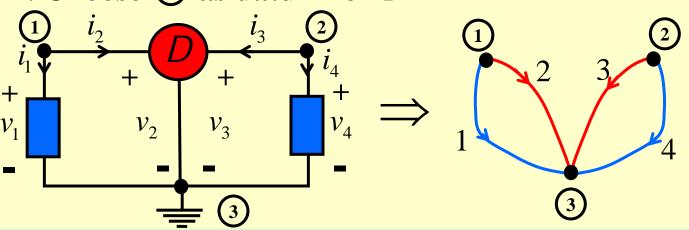


Some Examples of cut sets

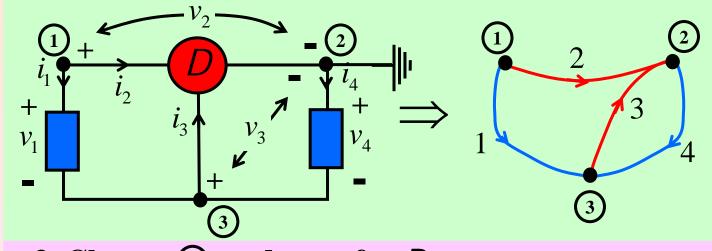
$$\begin{cases}
3,5,7 \} \Rightarrow -i_3 - i_5 - i_7 = 0 \\
\{1,3,8 \} \Rightarrow i_3 - i_1 - i_8 = 0 \\
\{2,5,7,8 \} \Rightarrow i_2 + i_5 + i_7 + i_8 = 0 \\
\{3,4,5 \} \Rightarrow i_3 - i_4 + i_5 = 0
\end{cases}$$

A Circuit with 3 different digraphs

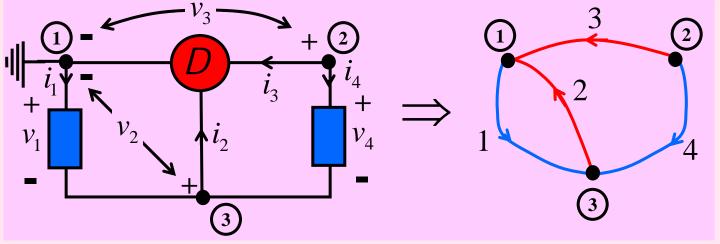
1. Choose 3 as datum for D



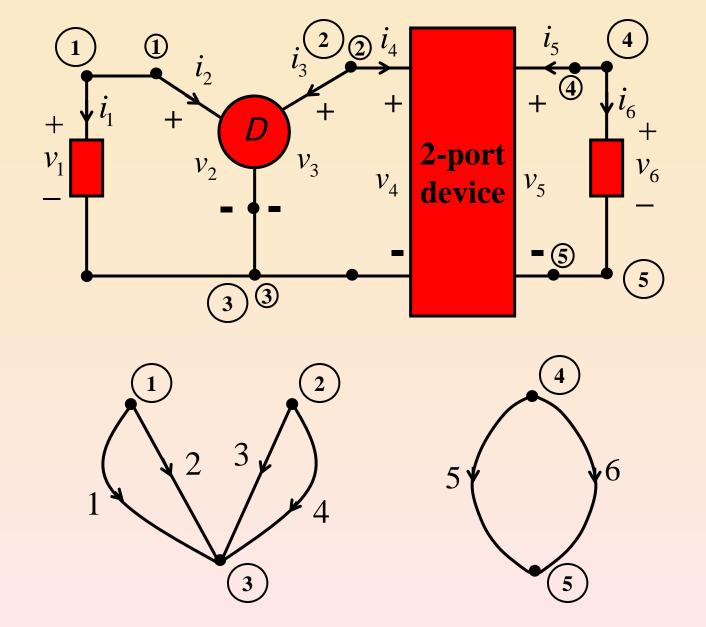
2. Choose ② as datum for D



3. Choose \bigcirc as datum for D



- Circuits containing *n*-terminal devices can have many distinct digraphs, due to different (arbitrary) choices of the datum terminal for each *n*-terminal device.
- Although the KCL and KVL equations associated with 2 different digraphs of a given circuit are different, they contain the same information because each set of equations can be derived from the other.



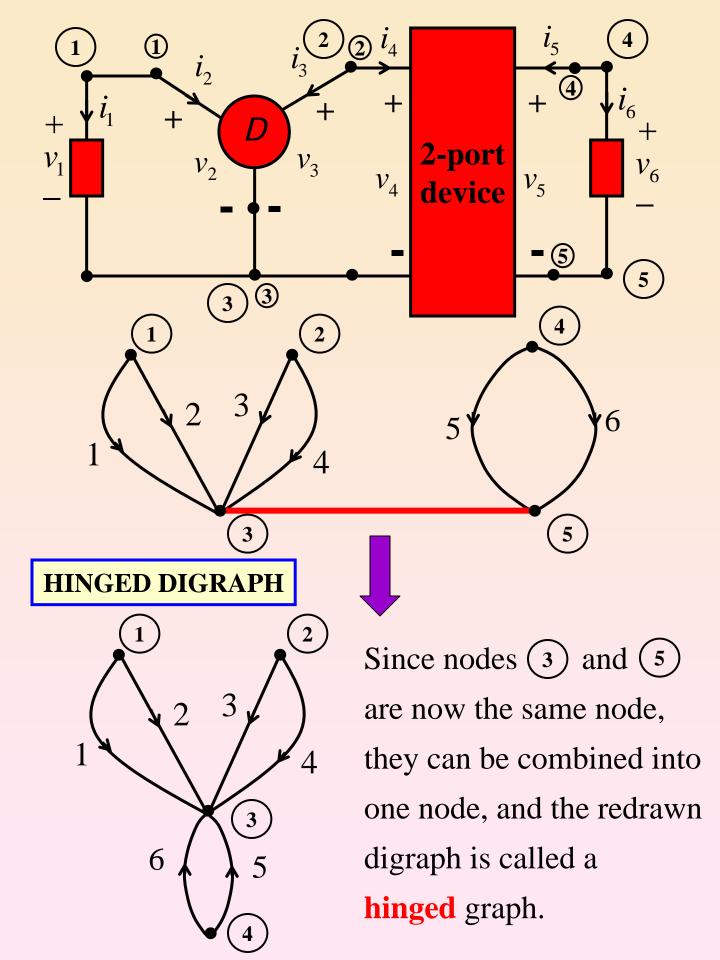
disconnected digraph

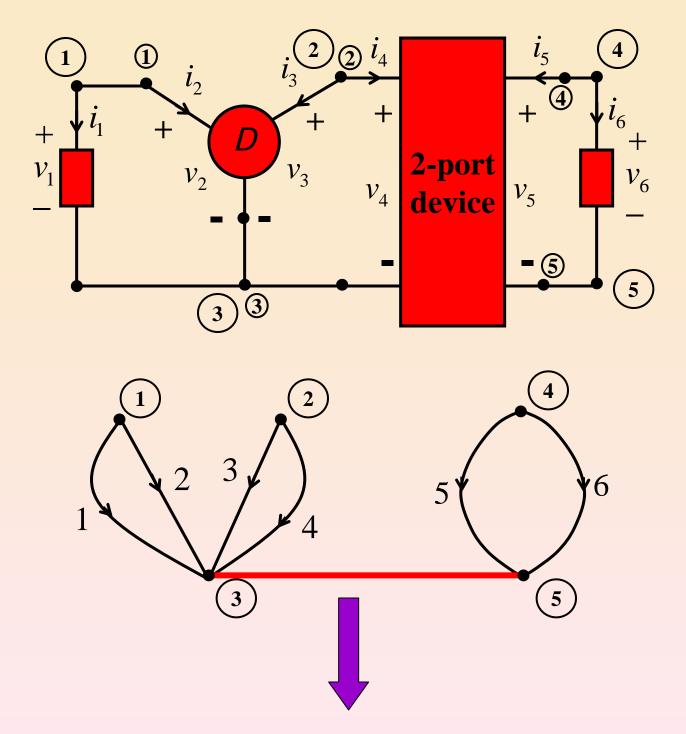
KCL at ②:
$$i_3 + i_4 = 0$$

KCL at (4):
$$i_5 + i_6 = 0$$

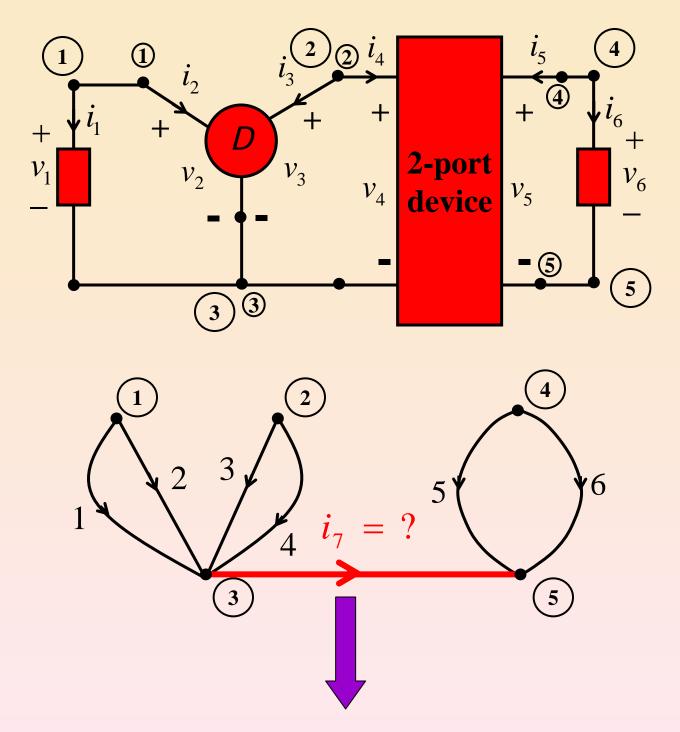
KVL around
$$(2)$$
 - (3) - (2) : $v_4 - v_3 = 0$

KVL around (4)-(5)-(4):
$$v_6 - v_5 = 0$$

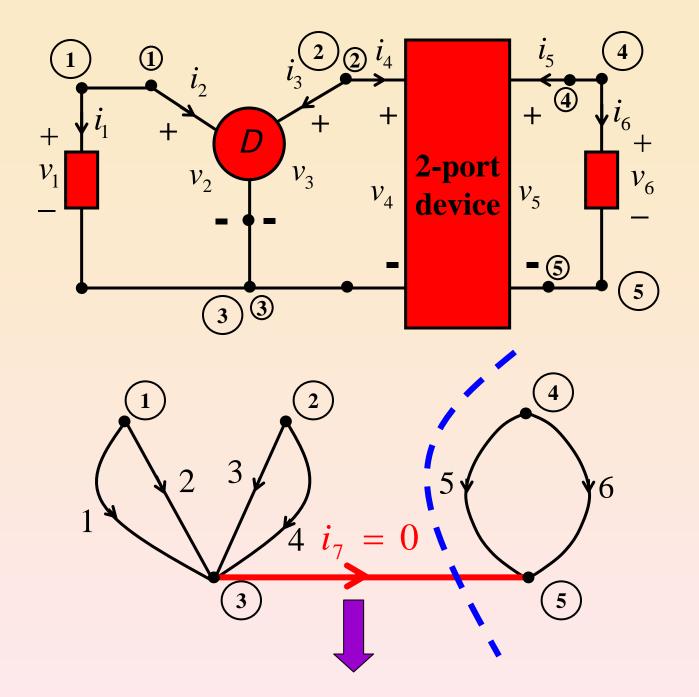




Adding a wire connecting one node from each separate component does **not** change KVL or KCL equations.



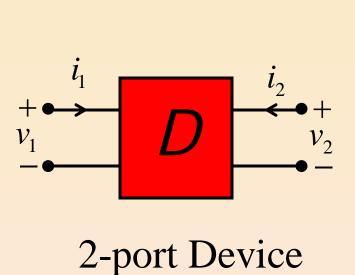
Adding a wire connecting one node from each separate component does **not** change KVL or KCL equations.

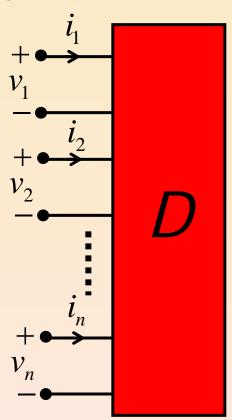


Adding a wire connecting one node from each separate component does **not** change KVL or KCL equations.

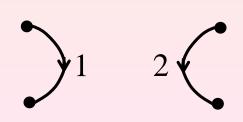
$$\{7\}$$
 is a **cut set** \Rightarrow $i_7 = 0$

Associated Reference Convention:

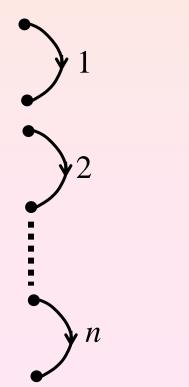


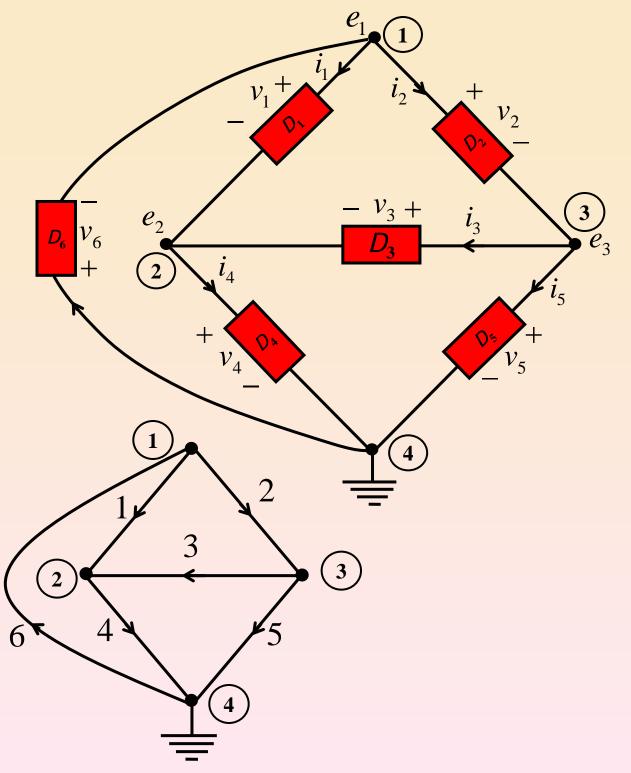


Device Graph



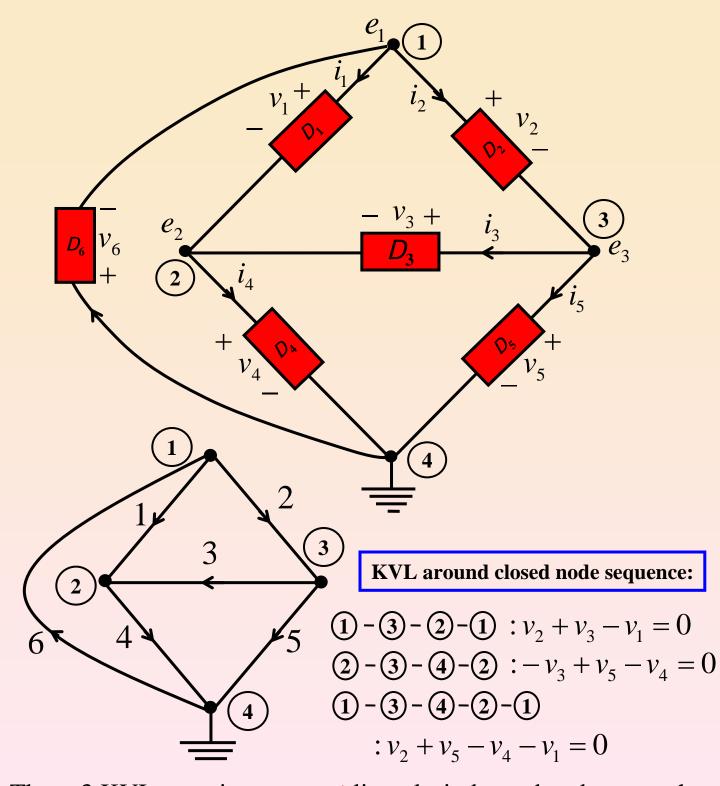






KCL at (1):
$$i_1 + i_2 - i_6 = 0$$

KVL around
$$1 - 3 - 4 - 2 - 1$$
 $v_2 + v_5 - v_4 - v_1 = 0$



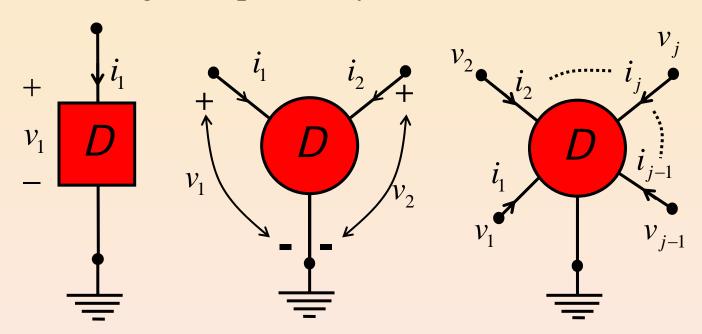
These 3 KVL equations are **not** linearly-independent because the 3rd equation can be obtained by adding the first 2 equations:

$$(v_2 + v_3 - v_1) + (-v_3 + v_5 - v_4)$$

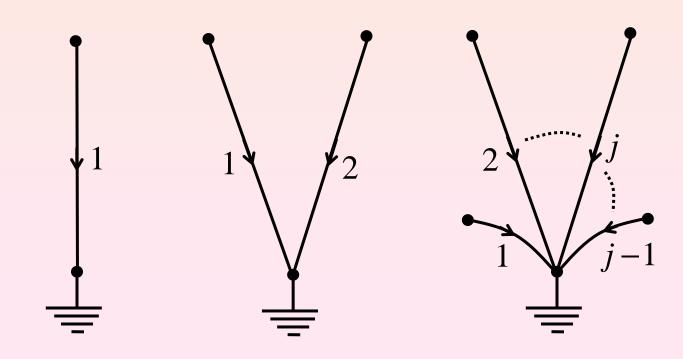
= $v_2 - v_1 + v_5 - v_4 = 0$

Associated Reference Convention:

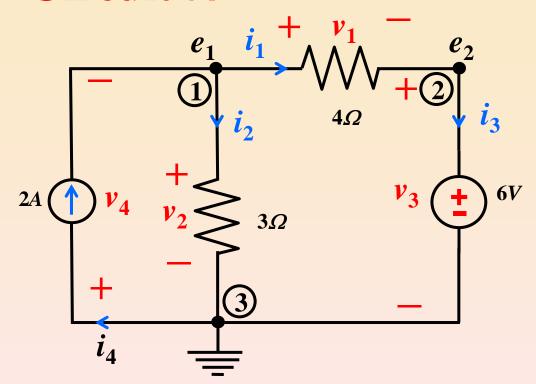
A current direction is chosen entering each positively-referenced terminal.



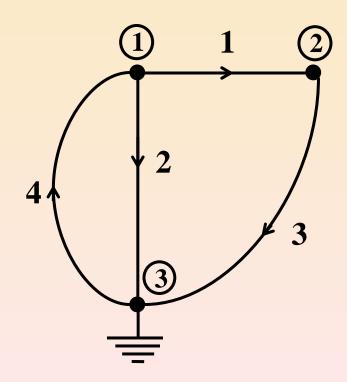
Device Graph: DIGRAPH (Directed Graph)



Circuit N



Digraph G



Reduced Incidence Matrix A

branch number

KCL:
$$\begin{bmatrix}
1 & 1 & 0 & -1 \\
-1 & 0 & 1 & 0
\end{bmatrix}
\begin{bmatrix}
i_1 \\
i_2 \\
i_3 \\
i_4
\end{bmatrix} = \begin{bmatrix}
0 \\
0 \\
0 \\
0
\end{bmatrix}
\Rightarrow \begin{bmatrix}
i_1 + i_2 - i_4 = 0 \\
-i_1 + i_3 = 0
\end{bmatrix}$$

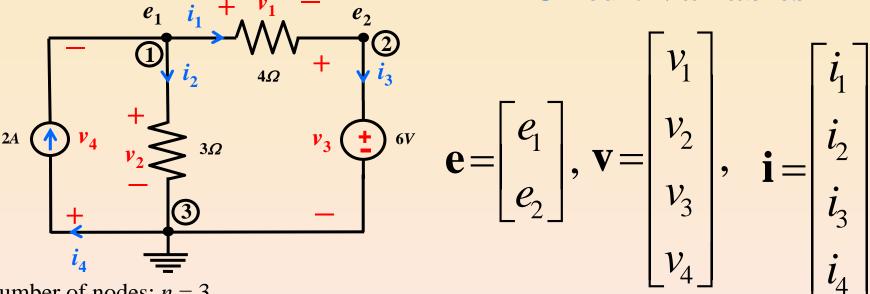
KVL:

$$\begin{bmatrix} v_1 \\ v_2 \\ v_3 \\ v_4 \end{bmatrix} = \begin{bmatrix} 1 & -1 \\ 1 & 0 \\ 0 & 1 \\ -1 & 0 \end{bmatrix} \begin{bmatrix} e_1 \\ e_2 \end{bmatrix} \Rightarrow \begin{bmatrix} v_1 = e_1 - e_2 \\ v_2 = e_1 \\ v_3 = e_2 \\ v_4 = -e_1 \end{bmatrix}$$

$$\mathbf{V} \qquad \mathbf{A^T} \qquad \mathbf{e}$$

Circuit N

Circuit Variables



Number of nodes: n = 3

Number of branches : b = 4

Number of circuit variables: 2b+(n-1)=(2x4)+(3-1)=10

Number of Independent KCL Equations : n-1 = 2

Number of Independent KVL Equations: b = 4

Total number of independent KCL and KVL Equations : b+(n-1) = 6

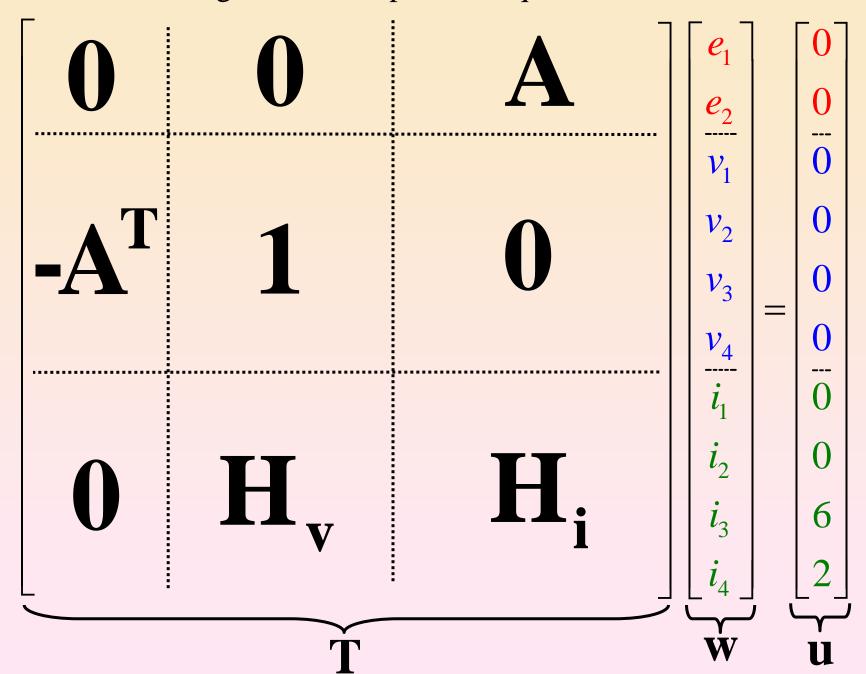
We need "b" additional independent equations in order to obtain a system of 2b+(n-1)**independent equations** in 2b+(n-1) circuit variables.

The additional equations must come from the **constitutive relation** which relate the terminal voltages and currents of the circuit elements.

Let us rearrange all 10 independent equations as follow:

$\int 0$	0	0	0	0	0	1	1	0	-1	e_1		0
0	0	0	0	0	0	-1	0	1	0	e_2		0
<u>-1</u>	1	1	0	0	0	0	0	0	0	v_1		0
-1	0	0	1	0	0	0	0	0	0	v_2		0
0	-1	0	0	1	0	0	0	0	0	v_3		0
1	0	0	0	0	1	0	0	0	0	v_4	_	0
0	0	1	0	0	0	-4	0	0	0	i_1		0
0	0	0	1	0	0	0	-3	0	0	i_2		0
0	0	0	0	1	0	0	0	0	0	i_3		6
0	0	0	0	0	0	0	0	0	1	$\lfloor i_4 \rfloor$		$\lfloor 2 \rfloor$

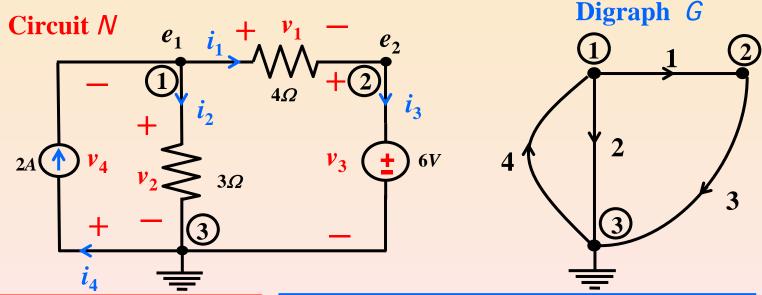
Let us rearrange all 10 independent equations as follow:



How Many Circuit Variables?

Answer:

Total Number of Circuit Variables = 2b + n - 1



Number of Nodes: n = 3

$$\mathbf{e} = \begin{bmatrix} e_1 \\ e_2 \end{bmatrix}$$

node-to-datum voltages:
$$n-1=2$$

Number of branches:
$$b = 4$$

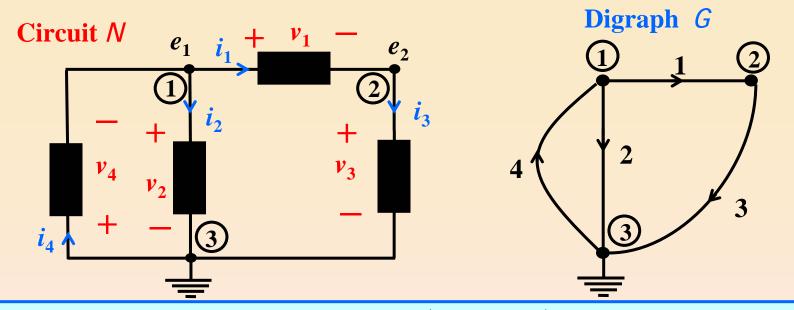
$$\mathbf{v} = \begin{bmatrix} v_1 \\ v_2 \\ v_3 \\ v_4 \end{bmatrix}$$

$$\mathbf{i} = \begin{bmatrix} i_1 \\ i_2 \\ i_3 \\ i_4 \end{bmatrix}$$

branch voltages: b = 4 b

branch currents: b = 4

There are (n-1)+b+b = 10 circuit variables; namely, $\{e_1, e_2, v_1, v_2, v_3, v_4, i_1, i_2, i_3, i_4\}$.



There are infinitely many sets of branch voltages (v_1, v_2, v_3, v_4) which satisfy KVL for G. 2 Examples satisfying KVL:

KVL solution 1:
$$v_1 = -3V, v_2 = 2V, v_3 = 5V, v_4 = -2V$$

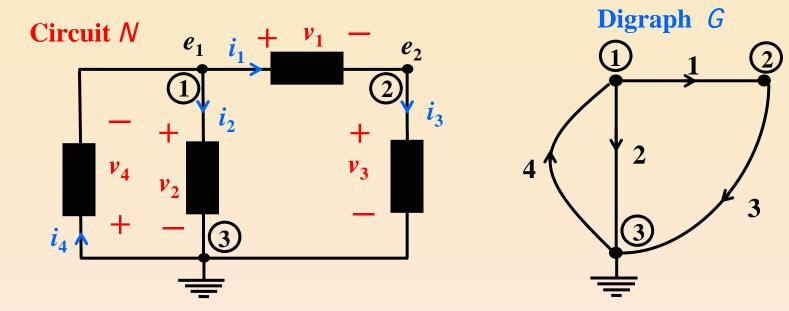
KVL solution 2:
$$\hat{v}_1 = 2V, \hat{v}_2 = 4V, \hat{v}_3 = 2V, \hat{v}_4 = -4V$$

There are infinitely many sets of branch currents (i_1, i_2, i_3, i_4) which satisfy KCL for G. 2 Examples satisfying KCL:

KCL solution 1:
$$i_1 = 3A$$
, $i_2 = 2A$, $i_3 = 3A$, $i_4 = 5A$

KCL solution 2:
$$\hat{i}_1 = 6A$$
, $\hat{i}_2 = -4A$, $\hat{i}_3 = 6A$, $\hat{i}_4 = 2A$

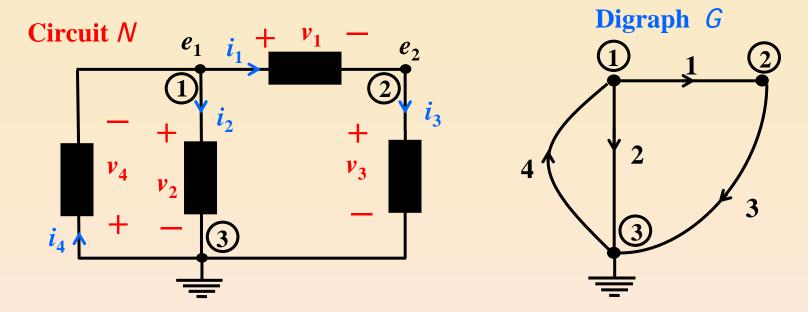
NOTE: So far we have not specified what circuit elements are used in this circuit. This explains why the voltage and current solutions are not unique.



KVL solution: Choose $v_1 = -3V, v_2 = 2V, v_3 = 5V, v_4 = -2V$

KCL solution: Choose $i_1 = 3 A$, $i_2 = 2 A$, $i_3 = 3 A$, $i_4 = 5 A$

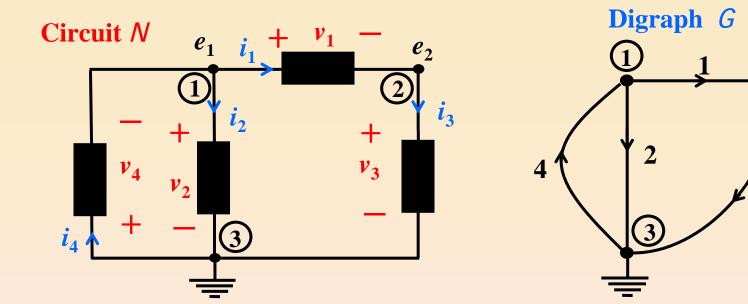
$$\sum_{j=1}^{4} v_j i_j = (-3)(3) + (2)(2) + (5)(3) + (-2)(5)$$
$$= -9 + 4 + 15 - 10$$
$$= 0$$



KVL solution: Choose $\hat{v}_1 = 2V$, $\hat{v}_2 = 4V$, $\hat{v}_3 = 2V$, $\hat{v}_4 = -4V$

KCL solution: Choose $i_1 = 3 A$, $i_2 = 2 A$, $i_3 = 3 A$, $i_4 = 5 A$

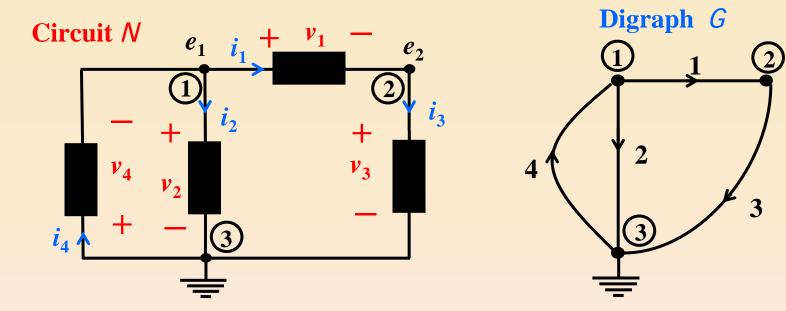
$$\sum_{j=1}^{4} \hat{v}_{j} i_{j} = (2)(3) + (4)(2) + (2)(3) + (-4)(5)$$
$$= 6 + 8 + 6 - 20$$
$$= 0$$



KVL solution: Choose $v_1 = -3V, v_2 = 2V, v_3 = 5V, v_4 = -2V$

KCL solution: Choose $\hat{i}_1 = 6A$, $\hat{i}_2 = -4A$, $\hat{i}_3 = 6A$, $\hat{i}_4 = 2A$

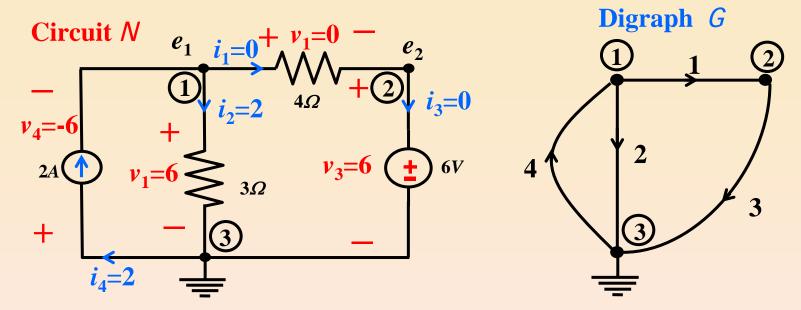
$$\sum_{j=1}^{4} v_j \, \hat{i}_j = (-3)(6) + (2)(-4) + (5)(6) + (-2)(2)$$
$$= -18 - 8 + 30 - 4$$
$$= 0$$



KVL solution: Choose $\hat{v}_1 = 2V$, $\hat{v}_2 = 4V$, $\hat{v}_3 = 2V$, $\hat{v}_4 = -4V$

KCL solution: Choose $\hat{i}_1 = 6A$, $\hat{i}_2 = -4A$, $\hat{i}_3 = 6A$, $\hat{i}_4 = 2A$

$$\sum_{j=1}^{4} \hat{v}_{j} \hat{i}_{j} = (2)(6) + (4)(-4) + (2)(6) + (-4)(2)$$
$$= 12 - 16 + 12 - 8$$
$$= 0$$



Solution:
$$e_1 = 6V, e_2 = 6V$$

 $v_1 = 0V, v_2 = 6V, v_3 = 6V, v_4 = -6V$
 $i_1 = 0A, i_2 = 2A, i_3 = 0A, i_4 = 2A$

Verifying the solution satisfying Tellegen's Theorem:

$$\sum_{j=1}^{4} v_j i_j = (v_1 i_1) + (v_2 i_2) + (v_3 i_3) + (v_4 i_4)$$

$$= (0)(0) + (6)(2) + (6)(0) + (-6)(2)$$

$$= 0 + 12 + 0 - 12$$

$$= 0$$

How to write An Independent System of KCL and KVL Equations

Let *N* be any connected circuit and let the **digraph** *G* associated with *N* contain "*n*" nodes and "*b*" branches. Choose an arbitrary datum node and define the associated **node-to-datum voltage** vector **e**, the **branch voltage vector V**, and the **branch current vector i**. Then we have the following system of **independent** KCL and KVL equations.

(n-1) Independent KCL Equations:

$$\mathbf{A} \mathbf{i} = \mathbf{0}$$

b Independent KVL Equations:

$$\mathbf{v} = \mathbf{A}' \mathbf{e}$$

Element Constitutive Relations

Element 1: Resistor

Described by Ohm's Law: $v_1 = 4 i_1$

Element 2: Resistor

Described by Ohm's Law: $v_2 = 3 i_2$

Element 3: Voltage source

Described by: $v_3 = 6$

Element 4: Current source

Described by: $i_4 = 2$

Rearranging these equations so that circuit variables appear on the left-hand side, we obtain

Observe we

have obtained Element

4 additional

independent Equations

equations.

 $v_1 - 4 i_1 = 0$

 $v_2 - 3 i_2 = 0$

 $v_3 = 6$

 $i_4 = 2$

Equations obtained from the element constitutive relations are guaranteed to be **independent** because different elements involved different circuit variables.

We can always recast **any** system of **linear** constitutive equations into the following standard matrix form

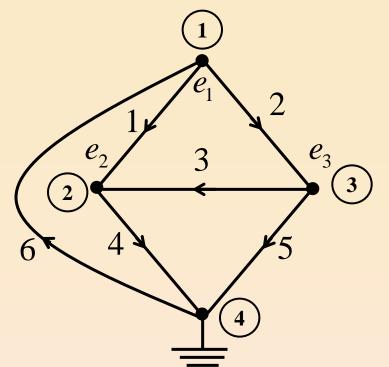
matrix form
$$\begin{bmatrix}
1 & 0 & 0 & 0 & | & -4 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 & | & 0 & -3 & 0 & 0 \\
0 & 0 & 1 & 0 & | & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & | & 0 & 0 & 0 & 1
\end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \\ v_3 \\ v_4 \\ \vdots \\ i_1 \\ i_2 \\ \vdots \\ i_3 \\ i_4 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 6 \\ 2 \end{bmatrix}$$

$$\mathbf{H}_{v}$$

$$\mathbf{H}_{i}$$

$$\begin{bmatrix} \mathbf{v} \\ \mathbf{v} \end{bmatrix} \text{ independent source vector}$$

 $\mathbf{H}_{v} \mathbf{v} + \mathbf{H}_{i} \mathbf{i} = \mathbf{v}$

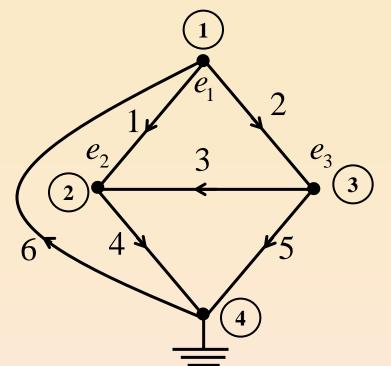


KCL Equations:

$$(2) -i_1 -i_3 +i_4 = 0$$

$$3 - i_2 + i_3 + i_5 = 0$$

$$\mathbf{A} \, \mathbf{i} = \mathbf{0} \implies \begin{array}{c} \text{node} & \text{Branch no.} \\ \text{no.} & 1 & 2 & 3 & 4 & 5 & 6 \\ \hline & \boxed{1} \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & -1 \\ -1 & 0 & -1 & 1 & 0 & 0 \\ 0 & -1 & 1 & 0 & 1 & 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}$$



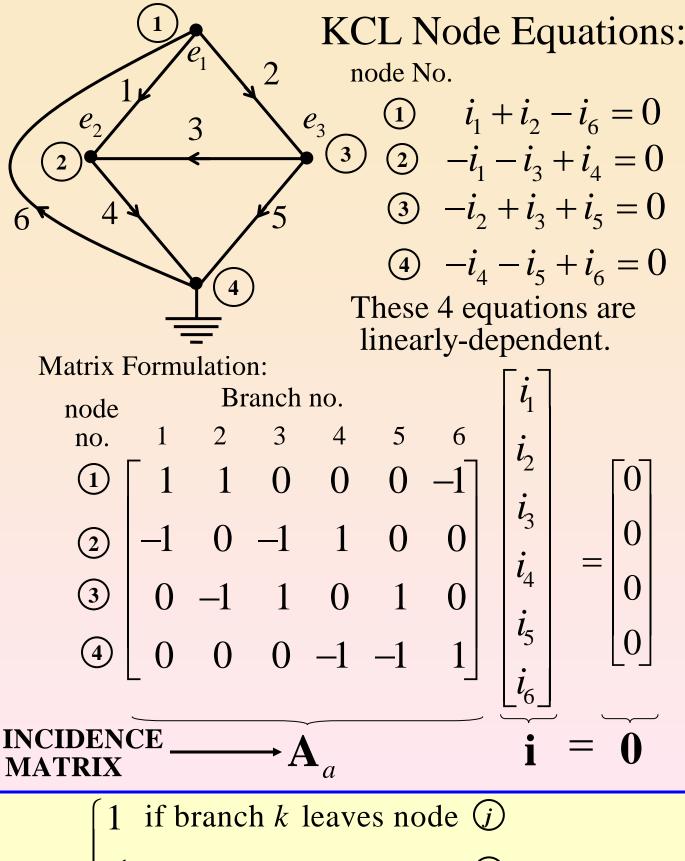
KCL Equations:

$$2 -i_1 - i_3 + i_4 = 0$$

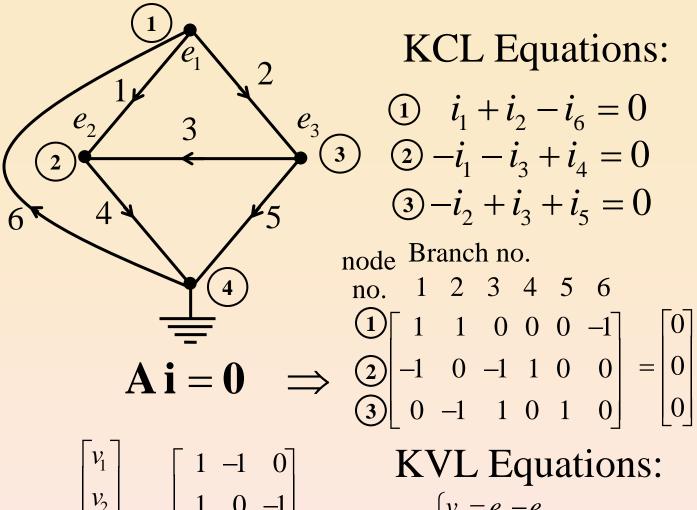
$$3 - i_2 + i_3 + i_5 = 0$$

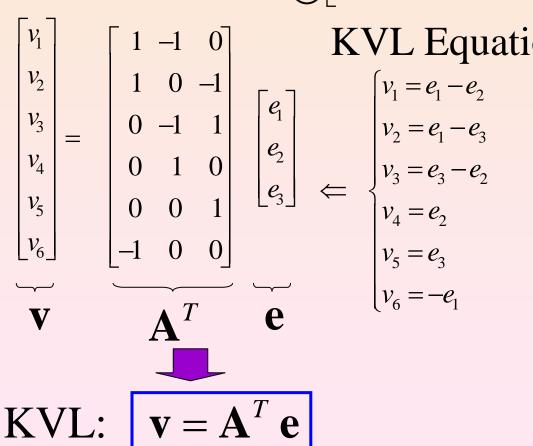
$$\mathbf{A} \, \mathbf{i} = \mathbf{0} \implies \mathbf{0} \quad \Rightarrow \begin{array}{c} \text{node} \quad \text{Branch no.} \\ \text{no.} \quad 1 \quad 2 \quad 3 \quad 4 \quad 5 \quad 6 \\ \hline 1 \quad 1 \quad 0 \quad 0 \quad 0 \quad -1 \\ -1 \quad 0 \quad -1 \quad 1 \quad 0 \quad 0 \\ \hline 0 \quad -1 \quad 1 \quad 0 \quad 1 \quad 0 \\ \hline \mathbf{A} \qquad \qquad \mathbf{i} \\ \hline \end{array} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ \end{bmatrix}$$

A is called the reduced Incidence Matrix of the diagraph G relative to datum node (4).



 $a_{jk} = \begin{cases} -1 & \text{if branch } k \text{ leaves node } \text{(j)} \\ 0 & \text{if branch } k \text{ is not connected to node } \text{(j)} \end{cases}$





Since v_j is present only in the *j*th equation, these k equations are **linearly** - **independent**.

Theorem

$$Ai = 0$$

gives the maximum possible number of linearly-independent KCL equations for a connected circuit.

Reduced Incidence Matrix

Let G be a connected digraph with "n" nodes and "b" branches. Let \mathbf{A}_a be the Incidence Matrix of G. The $(n-1) \times b$ matrix \mathbf{A} obtained by deleting any one row of \mathbf{A}_a is called a Reduced-Incidence Matrix of G.

Observation: The 4 KCL node equations are *not* linearly independent.

Adding the left side of the 4 KCL node equations, we obtain:

$$\underbrace{(i_{1}+i_{2}-i_{6})}_{(1)} + \underbrace{(-i_{1}-i_{3}+i_{4})}_{(2)} + \underbrace{(-i_{2}+i_{3}+i_{5})}_{(3)} + \underbrace{(-i_{4}-i_{5}+i_{6})}_{(4)} \equiv 0$$

This means we can derive any one of these 4 equations from the other 3.

Example: Derive KCL equations at node 4:

Adding the first 3 node equations gives:

$$\underbrace{(i_{1}+i_{2}-i_{6})}_{\text{1}} + \underbrace{(-i_{1}-i_{3}+i_{4})}_{\text{2}} + \underbrace{(-i_{2}+i_{3}+i_{5})}_{\text{3}}$$

$$= \underbrace{i_{4}+i_{5}-i_{6}}_{\text{4}}$$

Reduced Incidence Matrix A

Let G be a connected **digraph** with "n" nodes and "b" branches, the **reduced incidence matrix A** relative to **datum node** ⓐ is an $(n-1) \times b$ matrix whose coefficients a_{jk} are obtained from the (n-1) KCL equations written at the n-1 non-datum nodes:

By applying the various versions of KCL, we can write many different KCL equations for each circuit. However, these equations are usually **not** linearly independent in the sense that each equation can be derived by a linear combination of the others.

How can we write a maximum set of linearly-independent KCL equations?

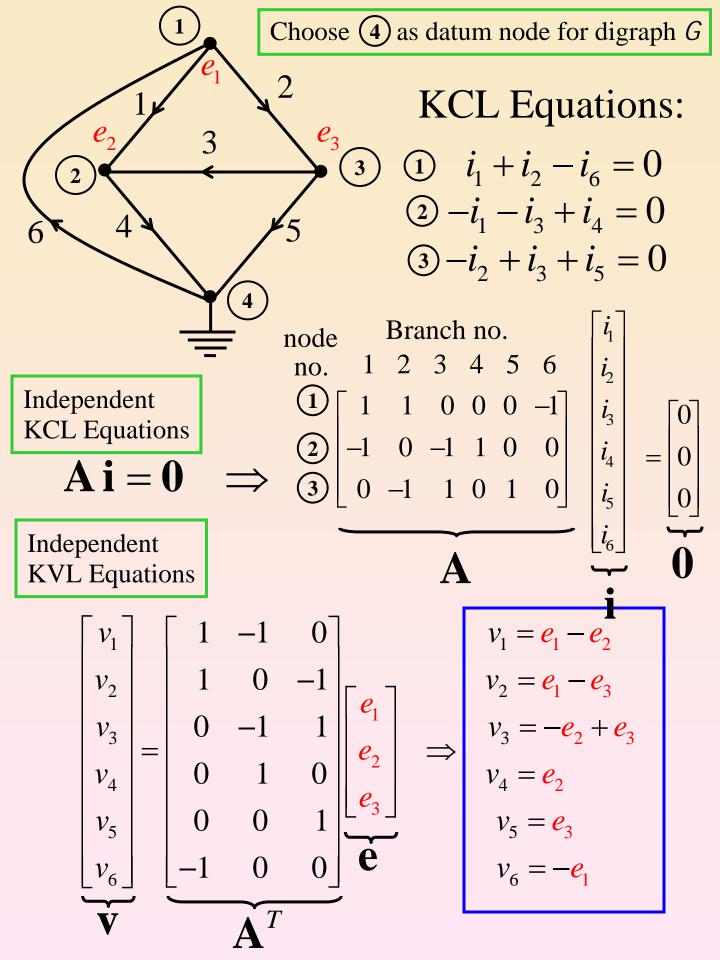
Simplest Method to write linearly-Independent KCL Equations.

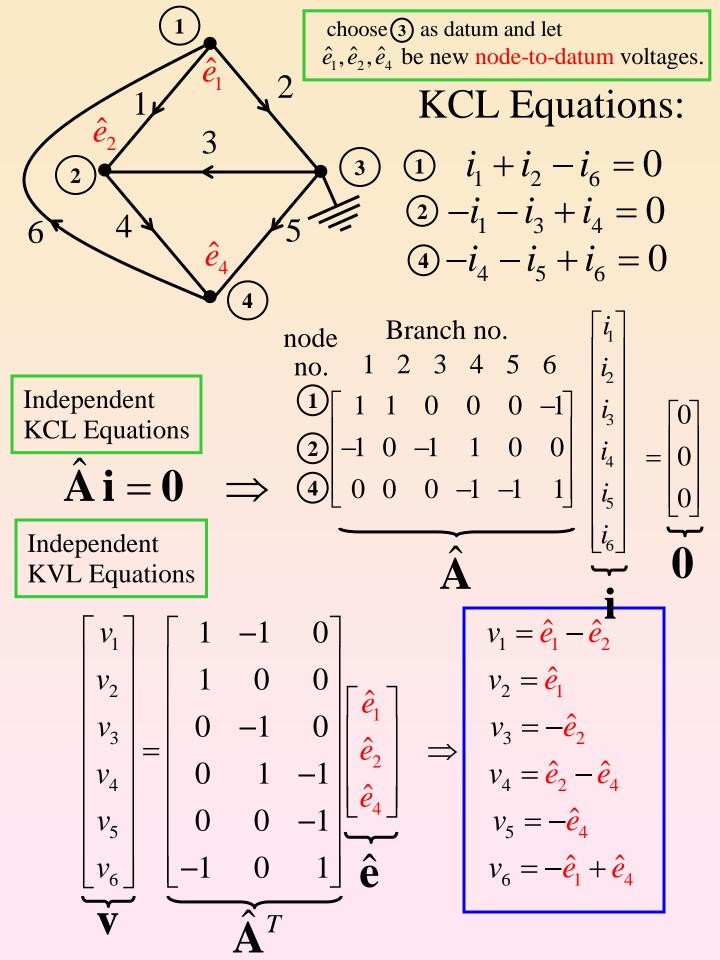
Given a connected circuit with "n" nodes, choose an arbitrary node as **datum**. Write a KCL equation at each of the remaining (n-1) nodes.

Relationship between A and A_a

Let A_a be the $n \times b$ Incidence matrix of a connected digraph G with "n" nodes and "b" branches.

By deleting any row corresponding to node m from \mathbf{A}_a , we obtain the **reduced incidence matrix** \mathbf{A} of G relative to the datum node m.





We can always recast **any** system of **linear** constitutive equations into the following standard matrix form

matrix form
$$\begin{bmatrix}
1 & 0 & 0 & 0 & | & -4 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 & | & 0 & -3 & 0 & 0 \\
0 & 0 & 1 & 0 & | & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & | & 0 & 0 & 0 & 1
\end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \\ v_3 \\ v_4 \\ i_1 \\ i_2 \\ \vdots \\ i_3 \\ i_4 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 6 \\ 2 \end{bmatrix}$$

$$\mathbf{H}_{v}$$

$$\mathbf{H}_{i}$$

$$\begin{bmatrix} \mathbf{v} \\ \mathbf{v} \end{bmatrix} \text{ independent source vector}$$

 $\mathbf{H}_{v} \mathbf{v} + \mathbf{H}_{i} \mathbf{i} = \mathbf{v}$

$$\begin{aligned} & \text{KCL} \begin{cases} i_1 + i_2 - i_4 = 0 \\ -i_1 + i_3 &= 0 \end{cases} & (1) \\ v_1 = e_1 - e_2 & (3) \\ v_2 = e_1 & (4) \\ v_3 = e_2 & (5) \\ v_4 = -e_1 & (6) \end{cases} & \text{independent} \\ & \text{linear} \\ & \text{equations} \\ & \text{involving} \end{cases} \\ & \text{Element} \\ & \text{Constitive} \\ & \text{Relation} \end{cases} & v_1 = 4 \ i_1 & (7) \\ & v_2 = 3 \ i_2 & (8) \\ & v_3 = 6 & (9) \\ & i_4 = 2 & (10) \end{aligned}$$

We can always find the solution using **Cramer's rule**.

For simple circuits, we can often find the solution by as hoc elimination and substitution of variables:

EXAMPLE: (5) and (9)
$$\Rightarrow e_2 = 6$$
 (11)

(1) and (10)
$$\Rightarrow i_1 + i_2 = 2$$
 (12)

(3), (7) and (11)
$$\Rightarrow i_1 = \frac{1}{4}(e_1 - 6)$$
 (13)

(1) and (10)
$$\Rightarrow i_1 + i_2 = 2$$
 (12)
(3), (7) and (11) $\Rightarrow i_1 = \frac{1}{4}(e_1 - 6)$ (13)
(4) and (8) $\Rightarrow i_2 = \frac{1}{3}e_1$ (14)

Substituting (10), (11), (12), and (13) into (1), we obtain

$$\frac{1}{4}(e_1 - 6) + \frac{1}{3}e_1 - 2 = 0$$

$$\Rightarrow e_1 = 6$$
(15)

Complete Solution:
$$e_1 = 6V, e_2 = 6V$$

 $v_1 = 0V, v_2 = 6V, v_3 = 6V, v_4 = -6V$
 $i_1 = 0A, i_2 = 2A, i_3 = 0A, i_4 = 2A$

Verification of solution via Tellegen's Theorem

$$\sum_{j=1}^{7} v_j i_j = (v_1 i_1) + (v_2 i_2) + (v_3 i_3) + (v_4 i_4)$$

$$= (0)(0) + (6)(2) + (6)(0) + (-6)(2) = 0$$

Tellegen's Theorem

Let G be a diagraph with "b" branches.

Let (v_1, v_2, \dots, v_b) be any set of b voltages of G which satisfy KVL.

Let (i_1, i_2, \dots, i_b) be any set of b currents of b which satisfy KCL.

Then

$$\sum_{j=1}^{b} v_j \ i_j = 0$$

Proof: suppose

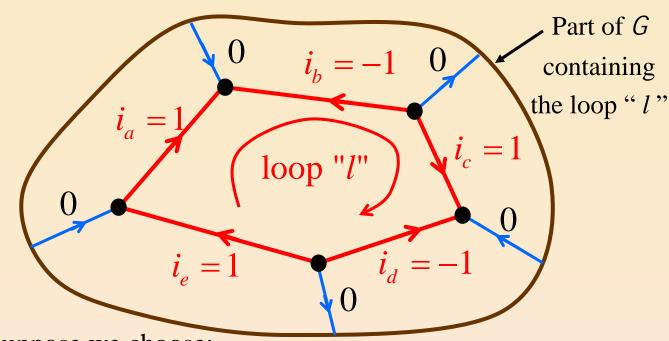
terminal.

$$\mathbf{v} = \begin{bmatrix} v_1 \\ v_2 \\ \vdots \\ v_b \end{bmatrix} \text{ satisfies KVL }, \quad \mathbf{i} = \begin{bmatrix} i_1 \\ i_2 \\ \vdots \\ i_b \end{bmatrix} \text{ satisfies KCL }$$

Then
$$\sum_{j=1}^{b} v_{j} i_{j} = \mathbf{v}^{T} \mathbf{i} = \underbrace{\left(\mathbf{A}^{T} \mathbf{e}\right)^{T}}_{\mathbf{KVL}} \mathbf{i} = \mathbf{e}^{T} \underbrace{\left(\mathbf{A} \mathbf{i}\right)}_{\mathbf{KCL}} = 0$$

where $\mathbf{e} = [e_1, e_2, \dots, e_{n-1}]^T$ is any node-to-datum voltage.

Warning: By definition of a diagraph, each branch voltage v_i and branch current i_j associated with branch j must follow the Associated reference convention: $i_j \text{ flows from the positive terminal to the negative}$



Suppose we choose:

This choice of
$$\{i_1, i_2, ..., i_b\}$$
 Satisfies KCL

$$i_j = 0$$
, if i_j is **not** in loop " l "

=1, if
$$i_j$$
 is in loop " l " and flows in the same direction as loop " l "

$$=-1$$
, if i_j is in loop " l " and flows in opposite direction as loop " l "

$$\therefore \sum_{j=1}^{b} v_j i_j = 0$$
 (because v_j chosen earlier satisfies Tellegen's theorem)

$$0 = \sum_{j=1}^{b} v_j \, i_j = \sum_{\substack{b_j \text{ belonging} \\ \text{to loop "}l"}} + \sum_{\substack{b_j \text{ not belonging} \\ \text{to loop "}l"}} b_j \text{ not belonging}$$

equals 0 because $i_j = 0$

$$\Rightarrow \sum_{b_j \text{ belonging to loop "}l"} v_j i_j = 0 \Rightarrow \text{KVL} \quad \blacksquare$$

Relationship Between

Kirchhoff's Laws and Tellegen's Theorem

1. KCL and KVL — Tellegen's Theorem

2. KVL and Tellegen's Theorem KCL

3. Tellegen's Theorem and KCL **KVL**

KVL and **Tellegen's Theorem KCL**

Proof.

Let **v** satisfy KVL for *G*:

$$\mathbf{v} = \mathbf{A}^T \mathbf{e} \tag{1}$$

Let **v** and **i** satisfy Tellegen's Theorem:

$$\mathbf{v}^T \mathbf{i} = 0 \tag{2}$$

Substitute (1) for **v** in (2):

$$\left(\mathbf{A}^T \mathbf{e}\right)^T \mathbf{i} = 0 \tag{3}$$

$$\mathbf{e}^{T}\left(\mathbf{A}\,\mathbf{i}\right) = 0\tag{4}$$

Since (4) is true for any node-to-datum voltages $\mathbf{e} \neq \mathbf{0}$,

(4) can be true only if

$$(Ai) = 0 \implies KCL$$

Proof.

Let G be any connected digraph with b branches $\{1, 2, ..., b\}$.

Let $\{i_1, i_2, ..., i_b\}$ be any set of branch currents satisfying KCL.

Choose *any* subset $\{b_a, b_b, ..., b_n\}$ of the *b* branches which form a closed loop "l". Let $\{v_1, v_2, ..., v_b\}$ be *any* set of branch voltages which, together with $\{i_1, i_2, ..., i_b\}$ satisfy Tellegen's Theorem.

Our goal is to prove that the subset of these voltages which belong to the above closed loop "l" must satisfy KVL around the loop.

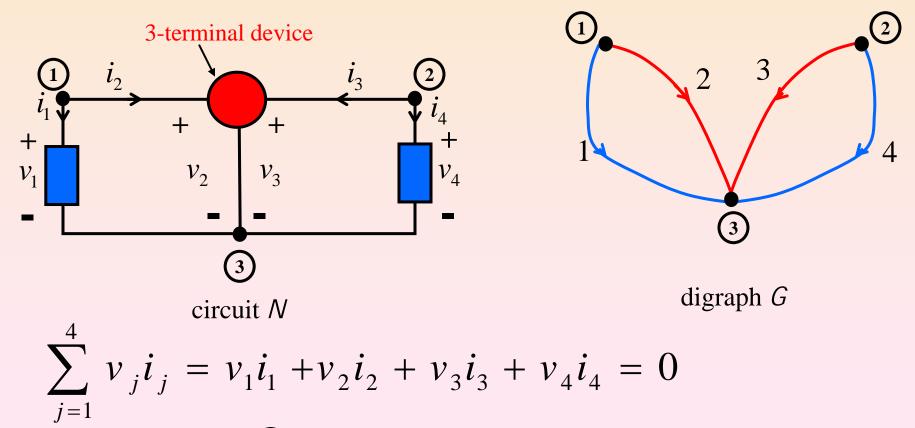
Applying Tellegen's Theorem to Circuits Containing (n+1)-terminal devices

Let N be any circuit containing (n+1)-terminal devices.

- Step 1. Assign a datum to each device. Assign "n" terminal-to-datum voltages for each (n+1)-terminal device, following associated reference convention.
- Step 2. Draw the digraph G of N.
- Step 3. Apply Tellegen's theorem to G.

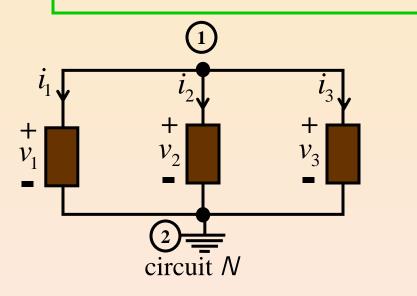
Remarks

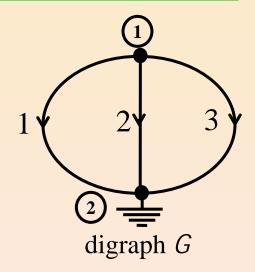
Tellegen's theorem can be applied directly to a circuit provided we use Associated Reference convention for all device terminal currents and voltages.



(choose 3) as datum node for the 3-terminal device)

Voltage and Current Solutions are Orthogonal!

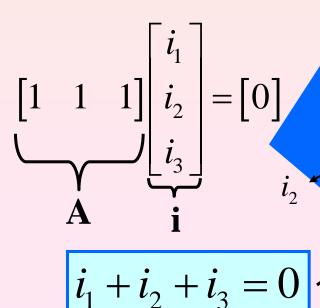


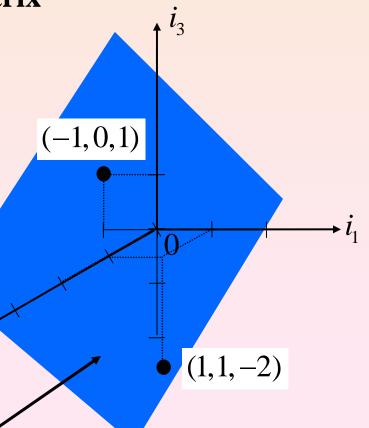


Reduced Incidence Matrix

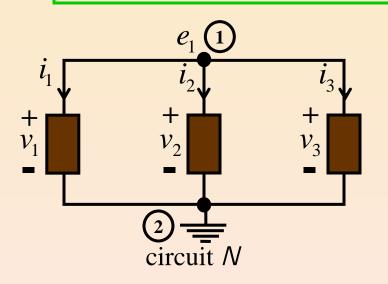
$$\mathbf{A} = \begin{bmatrix} 1 & 1 & 1 \end{bmatrix}$$

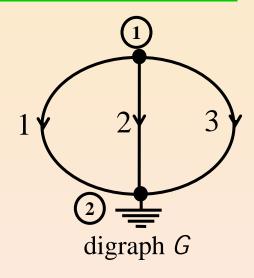
$$KCL : Ai = 0$$





Voltage and Current Solutions are Orthogonal!





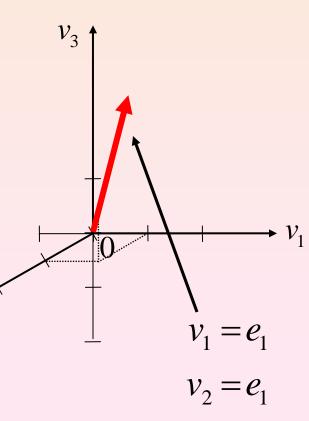
Reduced Incidence Matrix

$$\mathbf{A} = \begin{bmatrix} 1 & 1 & 1 \end{bmatrix}$$

$$KVL : \mathbf{v} = \mathbf{A}^T \mathbf{e}$$

$$\begin{bmatrix} v_1 \\ v_2 \\ v_3 \end{bmatrix} = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix} \begin{bmatrix} \mathbf{e}_1 \\ \mathbf{e} \end{bmatrix}$$

$$\mathbf{v} \quad \mathbf{\Delta}^T$$



Geometrical Interpretation of Tellegen's Theorem

$$KCL: i_1 + i_2 + i_3 = 0$$

$$KVL : v_1 = v_2 = v_3$$

$$\sum_{j=1}^{3} v_{j} i_{j} = v_{1} i_{1} + v_{2} i_{2} + v_{3} i_{3}$$

$$= e_{1} i_{1} + e_{1} i_{2} + e_{1} i_{3}$$

$$= e_{1} (i_{1} + i_{2} + i_{3}) = 0$$

All voltage solutions (v_1, v_2, v_3) falling on this line satisfy KVL.

$$i_1 + i_2 + i_3 = 0$$

All current solutions (i_1, i_2, i_3) falling on this plane satisfy KVL.

 i_2, v_2

