### EE100Su08 Lecture #5 (July 2<sup>nd</sup> 2008)

- Outline
  - Questions?
  - Lab notes:
    - Labs 2 and 3 have been shortened
    - Monday lab: go to your SECOND lab section next week.
  - Node-Voltage Analysis: wrap up
  - Mesh analysis: read it, OPTIONAL
  - Superposition
  - Thevenin's Theorem



# Nodal Analysis w/ "Floating Voltage Source"

A "floating" voltage source is one for which neither side is connected to the reference node, e.g.  $V_{LL}$  in the circuit below:



<u>Problem</u>: We cannot write KCL at nodes a or b because there is no way to express the current through the voltage source in terms of  $V_a$ - $V_b$ .

<u>Solution</u>: Define a "supernode" – that chunk of the circuit containing nodes a <u>and</u> b. Express KCL for this supernode. Incorporate voltage source constraint into KCL equation.

# **Nodal Analysis: Example #3**



# **Superposition**

A *linear circuit* is one constructed only of linear elements (linear resistors, and linear capacitors and inductors, linear dependent sources) and independent sources. Linear means I-V charcteristic of elements/sources are straight lines when plotted

# Principle of Superposition:

 In any linear circuit containing multiple independent sources, the current or voltage at any point in the network may be calculated as the algebraic sum of the individual contributions of each source acting alone.

# Superposition

#### Procedure:

- 1. Determine contribution due to one independent source
  - Set all other sources to 0: Replace independent voltage source by short circuit, independent current source by open circuit
- 2. Repeat for each independent source
- 3. Sum individual contributions to obtain desired voltage or current

#### **Open Circuit and Short Circuit**

- Open circuit  $\rightarrow$  i=0 ; <u>Cut off</u> the branch
- Short circuit  $\rightarrow$  v=0 ; replace the element by <u>wire</u>
- <u>Turn off</u> an independent <u>voltage</u> source means
  - V=0
  - Replace by wire
  - Short circuit
- <u>Turn off</u> an independent <u>current</u> source means
  - i=0
  - Cut off the branch
  - open circuit

#### **Superposition Example**





Superposition
 example
 (intd.)

 
$$V_0 = V_{01} + V_{02} + V_{03}$$
 $= 1b - \frac{1b}{3} - \frac{8}{3} = 1b - \frac{24}{3} = \frac{8}{3} \vee \frac{1}{3}$ 
 $= 1b - \frac{1b}{3} - \frac{8}{3} = 1b - \frac{24}{3} = \frac{8}{3} \vee \frac{1}{3}$ 

# **Equivalent Circuit Concept**

 A network of voltage sources, current sources, and resistors can be replaced by an *equivalent circuit* which has identical terminal properties (*I-V* characteristics) without affecting the operation of the rest of the circuit.



# **Thévenin Equivalent Circuit**

 Any\* *linear* 2-terminal (1-port) network of indep. voltage sources, indep. current sources, and linear resistors can be replaced by an equivalent circuit consisting of an independent voltage source in series with a resistor without affecting the operation of the rest of the circuit.



### **I-V Characteristic of Thévenin Equivalent**

• The *I-V* characteristic for the series combination of elements is obtained by adding their voltage drops:



Intritive ideas behind therening theorem Reg= 3 r. We dont know what is (arc) actual resistances inside the box! the  $R = 6 \| bn, 2t | n, 0.5 + 2.5 n \dots$ Cp. EE100 Summer 2008 Slide 14 Bharathwaj Muthuswamy







# **Thévenin Equivalent Example**

Find the Thevenin equivalent with respect to the terminals a,b:



**Thévenin Equivalent Example (contd.)** =) 12-Vx+9b= 2Vx , Voc= Vx+1L= 52V =  $V_{x} = \frac{108}{3} = \frac{36V}{5}$ Direction of Isc is foom a to b, because. 8 A 2Ω 12 Ω  $\Lambda \Lambda \Lambda$ Isc 260 12 V EE100 Summer 2008 Slide 19 Bharathwaj Muthuswamy

#### Aside: Prelab 1, question 3



### Thévenin Equivalent Example (contd.)





# Thévenin Equivalent Example (contd.)



