## EE100Su08 Lecture \#5 (July 2 ${ }^{\text {nd }}$ 2008)

- Outline
- Questions?
- Lab notes:
- Labs 2 and 3 have been shortened
- Monday lab: go to your SECOND lab section next week.
- Node-Voltage Analysis: wrap up
- Mesh analysis: read it, OPTIONAL
- Superposition
- Thevenin's Theorem

Nodal Analysis: Example \#2


## Nodal Analysis w/ "Floating Voltage Source"

A "floating" voltage source is one for which neither side is connected to the reference node, e.g. $\mathrm{V}_{\mathrm{LL}}$ in the circuit below:


Problem: We cannot write $K C L$ at nodes $a$ or $b$ because there is no way to express the current through the voltage source in terms of $\mathrm{V}_{\mathrm{a}}{ }^{-}$ $V_{b}$.

Solution: Define a "supernode" - that chunk of the circuit containing nodes a and b. Express KCL for this supernode. Incorporate voltage source constraint into KCL equation.

Nodal Analysis: Example \#3


Eq'n 1: KCL at supernode

$$
I_{1}+I_{2}=i_{3}+i_{4} \Rightarrow I_{1}+I_{2}=\frac{V_{a}}{R_{2}}+\frac{V_{b}}{R_{4}}
$$

Substitute property of voltage source: $V_{b}-V_{a}=V_{L}$

## Superposition

A linear circuit is one constructed only of linear elements (linear resistors, and linear capacitors and inductors, linear dependent sources) and independent sources. Linear
means I-V charcteristic of elements/sources are straight lines when plotted

## Principle of Superposition:

- In any linear circuit containing multiple independent sources, the current or voltage at any point in the network may be calculated as the algebraic sum of the individual contributions of each source acting alone.


## Superposition

## Procedure:

1. Determine contribution due to one independent source

- Set all other sources to 0: Replace independent voltage source by short circuit, independent current source by open circuit

2. Repeat for each independent source
3. Sum individual contributions to obtain desired voltage or current

## Open Circuit and Short Circuit

- Open circuit $\rightarrow \mathrm{i}=0$; Cut off the branch
- Short circuit $\rightarrow \mathrm{v}=0$; replace the element by wire
- Turn off an independent voltage source means
- V=0
- Replace by wire
- Short circuit
- Turn off an independent current source means
$-\mathrm{i}=0$
- Cut off the branch
- open circuit

Superposition Example


Suberbosition example (ontd.)



## Equivalent Circuit Concept

- A network of voltage sources, current sources, and resistors can be replaced by an equivalent circuit which has identical terminal properties (I-V characteristics) without affecting the operation of the rest of the circuit.


$$
i_{A}\left(v_{\mathrm{A}}\right)=i_{\mathrm{B}}\left(v_{\mathrm{B}}\right)
$$

## Thévenin Equivalent Circuit

- Any* linear 2-terminal (1-port) network of indep. voltage sources, indep. current sources, and linear resistors can be replaced by an equivalent circuit consisting of an independent voltage source in series with a resistor without affecting the operation of the rest of the circuit.



## I-V Characteristic of Thévenin Equivalent

- The I-V characteristic for the series combination of elements is obtained by adding their voltage drops:

For a given current $\boldsymbol{i}$, the voltage drop $v_{\mathrm{ab}}$ is equal to the sum of the voltages dropped across the source ( $\boldsymbol{V}_{\text {Th }}$ ) and across the resistor ( $\mathbf{i R}_{\mathbf{T h}}$ )

$I-V$ characteristic of voltage source: $v=V_{\text {Th }}$

Intritive ideas bchind Thevenirs theovem


$R_{\text {eq }}=3 \Omega$. We dond know what is (are) the actod resistence (rsistances) inside the box! еб: $\quad R_{e_{q}}=6 \| b_{\Omega}, 2+1 \Omega, 0.5+2.5 \Omega \cdots \cdot$

Jntiikive idees becrind Theverins theorem [condi.)


Intritive ideas bchind Thevenins theorem [contd.]
$N_{\text {ow }} I$ have $V_{\text {oc }} \& I_{S C} \Rightarrow R_{\text {meverin }}=R_{m} \triangleq-\frac{N_{0 c}}{I_{S C}} \triangleq \frac{1}{\delta T_{\text {pe }}}$




Thévenin Equivalent Example
Find the Thevenin equivalent with respect to the terminals $\mathrm{a}, \mathrm{b}$ :


Goal. Find Voc $\& I_{s c}$
Step 1: Findiy $V_{o c}$. $\quad V_{x}=V_{o c}-16$
Node egn e $x^{\prime} \cdot i_{1}+8 A=i_{2} \Rightarrow \frac{12-V_{x}}{12}+8=\frac{V_{x}}{6}$

Thévenin Equivalent Example (contd.)


Aside: Prelab 1, question 3

(Q.) Crove: $R=\frac{V_{R} R_{m}}{V_{\text {rest }}-V_{R}}$
$K$ vu: $V_{\text {ret }}+V_{m}-v_{R}=0$
$\begin{array}{ll}\left.\underbrace{+V_{m-}}_{R_{m}}\right|^{i} \mid+Y^{\prime} i & \Rightarrow V_{\text {rest }}+\left(-i R_{m}\right)-V_{R}=0 \\ V_{m}=-i R_{m} \mid V_{R}=i R & \therefore i=\frac{V_{\text {telt }}-V_{R}}{R_{m}} \\ \frac{\therefore R=V_{R / i}}{\text { EE100 Summer 2008 }}\end{array}$

Thévenin Equivalent Example (contd.)


$$
\left.\begin{array}{rl}
I_{s c} & =9 \mathrm{~A}(? ?) \\
v_{z} & =\left(\frac{\frac{3}{2}}{3 / 2+12}\right) 12 v
\end{array}\right)=\frac{36}{27}, ~ \$ 4 / 3 \mathrm{~V} .
$$



Thévenin Equivalent Example (contd.)


$$
\begin{aligned}
V_{z} & =\frac{4}{3} V \\
I & =\frac{V_{z}}{2}=\frac{2}{3} \mathrm{~A} \\
J_{S C} & =8+2 / 3=26 / 3 \mathrm{~A}
\end{aligned}
$$

$$
R_{r_{n}}=\frac{V_{0 c}}{I_{r c}}=\frac{52}{26 / 3}=\frac{52.3}{26}=6 \Omega
$$

## Thévenin Equivalent Example (contd.)


$R_{\mathrm{Th}}$ Calculation Example \#1


Set all independent sources to 0 :


