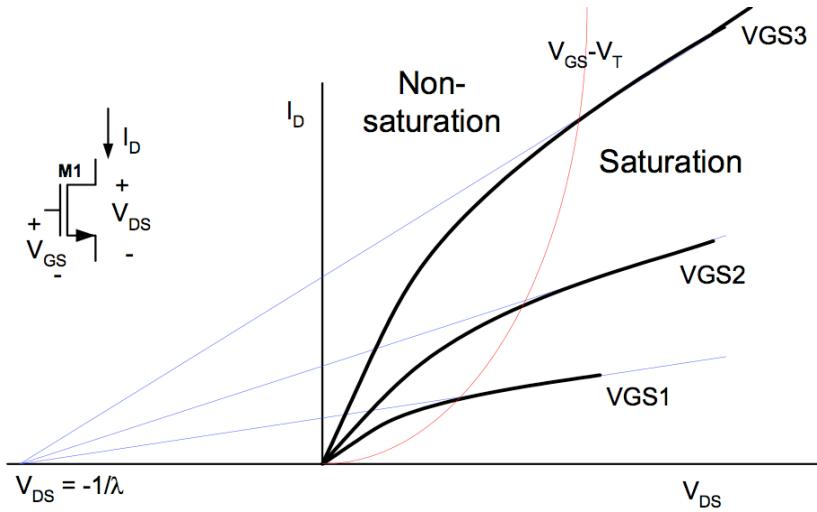


# 1. MOSFET Equations

## a) N-channel MOSFET

Cut Off	$V_{GS} \leq V_T$	$I_{DS} = 0$
Linear	$V_{GS} > V_T, V_{DS} \leq V_{GS} - V_T$	$I_{DS} = \mu_n C_{ox} \frac{W}{L} \left[ (V_{GS} - V_T)V_{DS} - \frac{V_{DS}^2}{2} \right] (1 + \lambda V_{DS})$
Saturation	$V_{GS} > V_T, V_{DS} > V_{GS} - V_T$	$I_{DS} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_T)^2 (1 + \lambda V_{DS})$

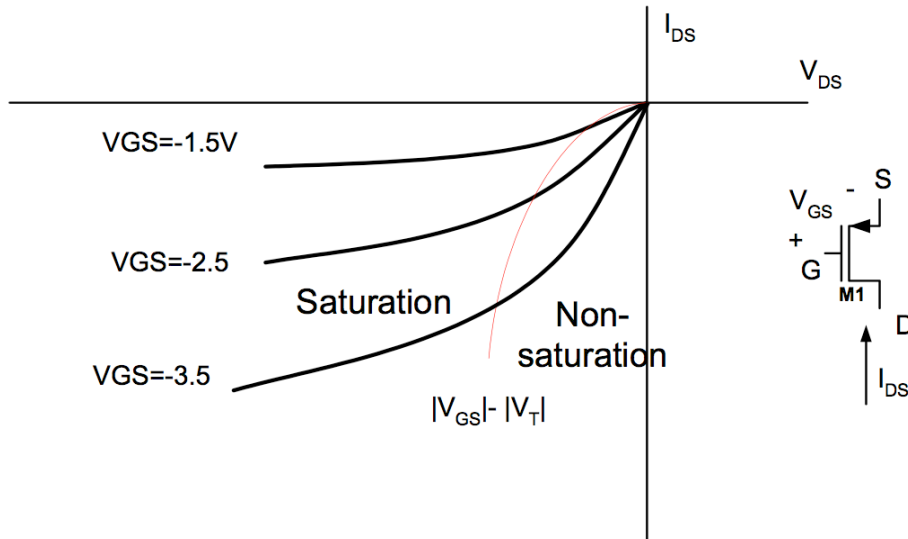


The simplest model in SPICE (Level 1 or default model) uses the above equations.

Parameter	SPICE Parameter	Units	Typical Values
$\mu_n C_{ox}$	KP	A/V <sup>2</sup>	200μ
$V_{T0}$	VTO	V	0.5 – 1.0
$\lambda$	LAMBDA	V <sup>-1</sup>	0.05 – 0.005

## b) P-channel MOSFET

Cut Off	$V_{SG} \leq  V_T $	$I_{SD} = 0$
Linear	$V_{SG} >  V_T , V_{SD} \leq V_{SG} -  V_T $	$I_{SD} = \mu_p C_{ox} \frac{W}{L} \left[ (V_{SG} -  V_T )V_{SD} - \frac{V_{SD}^2}{2} \right] (1 + \lambda V_{SD})$
Saturation	$V_{SG} >  V_T , V_{SD} > V_{SG} -  V_T $	$I_{SD} = \frac{1}{2} \mu_p C_{ox} \frac{W}{L} (V_{SG} -  V_T )^2 (1 + \lambda V_{SD})$



Example)

$$V_S = 4 \text{ V}, V_G = 2 \text{ V}, V_D = 1 \text{ V}$$

$$V_T = -0.8 \text{ V}, \lambda = 0, K_p = 100 \mu\text{A}/\text{V}^2$$

$$W = 10 \mu\text{m}, L = 2 \mu\text{m}$$

Find MOSFET type, operation region,  $I_{DS}$ .

- Solution

$$|V_{DS}| > |V_{GS}| - |V_T| \Rightarrow \text{saturation}$$

$$I_{SD} = \frac{100\mu}{2} \frac{10\mu}{2\mu} (2 - |-0.8|)^2 (1 + 0) = 360\mu\text{A}$$

$$I_{DS} = -360\mu\text{A}$$

## 2. MOSFET Circuits

Example) The PMOS transistor has  $V_T = -2 \text{ V}$ ,  $K_p = 8 \mu\text{A}/\text{V}^2$ ,

$$L = 10 \mu\text{m}, \lambda = 0.$$

Find the values required for  $W$  and  $R$  in order to establish a drain current of  $0.1 \text{ mA}$  and a voltage  $V_D$  of  $2 \text{ V}$ .

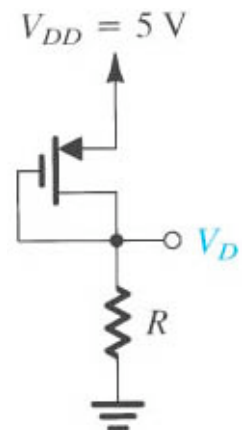
- Solution

$$V_D = V_G \Rightarrow V_{SD} > V_{SG} - |V_T| \Rightarrow \text{saturation}$$

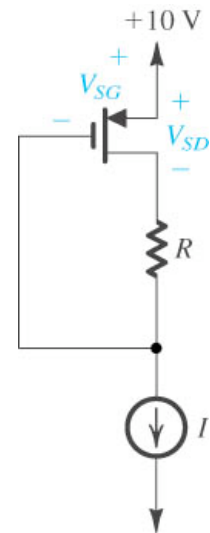
$$|I_{DS}| = \frac{1}{2} K_p \frac{W}{L} (V_{SG} - |V_T|)^2 (1 + \lambda V_{SD}) = \frac{8\mu}{2} \frac{W}{10\mu} (3 - 2)^2 (1 + 0) = 0.1\text{mA}$$

$$I_R = \frac{V_D}{R} = \frac{2}{R} = 0.1\text{mA}$$

$$W = 250\mu\text{m}, \quad R = 20\text{k}\Omega$$



Example) The PMOS transistor has  $V_T = -1$  V,  $K_p = 8 \mu\text{A}/\text{V}^2$ ,  $W/L = 25$ ,  $\lambda = 0$ .  
 For  $I = 100 \mu\text{A}$ , find the  $V_{SD}$  and  $V_{SG}$  for  $R = 0, 10\text{k}, 30\text{k}, 100\text{k}$ .



- Solution

$\lambda = 0$  (no channel length modulation)

1)  $R = 0$

$$V_D = V_G \Rightarrow V_{SD} > V_{SG} - |V_T| \Rightarrow \text{saturation}$$

$$I_{SD} = \frac{1}{2} K_p \frac{W}{L} (V_{SG} - |V_T|)^2 = \frac{8\mu}{2} \cdot 25 \cdot (V_{SG} - 1)^2 = 100\mu$$

$$V_{SG} = 2V \quad V_{SD} = 2V$$

2)  $R = 10\text{k}$

$$V_D - V_G = IR = 100\mu \cdot 10\text{k} = 1 \Rightarrow V_{SD} = V_{SG} - |V_T| \Rightarrow \text{saturation or linear}$$

$$I_{SD} = \frac{1}{2} K_p \frac{W}{L} (V_{SG} - |V_T|)^2 = \frac{8\mu}{2} \cdot 25 \cdot (V_{SG} - 1)^2 = 100\mu$$

$$V_{SG} = 2V \quad V_{SD} = 1V$$

3)  $R = 30\text{k}$

$$V_D - V_G = IR = 100\mu \cdot 30\text{k} = 3 \Rightarrow V_{SD} < V_{SG} - |V_T| \Rightarrow \text{linear}$$

$$I_{SD} = K_p \frac{W}{L} \left( (V_{SG} - |V_T|) V_{SD} - \frac{V_{SD}^2}{2} \right) = 8\mu \cdot 25 \cdot \left( (V_{SD} + 3 - 1) V_{SD} - \frac{V_{SD}^2}{2} \right) = 100\mu$$

$$V_{SD} \approx 0.24V \quad V_{SG} = 3.24V$$

4)  $R = 100\text{k}$

$$V_D - V_G = IR = 100\mu \cdot 100\text{k} = 10 \Rightarrow V_{SD} < V_{SG} - |V_T| \Rightarrow \text{linear}$$

$$I_{SD} = K_p \frac{W}{L} \left( (V_{SG} - |V_T|) V_{SD} - \frac{V_{SD}^2}{2} \right) = 8\mu \cdot 25 \cdot \left( (V_{SD} + 10 - 1) V_{SD} - \frac{V_{SD}^2}{2} \right) = 100\mu$$

$$V_{SD} \approx 0.06V \quad V_{SG} = 10.06V$$