

Lecture 14: MOSFETs II

- Announcements:
- HW#5 online and due Friday via Gradescope
- Lab#3 this week
 - ↳ Prelab due before lab next week
- Midterm 1 on Friday, Oct. 11
 - ↳ We have 7-9 p.m., 160 Kroeber Hall
- My Monday Office Hours will move to 5-6 p.m. on Oct. 14 and thereafter
- I will be traveling Friday, Oct. 4, and Monday, Oct. 7, so these lectures will be pre-recorded and put online

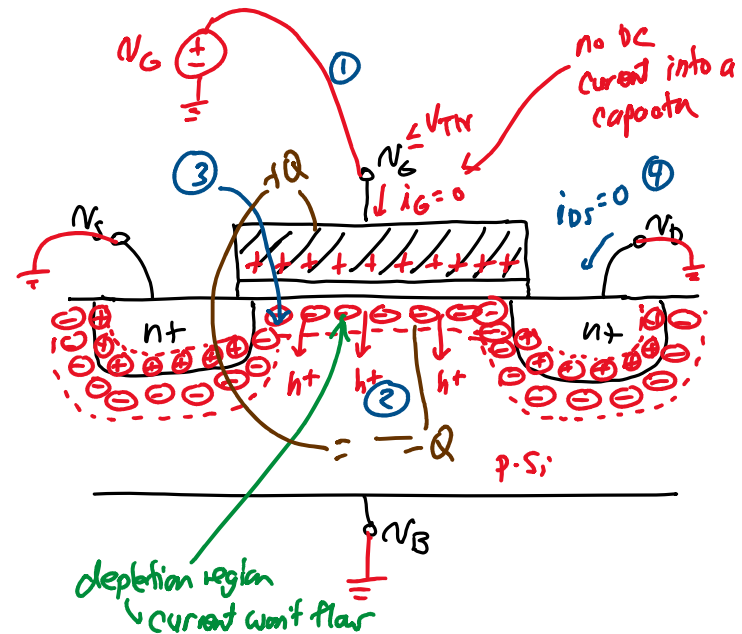
 • Lecture Topics:

- ↳ MOSFETs
 - Structure and Operation
 - Cutoff
 - Linear Region
 - Saturation

 • Last Time:

- Started linear region MOS analysis
- Now, continue with this ...

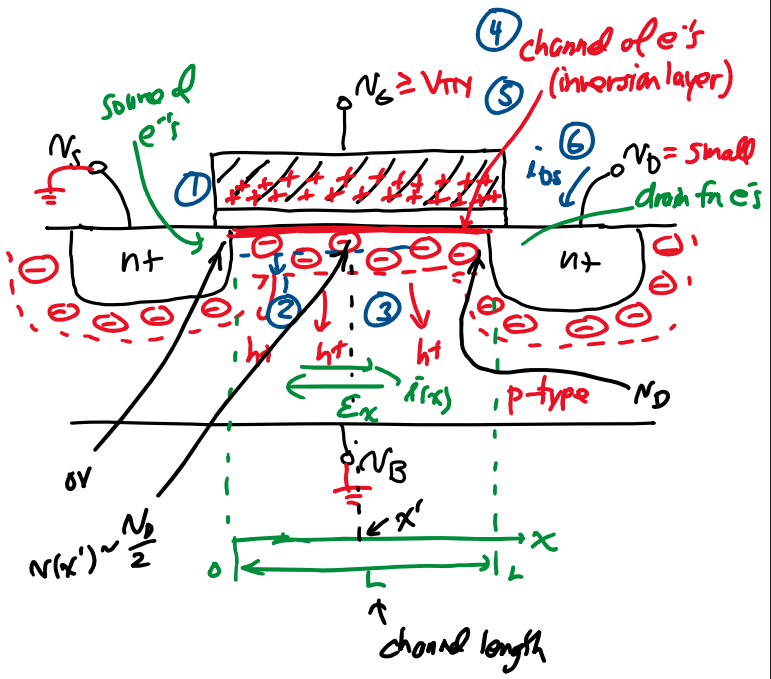
① Cutoff Region - ($V_{GS} \leq V_{TN}$)



- ① - Application of v_G puts (+) charge on the gate \rightarrow this induces (-) charge on the other side of the oxide
- ② - At this point, the easiest way to create (-) charge in the Si is for the h^+ in the p-type substrate to move away from the channel area
- ③ - i.e., a (-)ly charged depletion region forms in response to the initial (+) charge on the gate
- ④ - This is fixed charge that cannot conduct current $\rightarrow i_{DS} = 0$

- As v_G rises:
 - More (+) charge amasses on the gate
 - The depletion region of fixed (-) charge grows to accommodate
 - Soon, however the depletion region becomes large enough that it becomes easier to obtain (-) charge (to match the gate's (+) charge) by taking it from the S/D regions!
 - Result: a channel of e-'s forms between the S&D n+ regions \rightarrow inversion layer
 - This happens when $v_{GS} > V_{TN}$

② Linear Region: (or Triode Region)
 $(V_{GS} - V_{TN} \geq V_{DS} \geq 0) \rightarrow$ i.e., $V_{DS} = \text{small}$



- Channel of e-'s \rightarrow mobile \rightarrow silicon in this region now a conductor
- An E-field generated by v_{DS} gives rise to drift current flow

Devise how much current i_{DS} flows as a function of voltages V_{GS} & V_{DS} :

\Rightarrow the e- drift current at any point in the channel:

$$i(x) = Q'(x) v_n(x)$$

\uparrow e- charge per unit length \nwarrow velocity of e-'s $\left. \vphantom{\begin{matrix} \uparrow \\ \nwarrow \end{matrix}} \right\} = -\mu_n E_x$

Basically given by the charge on the gate-to-substrate capacitance: $q = CV$

$$Q'(x) = -W C_{ox}'' (V_{GS} - V_{TN} - V(x))$$

\uparrow e-'s width of channel \uparrow Voltage in the channel @ location $x \rightarrow$ @ D: it's V_{DS} @ S: it's $V_S = 0V$

$$C_{ox}'' = \frac{\epsilon_{ox}}{t_{ox}} \triangleq \text{oxide capacitance per unit area}$$

where $\epsilon_{ox} = \text{oxide permittivity} = 3.9 \epsilon_0$
 $t_{ox} = \text{oxide thickness [cm]}$

*
↓

$$i(x) = [-WC_{ox}''(V_{GS} - V_{TN} - N(x))] [-\mu_n E_x]$$

$$\left[E_x = -\frac{dN(x)}{dx} \right]$$

$$i(x) = -\mu_n C_{ox}'' W (V_{GS} - V_{TN} - N(x)) \frac{dN(x)}{dx}$$

$$i(x) dx = -\mu_n C_{ox}'' W (V_{GS} - V_{TN} - N(x)) dN(x)$$

$$\int_0^L i(x) dx = -\int_0^{N_{DS}} \mu_n C_{ox}'' W (V_{GS} - V_{TN} - N(x)) dN(x)$$

But $i_{DS} = -i(x)$

$$i_{DS} L = \mu_n C_{ox}'' W \left[(V_{GS} - V_{TN}) N_{DS} - \frac{N_{DS}^2}{2} \right]$$

$$i_{DS} = \mu_n C_{ox}'' \frac{W}{L} (V_{GS} - V_{TN} - \frac{N_{DS}}{2}) N_{DS}$$

(linear region)
↓
small N_{DS}
 $N_{DS} < (V_{GS} - V_{TN})$

Linear Region IV Characteristic

characteristic curves
look fairly linear
for small N_{DS}

⇒ can define an equivalent small-signal (small N_{DS}) linear resistance for an MOS Xistor in the linear region:

$$\frac{\partial i_{DS}}{\partial N_{DS}} = \mu_n C_{ox}'' \frac{W}{L} (V_{GS} - V_{TN} - N_{DS})$$

$$[N_{DS} = \text{small}] \Rightarrow \approx \mu_n C_{ox}'' \frac{W}{L} (V_{GS} - V_{TN})$$

$$R_{on} = \left[\frac{\partial i_{DS}}{\partial N_{DS}} \right]^{-1} = \frac{1}{\mu_n C_{ox}'' \frac{W}{L} (V_{GS} - V_{TN})}$$

↑
You'll need this for next lab!

Lab#3:

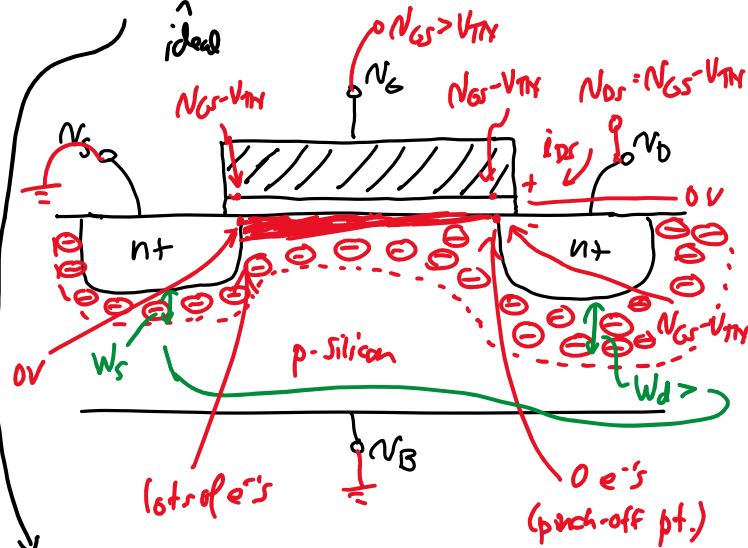
③ Saturation Region - ($V_{DS} \geq V_{GS} - V_{TN} \geq 0$)

As $V_{DS} \uparrow \rightarrow$ the voltage across the gate-to-substrate
 capacitor near the drain:

$$(N_{GS} - V_{TN} - V(x)) \rightarrow 0$$

\uparrow \uparrow
 N_D $\left\{ \begin{array}{l} \text{at drain edge} \\ \therefore \text{the inversion charge} \\ \text{at drain} \rightarrow 0 \end{array} \right.$

At this point, i_{DS} has reached its maximum!



Plug in $N_{GS} - V_{TN} - V_{DS} = 0 \rightarrow V_{DS} = N_{GS} - V_{TN}$ in the i_{DS} equation:

$$i_{DS} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (N_{GS} - V_{TN})^2 \text{ for } V_{DS} = N_{GS} - V_{TN}$$

(onset of saturation)