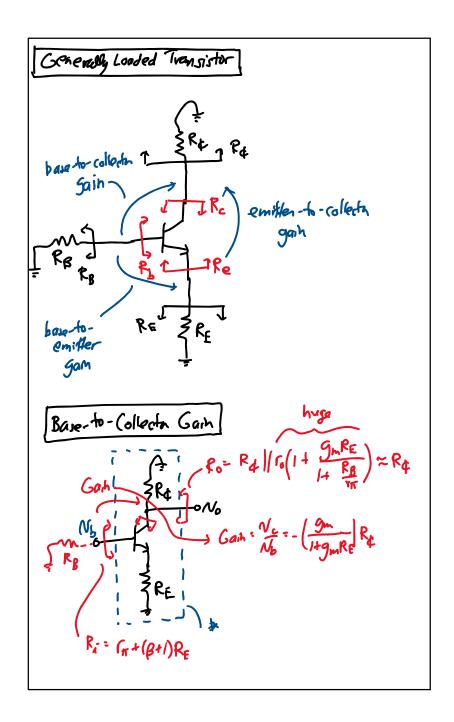
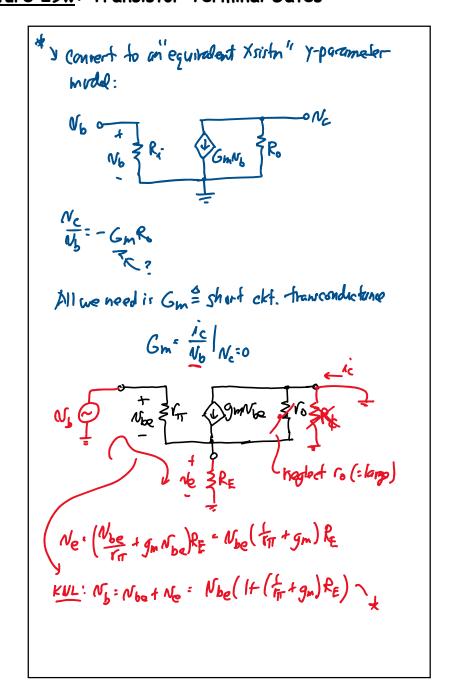
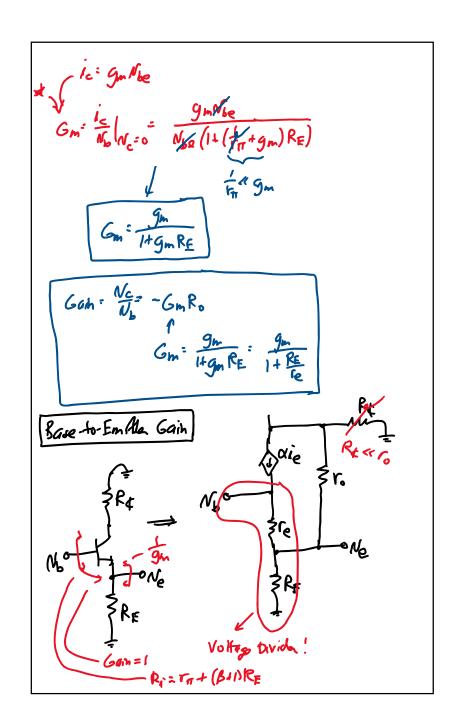
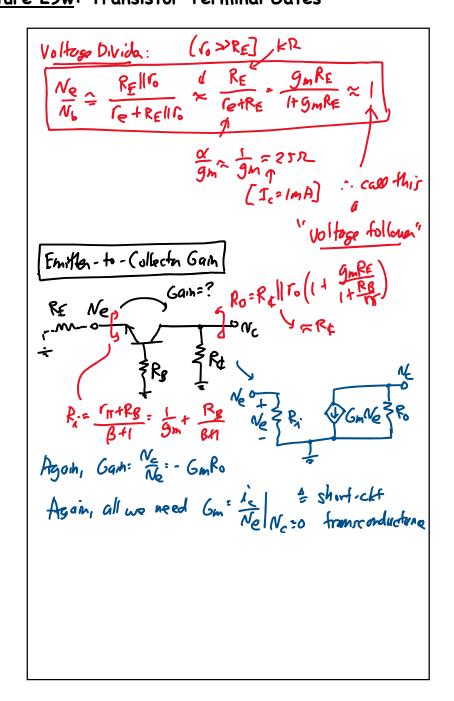
Lecture 29: Transistor Terminal Gains

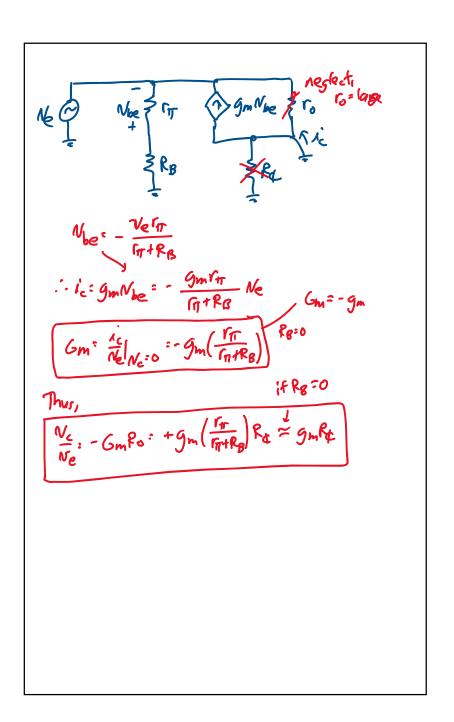
- · Announcements:
- · HW#9 online and due Friday via Gradescope
- · Lab#5 due Tuesday, Nov. 12, 5 p.m.
 - ♦ If you are using excel or matlab to compute equations for Lab#5 → you will regret doing this when taking midterm 2
 - ♦ You need to design by hand at least 3 times before going to a computer
 - This is the only way to get familiar with the process
 - You cannot see the trade-offs without analyzing by hand
 - ♦ You need to put the work in to be able to recognize things
- · Midterm 2 coming up in about 2 weeks
 - \$Friday next week, Nov. 15, @ 7 p.m., in 160 Kroeber Hall
 - Review Session will be next Tuesday, 6-8 p.m., in 400 Cory
- -----
- · Lecture Topics:
 - Senerally-Loaded Transistor
 - -Terminal Resistances
 - -Terminal-to-Terminal Gains
 - -Inspection Analysis Sheet
 - -Examples
- -----
- · Last Time:
- In the midst of getting terminal-to-terminal gains
- · Now, continue with this ...











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