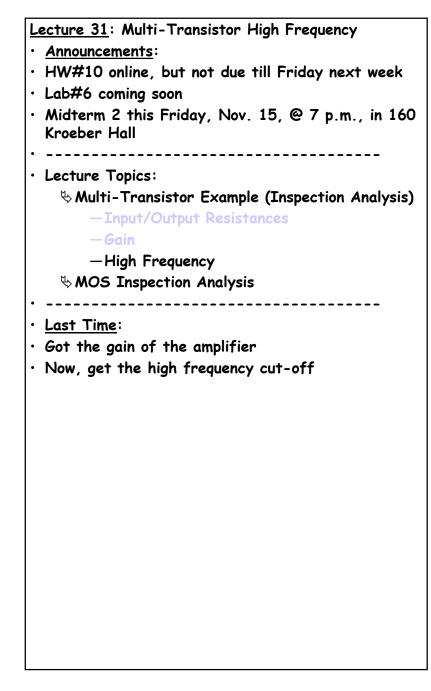
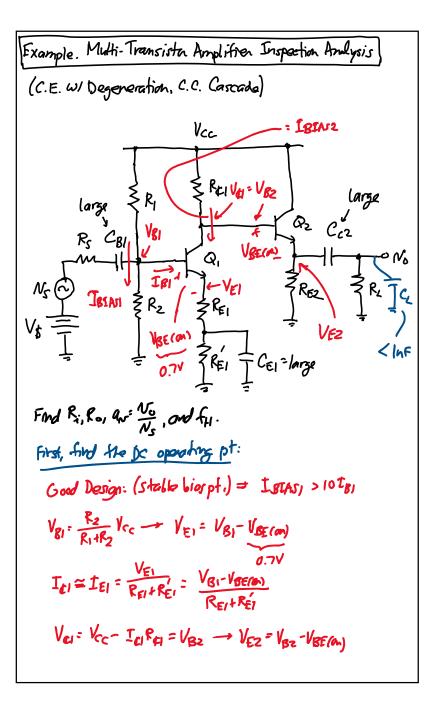
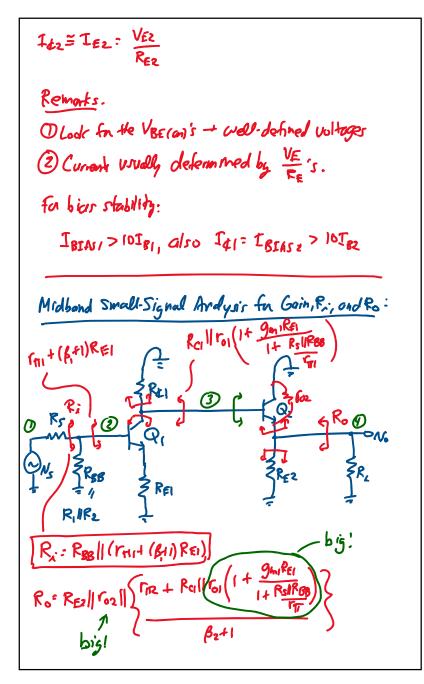
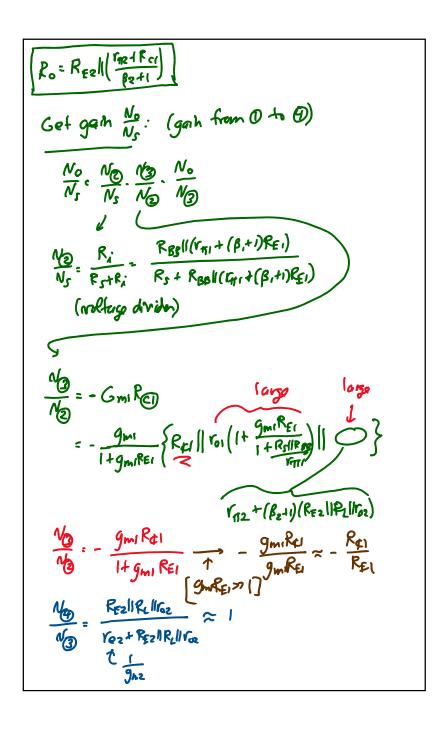
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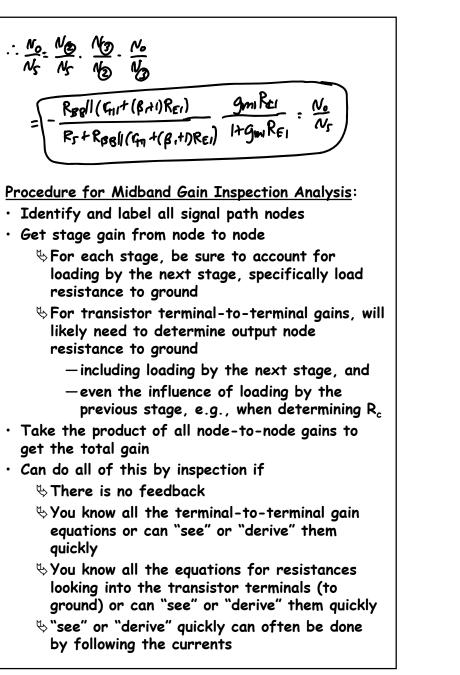






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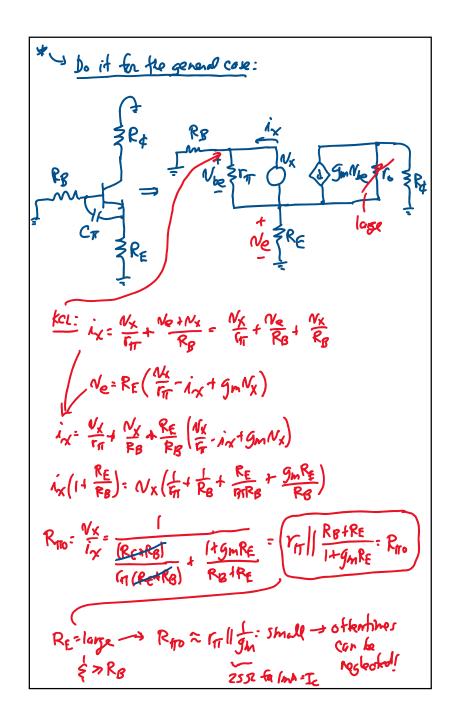


Procedure for High Frequency Inspection Analysis:

- Identify and label all signal path nodes
- Draw in the small transistor capacitors
- Use the Miller transform to turn the base-tocollector or gate-to-drain capacitor into shunt capacitors to ground
- For the base-to-emitter or gate-to-source capacitor you will need to know the equation for driving point resistance, i.e., resistance in parallel
- Get the time constant for each node by
  - Determining the total capacitance  $C_{\rm node}$  from that node to ground

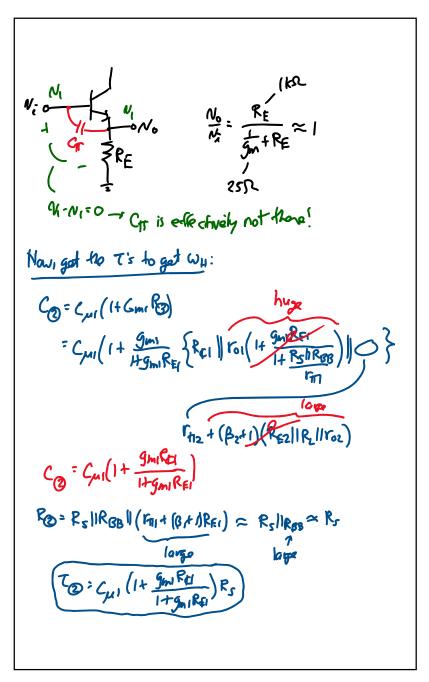
  - ♦ Time constant = R<sub>node</sub> \* C<sub>node</sub>
- Handle each feedback capacitor separately using knowledge of its driving point R equation (or derive the equation from scratch using the hybrid- $\pi$  model
- Add up all the time constants and take the reciprocal to get the  $\omega_{\rm H}$

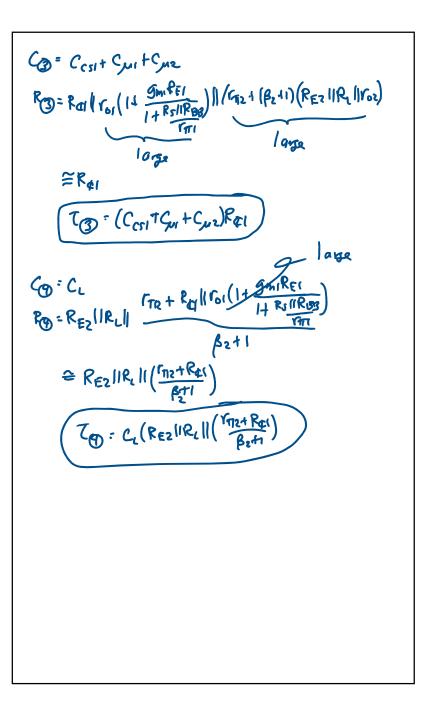
# High Freq Analysis shorted to god! Cyz Сш QITCO ر۴ ₹r<sub>bb</sub> No 3 Rs QTCott $\bigcirc$ ZRBB Cm C<sub>M1</sub> = C<sub>µ1</sub>(1-a<sub>N1</sub>) = C<sub>µ1</sub>(H-G<sub>m1</sub>R<sub>3</sub>) C<sub>11mn</sub> Using OCTC Analysis: WH= CORO + CORO + CMRVIII + CARRIE A A total shunt R total shunt total shunt R C @ node @ @ node@ G need to determine Par's



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